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V V0002  
V V0003  
V V0004  
V V0005  
V V0379  
V V0544  
V V0601  
C C0001  
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C C0005  
C C0340  
D D0001  
D D0002  
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D D0283  
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d V1\_APPC  
d V1\_CHAP1  
d V1\_CHAP4  
d V1\_CHAP5  
d V1\_TOC  
d V1\_MODEM  
M M0001  
M M0002  
M M0003  
M M0004  
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M M0172  
M M0349  
M M0630  
M M1985  
m REF\_1  
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m REF\_B

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m REF\_C  
m REF\_D  
m REF\_E  
m REF\_F  
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N N0001  
N N0002  
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N N0004  
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N N0498  
N N0659  
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DESC,C,70

MATROX ELECTRONIC SYSTEMS LTD.: RANDOMLY SELECTED DEMONSTRATION DOCUME  
DIAMOND COMPUTER SYSTEMS, INC.: RANDOMLY SELECTED DEMONSTRATION DOCUME  
COLORGRAPHIC COMMUNICATIONS: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
UNIDENTIFIED: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
CREATIVE LABS, INC.: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
AMERICAN MEGATRENDS, INC.: FAST VIEW EISA  
U.S.ROBOTICS, INC.: SPORTSTER 14,400/PC  
DIAMOND MULTIMEDIA SYSTEMS, INC.: STEALTH 64 VLB  
ADAPTEC: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
EVEREX: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
DISTRIBUTED PROCESSING TECHNOLOGY (DPT): RANDOMLY SELECTED DEMONSTRATI  
ALPHA DESIGN: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
MYLEX CORPORATION: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
BUSLOGIC, INC.: BT-910A  
FUJITSU AMERICA: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
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SEAGATE TECHNOLOGY: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
MAXTOR: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
MINISCRIBE: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
SEAGATE TECHNOLOGY: ST1980N, ST11200N, ST11700N, ST11900N,  
SEAGATE TECHNOLOGY: ST41600N, ST41601N, ST42000N  
MICROPOLIS: 2105-8, 2108-10, 2112-15, 2205-5, 2207-6, 2210-9, 2217-15  
SEAGATE TECHNOLOGY: ST11950N, ST11950ND, ST12550N, ST12550ND  
SEAGATE TECHNOLOGY, INC.: ST31250W, ST32550W

Appendix A: GLOSSARY

Appendix B: IBM BIOS ERROR CODES

Appendix C: RECOMMENDED READING

Chapter 1: INTERFACE TYPES & INSTALLATION

Chapter 4: BIOS DRIVE TYPE TABLES

Chapter 5: SOLUTIONS TO COMMON PROBLEMS

The Micro House Encyclopedia of Hard Drives: Volume One Editor-in-Ch

Chapter 1: Modem Communications

DELL COMPUTER CORPORATION: RANDOMLY SELECTED DEMONSTRATION DOCUMENT

UNIDENTIFIED: RANDOMLY SELECTED DEMONSTRATION DOCUMENT

IBM CORPORATION: RANDOMLY SELECTED DEMONSTRATION DOCUMENT

MICRONICS COMPUTERS, INC.: RANDOMLY SELECTED DEMONSTRATION DOCUMENT

DEICO ELECTRONICS, INC.: RANDOMLY SELECTED DEMONSTRATION DOCUMENT

HAWK COMPUTERS: 486 CACHE VER. 1

GIGA-BYTE TECHNOLOGY, INC.: GA-486US

MICRONICS COMPUTERS, INCORPORATED: 80486 ASIC EISA

ASUS AMERICA, INC.: PCI/E-P54NP4 (VERSION 2.0)

SYMBOL 110 \f "ZapfDingbats" Introduction:

SYMBOL 110 \f "ZapfDingbats" Chapter 2: The IntelXE "INTEL" CP

SYMBOL 110 \f "ZapfDingbats" Chapter 3: The IntelXE "INTEL" NP

SYMBOL 110 \f "ZapfDingbats" Chapter 4: The ChipsetsXE "CHIPSET

SYMBOL 110 \f "ZapfDingbats" Chapter 5: CMOSXE "CMOS" and Exten

SYMBOL 110 \f "ZapfDingbats" Chapter 6: System MemoryXE "MEMOR

symbol 110 \f "ZapfDingbats" Chapter 7: Busxe "BUS" Architect

SYMBOL 110 \f "ZapfDingbats" Chapter 8: Miscellaneous ComponentsXE

SYMBOL 110 \f "ZapfDingbats" Chapter 9: The System BIOS

SYMBOL 110 \f "ZapfDingbats" Appendix A: Bibliography

SYMBOL 110 \f "ZapfDingbats" Appendix B: PinXE "PIN"-Out Referen

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SYMBOL 110 \f "ZapfDingbats" Appendix C: Component Identifi  
symbol 110 \f "ZapfDingbats" Appendix D: Glossary Of Main Board  
SYMBOL 110 \f "ZapfDingbats" Appendix E: Acronyms and Abbreviatio  
SYMBOL 110 \f "ZapfDingbats" Appendix F: MemoryXE "MEMORY" Spe  
SYMBOL 110 \f "ZapfDingbats" Appendix G: Conversion Tables  
THOMAS-CONRAD CORPORATION: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
DANPEX CORPORATION (ADI SYSTEMS, INC.): RANDOMLY SELECTED DEMONSTRATIO  
INTEL CORPORATION: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
GATEWAY COMMUNICATIONS, INC.: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
NOVELL, INC.: RANDOMLY SELECTED DEMONSTRATION DOCUMENT  
ACTINET SYSTEMS, INC.: ARCTalk 030  
EAGLE TECHNOLOGY: NE2000T  
The Network: Interface Card Technical Guid  
Chapter 1A: Topology  
Chapter 2: LAN Access Protocols  
Chapter 3: Cable Types  
Chapter 4: Directory of Manufacturers  
Chapter 5: Jumper Settings  
Appendix A: Glossary of Terms  
Appendix B: Node ID Quick Reference  
Appendix C: Hardware Device Settings  
Appendix D: Common Cable Pin Assignments  
Appendix E: Bibliography  
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