BIOS: Basic **I**nput/**O**utput **S**ystem - A set of programs encoded in Read Only Memory (ROM) to facilitate the transfer of data and control instructions between the computer and peripherals.

CMOS: Complementary **M**etal-**O**xide **S**emiconductor - An energy-saving chip fabricated to duplicate the functions of other chips, such as memory chips or microprocessors. The system settings for most PCs are stored in the CMOS.

DLL: the extension used by **D**ynamic **L**ink **L**ibrary files, which contain program code.

Glossary

It is recommended that the help file be read from the Readme.hlp.

B
    BIOS
C
    CMOS
D
    DLL
I
    ISP
J
    JRE
L
    LAN
P
    PCI
Q
    QEMM
R
    RAM
U
    URL
V
    VESA
    VLB

ISP: Internet **S**ervice **P**rovider

JRE: Java™ **R**untime **E**nvironment

LAN: Local **A**rea **N**etwork

PCI: Peripheral **C**omponent **I**nterconnect local bus - This architecture is the successor to VLB.

QEMM: Quarterdeck **E**xpanded **M**emory **M**anager from Quarterdeck Corp.

RAM: **R**andom **A**ccess **M**emory

URL: Uniform **R**esource **L**ocator - An addressing scheme used to link resources on the World Wide Web. Using URLs, a web browser can point to most resources on the net.

VESA: Video **E**lectronics **S**tandards **A**ssociation.

VLB: VESA **L**ocal **B**us - A high-speed bus for graphics cards that provides better video performance on high-end machines.