

## **miroVIDEO DC1 tv Hardware Settings**

Sets the I/O Address Range, Video Memory Range, Interrupt, DMA Channel.

You can also activate/deactivate 0 Wait State.

For further information, click on the desired item using the mouse.

**DMA Burstmode**

**0-Wait State**



## **DMA Burstmode**

With DMA Burstmode switched on, during each DMA request a packet of data is sent. This method is significant faster than single mode.

Unfortunately there are a few motherboards with incorrectly implemented DMA controller, which is not capable of doing bursts. If you notice data destruction during capture or playback switch off DMA Burstmode.

Be aware that switching off DMA Burstmode will decrease the maximum achievable data rate.

## **0 Wait State**

The access to the miroVIDEO DC1 tv memory can be carried out with zero or with one waitstate. With zero waitstates the data transfer is accelerated via the ISA bus. In case of problems especially with old computer models, deactivate this check box.

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