APPENDIX P RS232 INFO AND OUTP EXAMPLE

RS232 MEMORY MAP FOR MYBASIC ONLY
C000 - CFFE DSR ROM
D000 - DFFE PARALLEL I/O

RS232 CARD OUTPUT/INPUT BIT DEFINITION
MYBASIC ONLY SUPPORTS CRU ADDRESS >1300(PORT/1) AND >1500(PORT/2) FOR INP AND OUTP.

ADDRESS BUS BIT LAYOUT (Only A3 thru A14 are used)

A0 A1 A2	A3 A4 A5 A6 A7	A8 A9 A10 A11 A12 A13 A14	A15
NOT USED	BASE ADDRESS	CRU ADDRESS	

RS232 CARD CRU OUTPUT BIT DEFINITION

ADDR	BIT	DEFINITION
1300	0	DSR ROM page enable, 1=enable
1302	1	Parallel Port mode set, 1=input mode
1304	2	Parallel Port Strobe bit
1306	3	Spare Parallel Port bit
1308	4	Flag 0
130A	5	Clear To Send, RS232 Port 0, 0=active
130C	6	Clear To Send, RS232 Port 1, 0=active
130E	7	Indicator LED control, 1=LED on

RS232 CARD CRU INPUT DEFINITION

ADDR	BIT	DEFINITION
1300	0	Spare
1302	1	Parallel Port configuration sense
1304	2	Parallel Port Acknowledge sense bit
1306	3	Spare Parallel Port Sense bit
1308	4	Flag 0
130A	5	Clear To Send, RS232 Port 0 sense
130C	6	Clear To Send, RS232 Port 1 sense
130E	7	LED state sense

9902 UART BASE ADDRESSES
UART 0=1340 UART 1=1380

TI RS232 CARD DEFINITIONS FOR ADDRESS C000 - C00E

ADDR	CONTENTS	EXPLANATION
C000	BYTE >AA	Identification
C001	BYTE 1	Version number
C002	BYTE 0	Number of programs
C003	BYTE 0	Reserved
C004	DATA >C010	Power up routine
C006	DATA 0	User program header
C008	DATA >C016	DSR header
C00A	DATA 0	Subroutine link header
C00C	DATA >C06C	Address of interupt link
C00E	DATA 0	Address of subroutine libraries