

## CHAPTER 3:

# TURING MACHINES

- \* FINITE STATE MACHINES
  - REGULAR LANGUAGES
- \* PUSHDOWN AUTOMATA
  - CONTEXT-FREE LANGUAGES
- \* TM'S: TURING MACHINES

A NEW MODEL OF COMPUTATION  
NOT MUCH MORE ELABORATE.

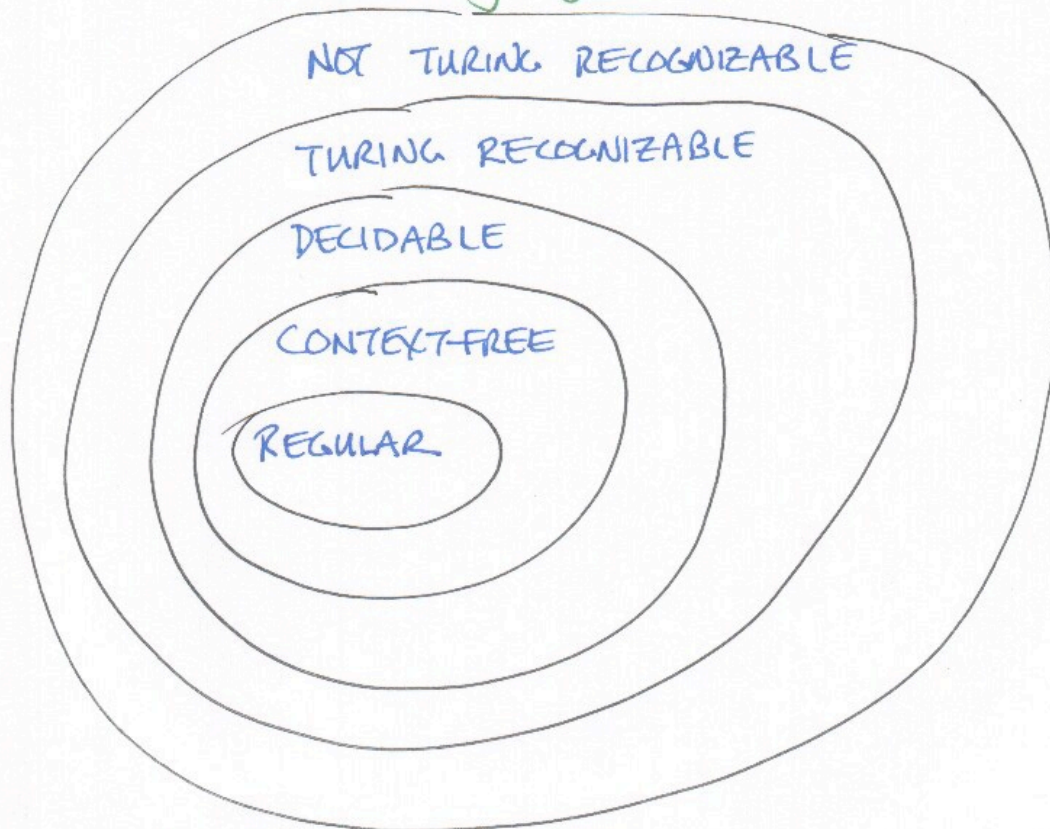
A "MODEL" FOR ALL COMPUTERS.

THREE NEW CLASSES  
OF LANGUAGE

- "DECIDABLE"
- "TURING RECOGNIZABLE"
- "NOT TURING RECOGNIZABLE"

# CLASSES OF LANGUAGES

(The Language Onion)



VENN DIAGRAM

The relationship is proper subset.

# TURING MACHINE DEFINITION

NOTE: THERE ARE VARIATIONS  
IN THE EXACT DEFINITION  
FROM TEXTBOOK TO TEXTBOOK.

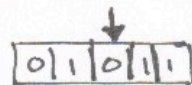
ALL VARIATIONS ~~ARE~~ ARE  
EQUIVALENT!

WE'LL DISCUSS THIS LATER.

## DATA STRUCTURE

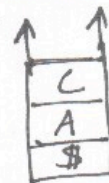
### FSM

- THE INPUT STRING



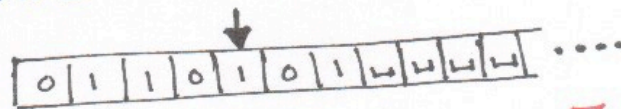
### PDA

- THE INPUT STRING
- A STACK



### TM

- A "TAPE"



SYMBOLS ~~ARE~~ FROM AN ALPHABET  $\Sigma$   
A SPECIAL BLANK SYMBOL  $\sqcup$   
INFINITE IN ONE DIRECTION  
- BUT FILLED WITH BLANKS.  
CURRENT POSITION.

## TAPE ALPHABET

TYPICAL:  $\Sigma = \{0, 1\}$

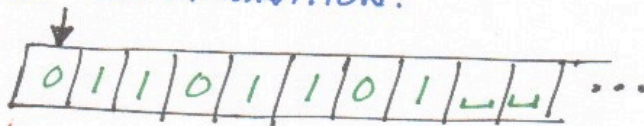
BUT ALSO COMMON:

$\Sigma = \{0, 1, a, b, x, \#, \$\}$

THE "BLANK" SYMBOL IS SPECIAL

$\sqcup \notin \Sigma$

INITIAL CONFIGURATION:



THE "INPUT" STRING

BLANKS OUT  
TO INFINITY.

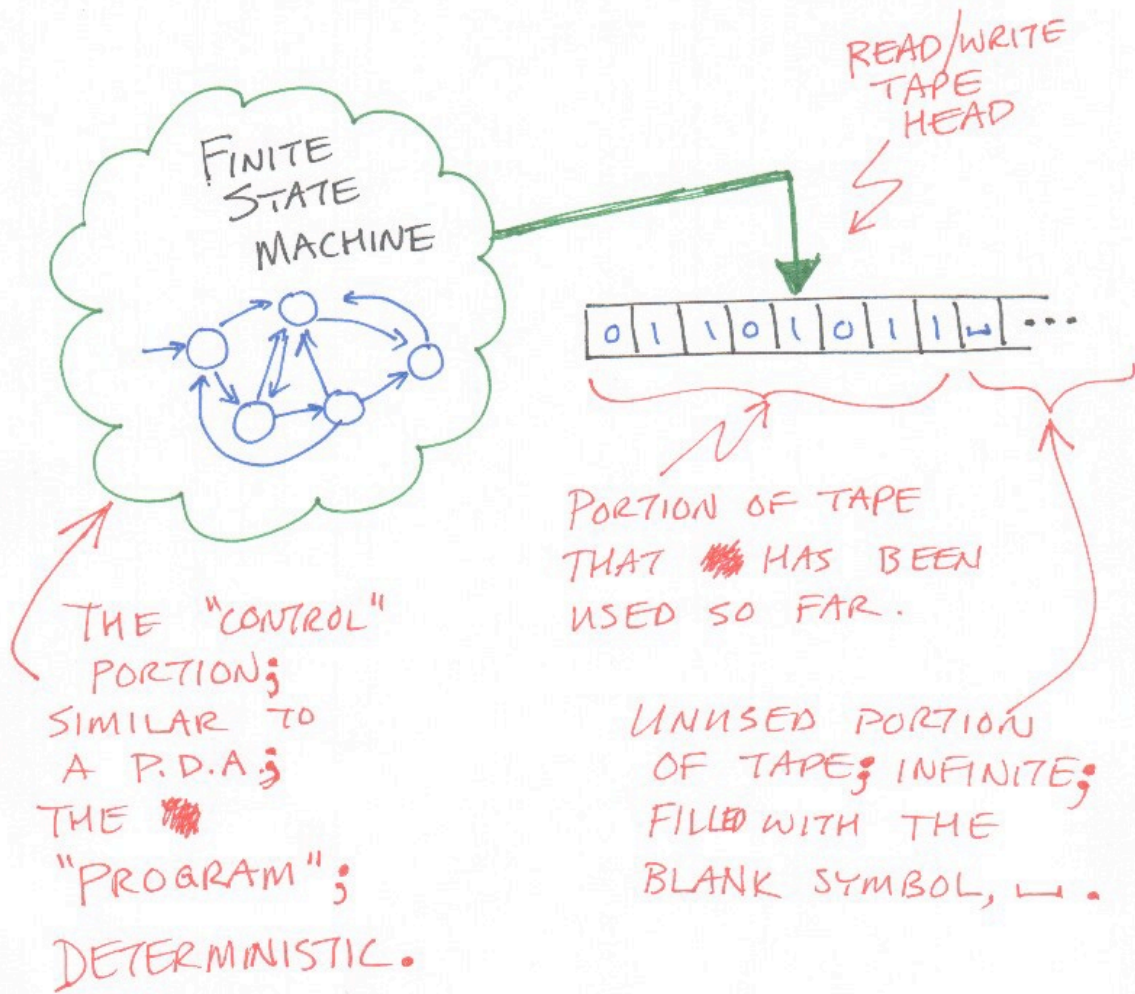
THE CURRENT POSITION  
("THE TAPE HEAD")

INITIALLY AT THE LEFTMOST CELL.

CAN MOVE LEFT OR RIGHT.

CAN READ ("SCAN") THE CURRENT  
SYMBOL

CAN WRITE THE CURRENT  
SYMBOL



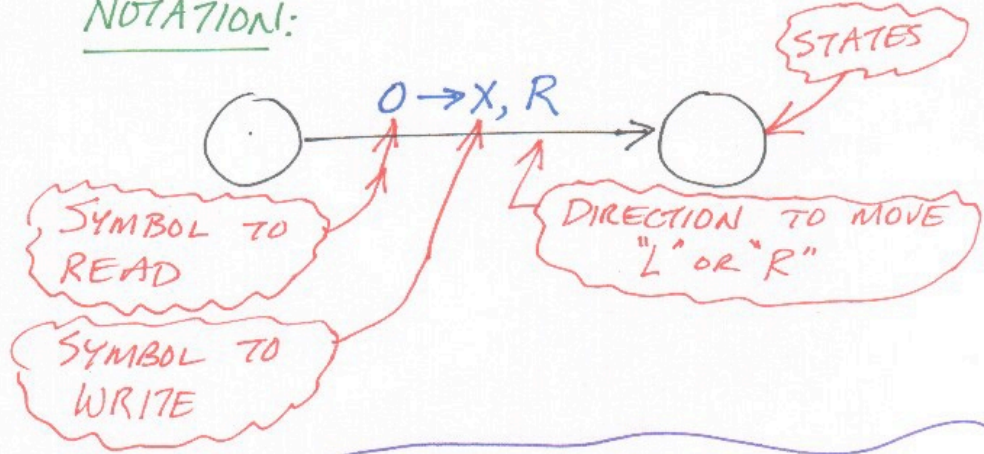
# RULES OF OPERATION

AT EACH STEP OF THE COMPUTATION:

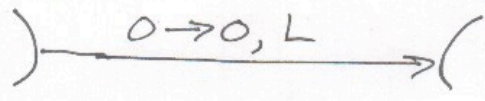
- READ THE CURRENT SYMBOL.
- UPDATE (i.e. WRITE) THE SAME CELL.
- MOVE EXACTLY ONE CELL EITHER LEFT OR RIGHT.

(IF AT LEFT END OF TAPE, WHEN MOVING LEFT, JUST STAY AT LEFT END.)

NOTATION:



DON'T WANT TO UPDATE THE CELL?  
JUST WRITE THE SAME SYMBOL.



## RULES OF OPERATION - 2

- CONTROL IS VIA A SORT OF FINITE STATE MACHINE.
- INITIAL STATE
- FINAL STATES:
  - THE "ACCEPT" STATE
  - THE "REJECT" STATE

- COMPUTATION ~~WILL~~ CAN:

HALT + "ACCEPT"

The MACHINE ACCEPTS immediately when ~~the~~ the ACCEPT STATE is reached.

HALT AND "REJECT"

The machine REJECTS immediately whenever the REJECT STATE is entered.

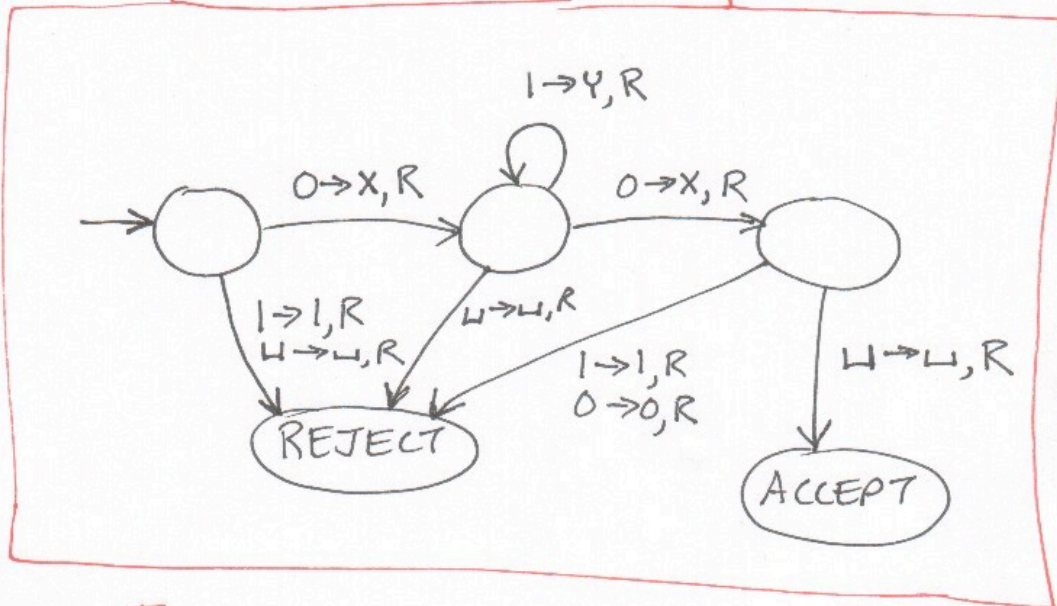
"LOOP"

The machine fails to halt.

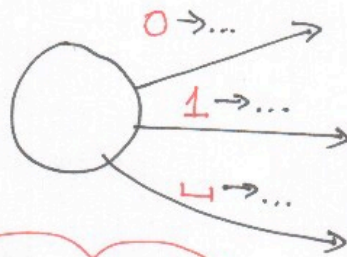
- THE T.M. IS DETERMINISTIC.

AN EXAMPLE

$$L = 01^*0$$



IS IT DETERMINISTIC?



HENCEFORTH...

IF AN EDGE IS MISSING...

ASSUME IT LEADS TO REJECT



DETAILS

$$(Q, \Sigma, \Gamma, \delta, q_0, q_{ACCEPT}, q_{REJECT})$$

$Q$  = Set of states.

$\Sigma$  = INPUT ALPHABET.

$\Gamma$  = TAPE ALPHABET

OFTEN WE NEED A FEW EXTRA SYMBOLS TO MAKE OUR COMPUTATION EASIER.

$$\Sigma \subseteq \Gamma$$

THE INPUT CANNOT CONTAIN A BLANK.  
 $\sqcup \notin \Sigma$  AND  $\sqcup \in \Gamma$

$q_0$  = Initial state  $q_0 \in Q$

$q_{ACCEPT} \in Q$   
 $q_{REJECT} \in Q$  } WE ONLY NEED ONE ACCEPT AND ONE REJECT STATE.

$$\delta: Q \times \Gamma \rightarrow Q \times \Gamma \times \{L, R\}$$

Transition function

### EXAMPLE

$$L = 0^N 1^N$$

INPUT  
ALPHABET:

$$\Sigma = \{0, 1\}$$

### ALGORITHM

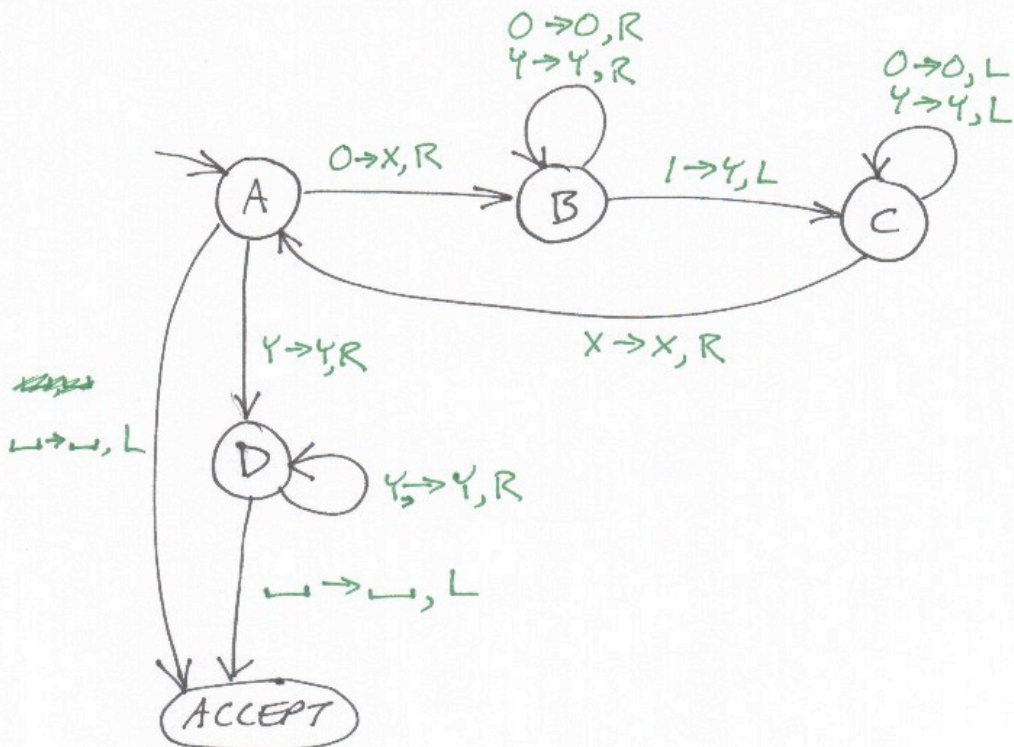
CHANGE "0" TO "X"  
MOVE RIGHT TO FIRST "1"  
IF NONE: REJECT.  
CHANGE "1" INTO "Y"  
MOVE LEFT TO LEFTMOST "0"  
REPEAT UNTIL NO MORE "0"s  
MAKE SURE NO MORE "1"s REMAIN

### A COMPUTATION HISTORY

|   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|
| 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 |
| X | 0 | 0 | 0 | 1 | 1 | 1 | 1 |
| X | 0 | 0 | 0 | Y | 1 | 1 | 1 |
| X | X | 0 | 0 | Y | 1 | 1 | 1 |
|   |   |   |   | ⋮ |   |   |   |
| X | X | X | X | Y | Y | Y | Y |

TAPE  
ALPHABET:

$$\Gamma = \{0, 1, X, Y, \_ \}$$



QUESTION:

Is this machine correct?  
 Does it work?  
 Does it contain bugs?

*TM's model computers.*

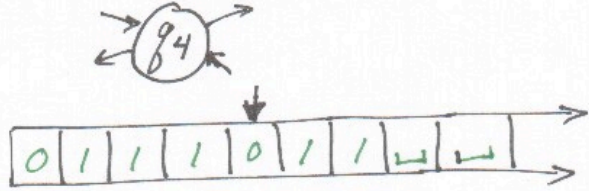
*In this way they are similar!*

**"CONFIGURATION"**

GIVES THE ENTIRE STATE OF THE MACHINE  
SNAPSHOT OF EXECUTION AT SOME STEP.

NEED:

- CONTENTS OF THE TAPE.
- LOCATION OF THE "TAPE HEAD"
- CURRENT STATE.



A CONFIGURATION IS A STRING LIKE THIS:



A SEQUENCE OF CONFIGURATIONS,  
STARTING WITH THE "START CONFIGURATION",  
AND ENDING WITH AN [ACCEPTING]\* CONFIGURATION,  
AND CONTAINING ONLY LEGAL TRANSITIONS  
PROVIDES A  
"COMPUTATION HISTORY"

\* OR "REJECTING"

## DECIDABLE LANGUAGES

When given a string as input, the TM will always halt.

The TM will ACCEPT if it is in L.

The TM will REJECT if it is not in L.

ALSO:

"RECURSIVE"

"COMPUTABLE"

"SOLVABLE"

## TURING RECOGNIZABLE LANGUAGES

When given a string that is in the language, the TM will always HALT and ACCEPT.

When given a string that is not in the language, the TM will either REJECT or LOOP.

ALSO:

"RECURSIVELY ENUMERABLE," RE

"PARTIALLY DECIDABLE"

"SEMI-DECIDABLE"

## NOT TURING RECOGNIZABLE LANGUAGES

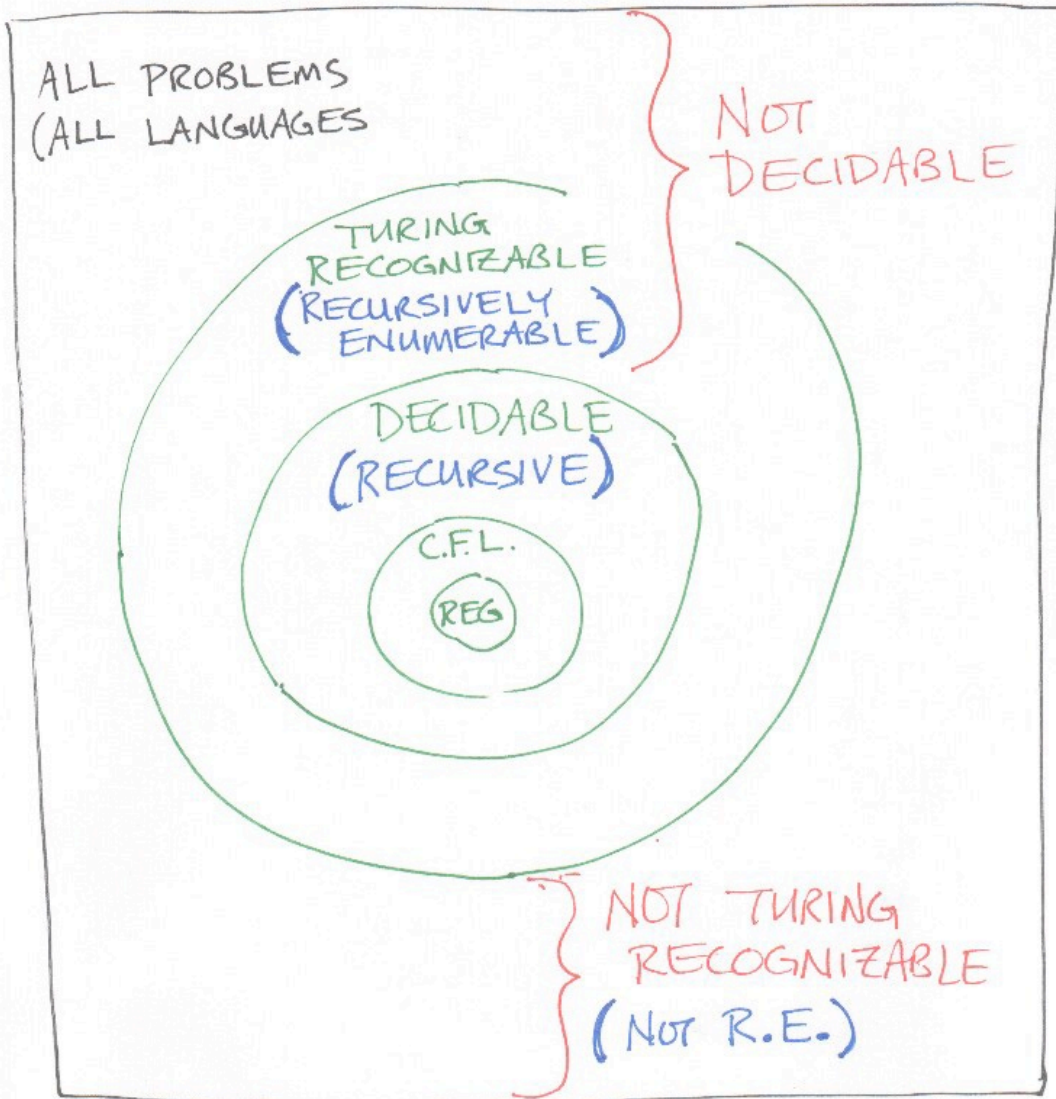
CAN'T EVEN RECOGNIZE MEMBERS RELIABLY!

ALSO:

NOT RECURSIVELY ENUMERABLE

NOT R.E.

NOT PARTIALLY DECIDABLE



A VENN  
DIAGRAM

## THE CHURCH-TURING THESIS

1930's: What does "COMPUTABLE" mean?

ALONZO CHURCH: LAMBDA CALCULUS

ALAN TURING: TMs.

Several variations on TURING MACHINES.

- ONE TAPE OR MANY?
- INFINITE ON BOTH ENDS?
- TINY ~~ALPHABET~~ ALPHABET  $\{0, 1\}$  OR NOT?
- CAN THE HEAD ALSO STAY IN THE SAME PLACE?
- ~~ALLOW~~ ALLOW NONDETERMINISM.

All variations ~~show~~ are equivalent in computing capability!

TM's AND LAMBDA CALCULUS are also equivalent in power.

CONCLUSION: (OR DEFINITION?)

"Algorithmically Computable"  
EQUALS  
"Computable by a TM."

WARNING

"COMPUTABLE" ← Avoid this term

PARTIALLY COMPUTABLE

≡ TURING RECOGNIZABLE

MAY BE UNDEFINED ON SOME INPUTS.

TOTALLY COMPUTABLE

≡ DECIDABLE

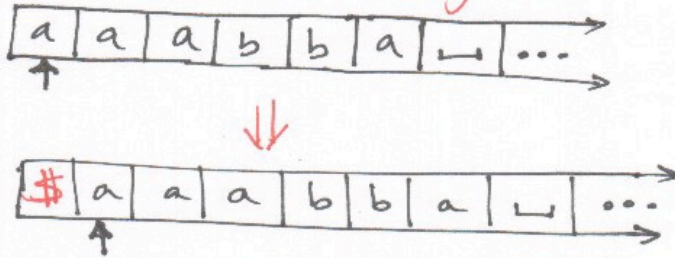
DEFINED ON ALL INPUTS.



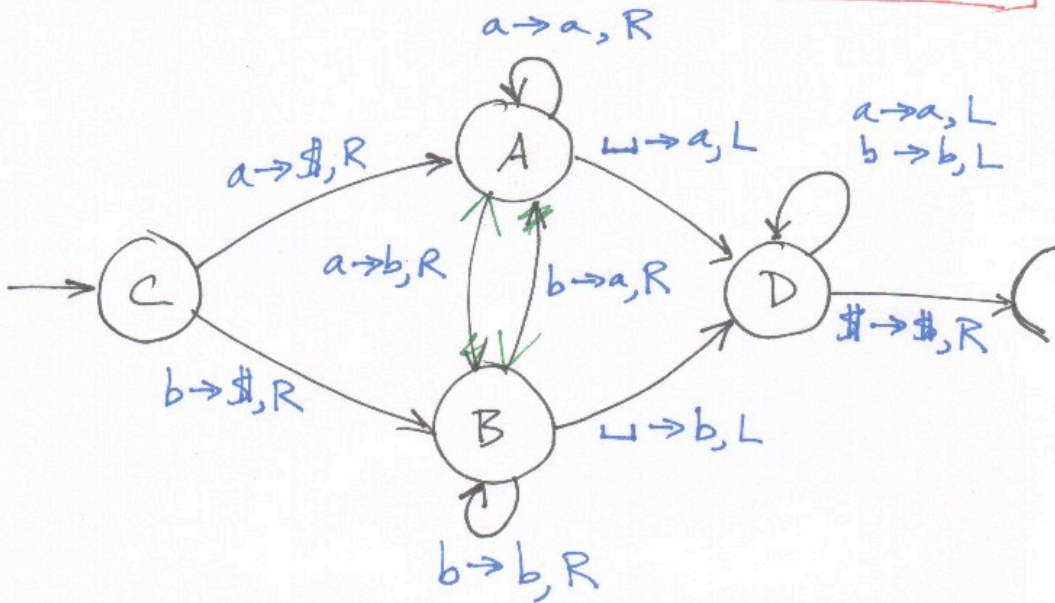
# PROBLEM

How can we recognize the left end of the tape?

GOAL: Want to put a special symbol  $\$$  on the left end and shift the input over 1 cell to the right.



Assume  $\Sigma = \{a, b\}$



Q: How much TM programming do you need to do?

A: Just enough to ~~see~~ get the idea and to convince yourself that all programs/algorithms can be implemented on a TM.

Machine Code  
~~Binary~~ 0110, 1100



Assembly Code  
ADD R1, R2, R3



C code  
 $i = (z+k) * n;$



Algorithms  
If  $S \cap T = \emptyset \dots$   
No implementation details.

TURING MACHINES  
STATES,  
TRANSITION FUNCTION  
(complete TM specification)



Outline of Algorithm  
Still talking about  
Tape Head movement,  
Data representation



High-level specification  
of algorithm  
No TM-specific  
details  
If  $S \cap T = \emptyset \dots$

## EXAMPLE

Build a TM to ~~recognize~~ <sup>decide</sup>  
the language  $0^N 1^N 0^N$ .

ON THE EXAM YOU SHOWED THIS  
WAS NOT CONTEXT FREE, SO THIS  
WILL SHOW THAT THE "ONION"  
IS PROPER SUBSET.

IDEA We already have a TM to  
turn  $0^N 1^N$  into  $x^N y^N$  and to  
decide that language.

Use it as a "subroutine"!

STEP 1

000000 111111 000000

↓

xxxxxx yyyyyy 000000

Reject if problems.

STEP 2 Build a similar TM  
to recognize  $y^N 0^N$

STEP 3 Build the final TM  
by "GLUING" these TMs together.  
into one large TM.

PROBLEM:

Compare two strings  $\{W \# W / W \in \{a, b, c\}^*\}$

Solution:

Use a new symbol, such as "x".  
Turn the symbols into a "x" after they have been examined.

a b a c # a b a c  
    ↓  
x a b a c # x a b a c

PROBLEM:

Do it non-destructively, without losing the strings.

[Perhaps this task is part of a larger task.]

Solution:

"**MARK**" each symbol to keep track of what we've already done.

Add some new symbols to help.

a → x      a b c a # a b c a  
b → y  
c → z      x x y z a # x x y z a

$\Gamma = \{a, \dot{a}, b, \dot{b}, c, \dot{c}\}$

Later, we can restore the strings if we need to.

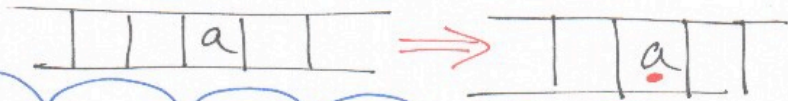
a b c b # a b c b  
↓  
a b c b # a b c b  
↓  
a b c b # a b c b

## MARKING

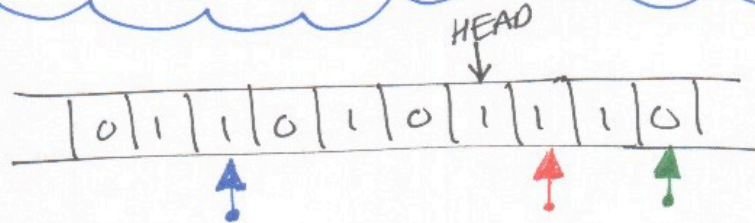
NOW WE CAN SAY

"MARK THIS SYMBOL"

OR "PUT A DOT UNDER THIS SYMBOL."



We could also have several types of marks, such as



## THEOREM

EVERY MULTITAPE TURING MACHINE HAS AN EQUIVALENT SINGLE-TAPE TURING MACHINE.

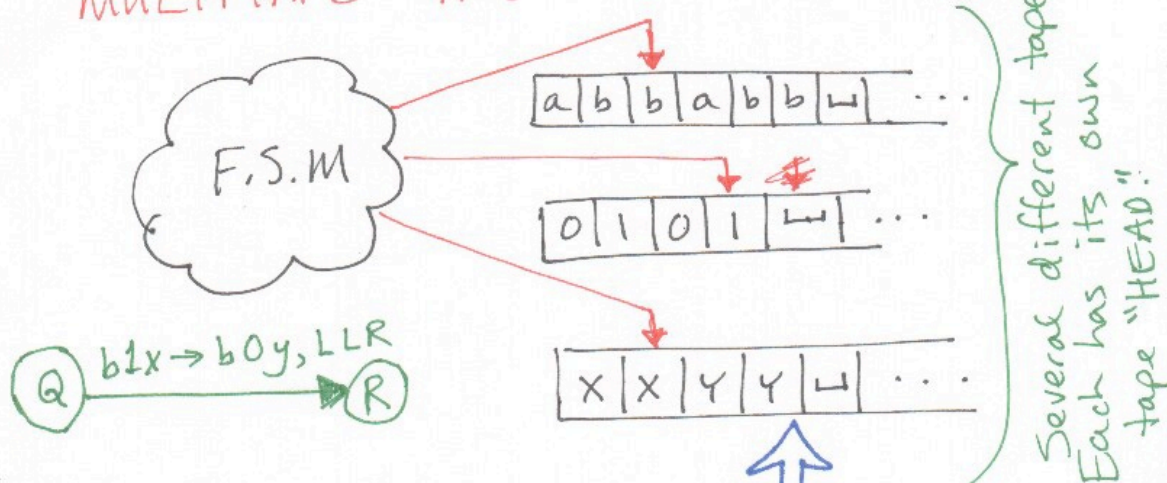
"EQUIVALENT" MEANS IT DECIDES / RECOGNIZES THE SAME LANGUAGES. IT'S NOT ABOUT SPEED, EFFICIENCY OR EASE OF PROGRAMMING.

## PROOF

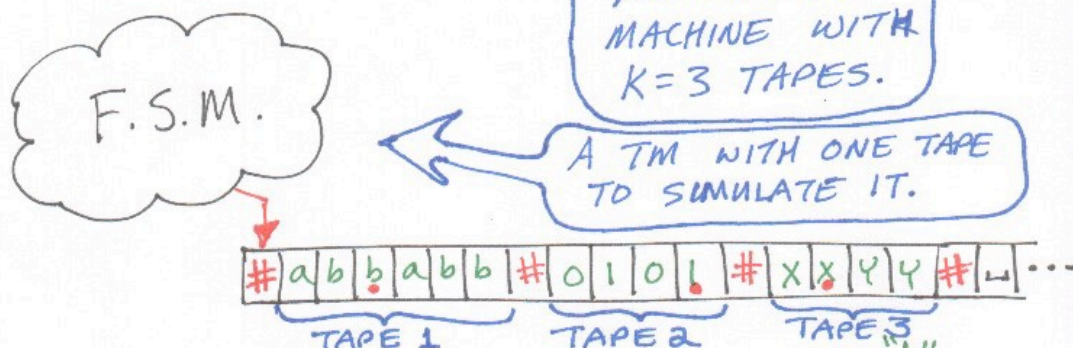
GIVEN A MULTITAPE TM, SHOW HOW TO BUILD A SINGLE-TAPE TM.

- NEED TO STORE ALL TAPES ON A SINGLE TAPE.  
⇒ SHOW DATA REPRESENTATION.
- EACH TAPE HAS A TAPE "HEAD".  
⇒ SHOW HOW TO STORE THAT INFO.
- NEED TO TRANSFORM <sup>A</sup> MOVE~~S~~ IN THE MULTITAPE TM INTO ONE OR~~E~~ MORE MOVES IN THE SINGLE-TAPE TM.

## MULTITAPE TM:



## SINGLE-TAPE TM:



- ADD "DOTS" TO SHOW WHERE HEAD "K" IS.
- To simulate a transition from state Q, we must scan our tape to see which symbols are "UNDER" the K tape heads.
- Once we determine this, and are ready to "MAKE" the transition, we must scan across the tape again to update the cells and move the dots.
- Whenever one head moves off the right end, we must shift our tape so we can insert a  $\perp$ . 23

## NONDETERMINISTIC TURING MACHINES

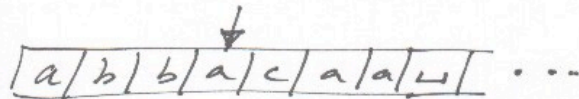
### TRANSITION FUNCTION

$$\delta: Q \times \Gamma \rightarrow \mathcal{P}(Q \times \Gamma \times \{L, R\})$$

A "CONFIGURATION" is...

A STRING WHICH CAPTURES THE ENTIRE STATE OF A T.M. AT ONE MOMENT DURING A COMPUTATION

- STATE
- TAPE CONTENTS
- HEAD POSITION

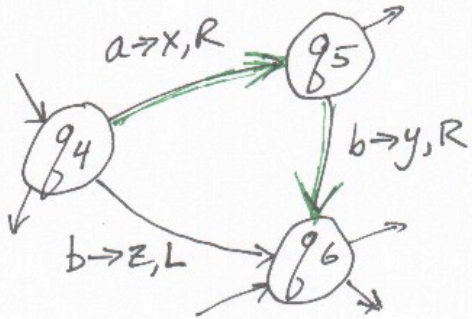


"abb<sup>q<sub>37</sub></sup>acaa"

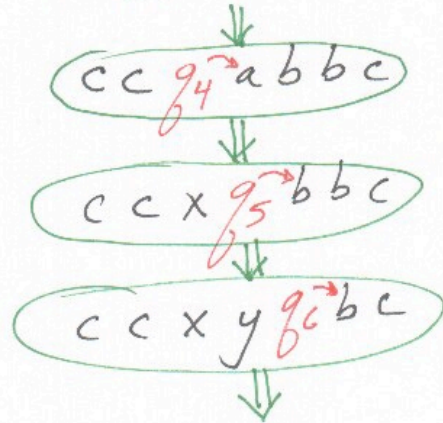
AT EACH MOMENT IN THE COMPUTATION THERE CAN BE MORE THAN ONE SUCCESSOR CONFIGURATIONS!



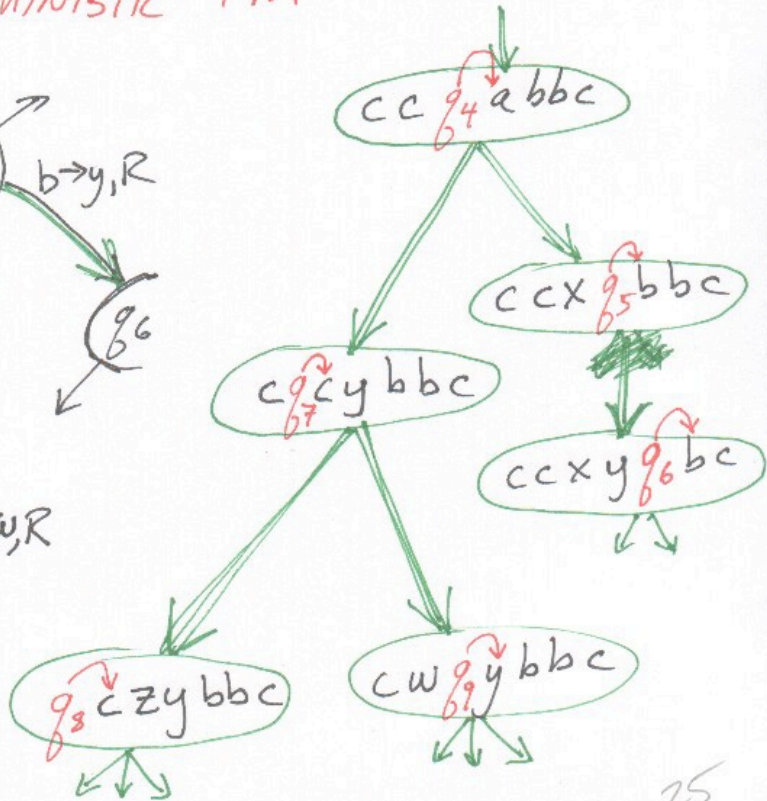
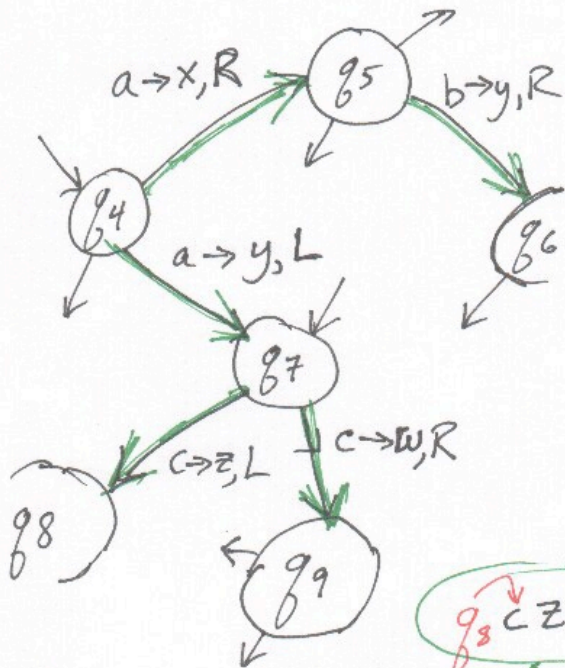
## DETERMINISTIC TM



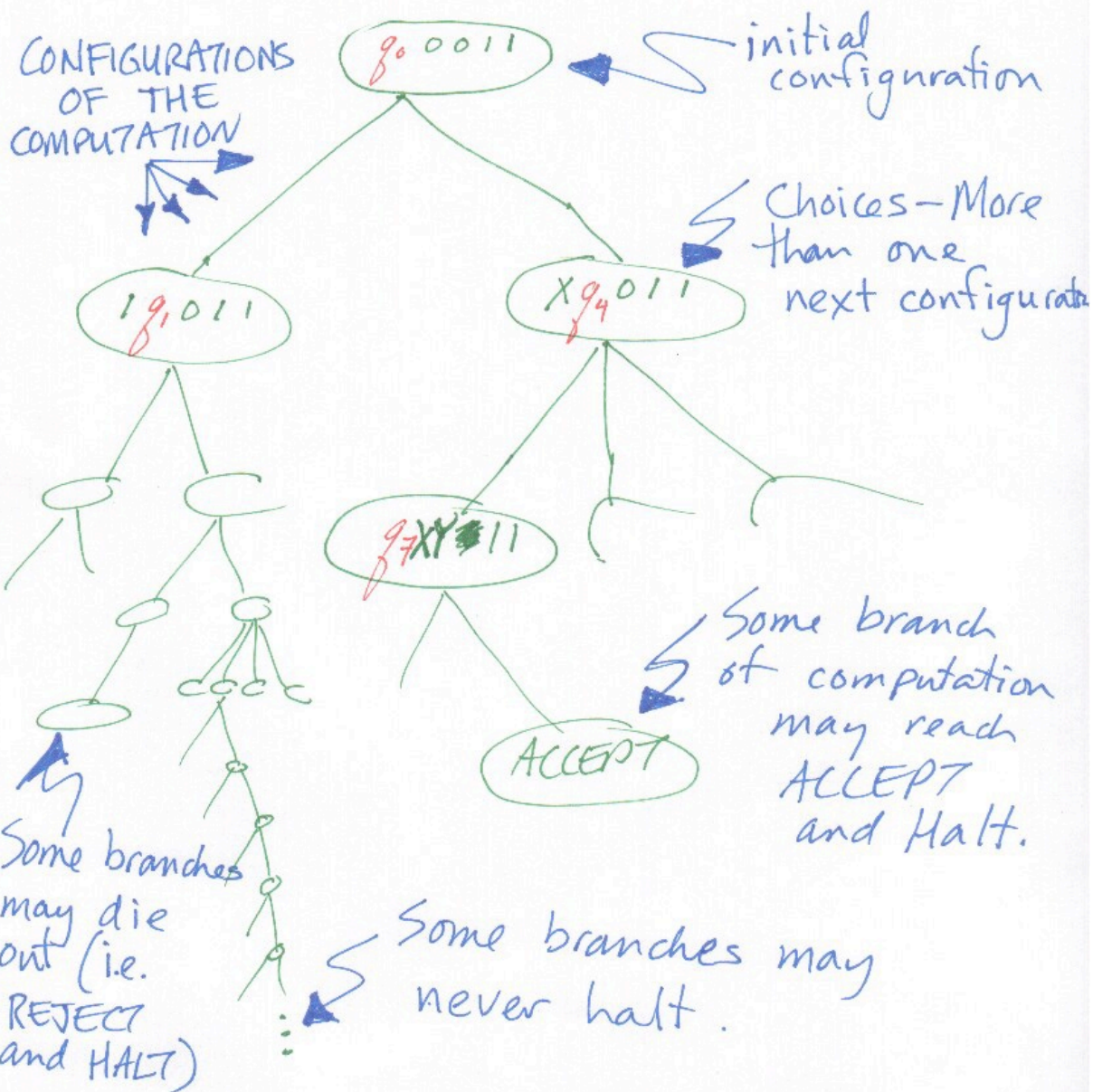
## COMPUTATION HISTORY



## NONDETERMINISTIC TM



A TREE SHOWS THE COMPUTATION OF A NON-DETERMINISTIC TM.



## OUTCOMES OF A NONDETERMINISTIC COMPUTATION:

### ACCEPT

IF ANY BRANCH OF THE COMPUTATION ACCEPTS, THEN THE NONDETERMINISTIC TM WILL ACCEPT.

### REJECT

IF ALL BRANCHES OF THE COMPUTATION HALT AND REJECT (i.e., NO BRANCHES ACCEPT, BUT ALL COMPUTATION HALTS), THEN THE NONDETERMINISTIC TM REJECTS.

### LOOP

COMPUTATION CONTINUES, BUT "ACCEPT" IS NEVER ENCOUNTERED.

SOME BRANCHES IN THE COMPUTATION HISTORY ARE INFINITE.

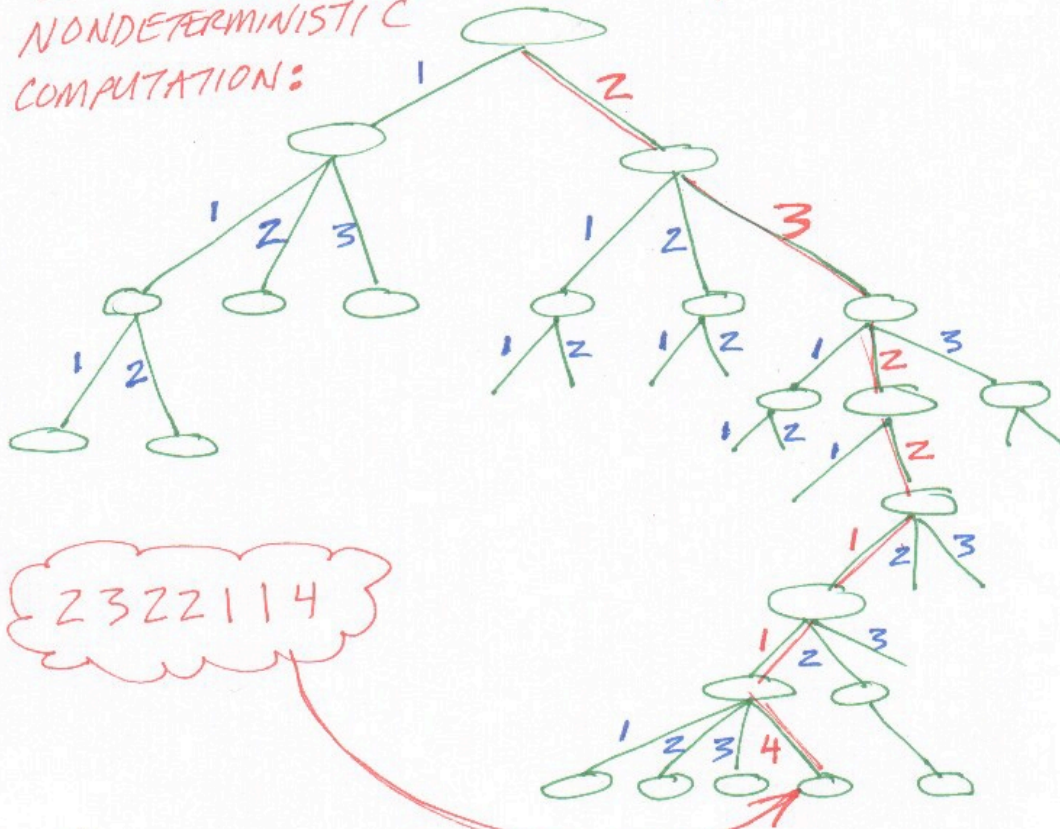
## THEOREM

EVERY NONDETERMINISTIC TM HAS  
AN EQUIVALENT DETERMINISTIC TM.

## PROOF

- GIVEN A NON-DETERMINISTIC TM, (N)  
SHOW HOW TO CONSTRUCT AN  
EQUIVALENT ~~TM~~  
DETERMINISTIC TM (D)
- IF N ACCEPTS (ON ANY BRANCH)  
THEN D WILL ACCEPT.
- IF N HALTS ON EVERY BRANCH  
WITHOUT ANY "ACCEPTS", THEN  
D WILL HALT AND REJECT.
- APPROACH: SIMULATE ~~N~~ N;  
SIMULATE ALL BRANCHES OF  
COMPUTATION; SEARCH FOR  
ANY WAY N CAN ACCEPT.

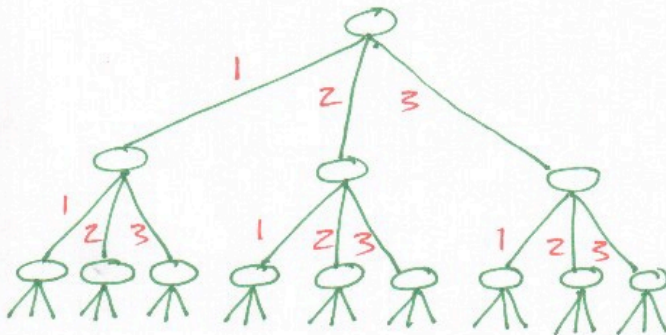
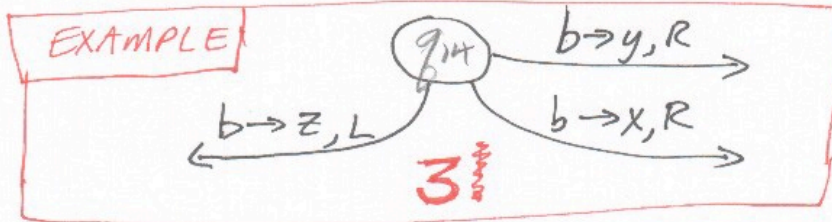
THE "COMPUTATION HISTORY" IS A TREE SHOWING ALL POSSIBLE ~~BRANCHES~~ BRANCHES/CHOICES IN A NONDETERMINISTIC COMPUTATION:



- A PATH TO ANY NODE IS GIVEN BY A NUMBER.
- SEARCH THE TREE, LOOKING FOR "ACCEPT."
- SEARCH ORDER?
  - ~~DEPTH-FIRST?~~ ← No!
  - BREADTH-FIRST! ← YES!
- TO EXAMINE A NODE:
  - PERFORM THE ENTIRE COMPUTATION FROM SCRATCH.
  - THE PATH NUMBERS TELL WHICH OF THE MANY NONDETERMINISTIC CHOICES TO MAKE. 29

HOW MANY CHOICES AT EACH STEP  
IN THE COMPUTATION?

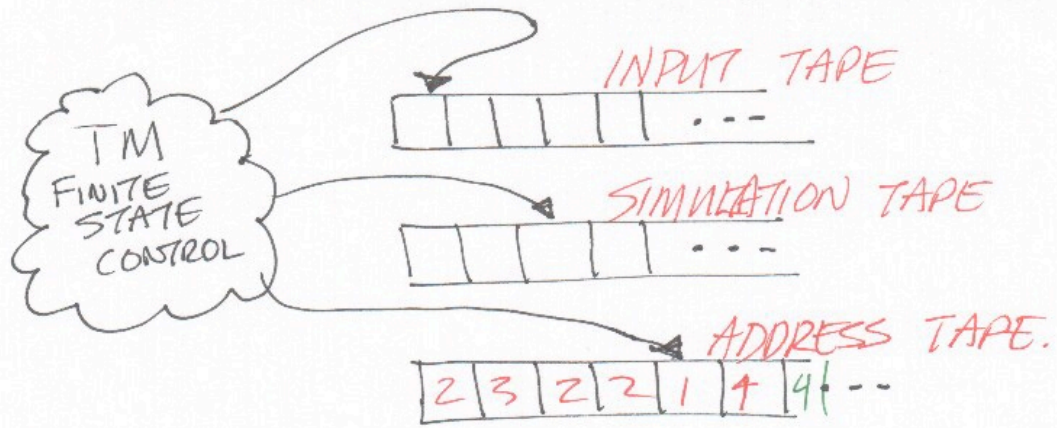
EXAMINE THE NONDETERMINISTIC MACHINE;  
THERE WILL BE SOME MAXIMUM. #.



BREADTH-FIRST  
SEARCH ORDER:

- ε
- 1
- 2
- 3
- 11
- 12
- 13
- 21
- 22
- 23
- 31
- 32
- 33
- 1 11
- 1 12
- 1 13
- 1 21
- 1 22
- 1 23
- ⋮

IN ANY PARTICULAR  
COMPUTATION THERE WILL BE  
FEWER THAN 3 CHOICES  
AT MOST OF THE COMPUTATION  
STEPS.  
SOME POINTS WILL HAVE ~~ZERO~~ ZERO  
CHOICES  $\Rightarrow$  THESE BRANCHES  
OF THE NONDETERMINISM  
HALT AND REJECT.



INPUT TAPE: INITIAL INPUT; NEVER MODIFIED.

SIMULATION TAPE: LIKE THE TAPE OF A DETERMINISTIC T.M.

ADDRESS TAPE: USED TO CONTROL THE BREADTH-FIRST SEARCH.

- TELLS WHICH CHOICES TO MAKE DURING A SIMULATION.

## ALGORITHM

INITIALLY: TAPE 1 CONTAINS THE INPUT.  
TAPES 2 & 3 ARE EMPTY.

↓  
COPY TAPE 1 TO TAPE 2.

RUN THE SIMULATION.

- USE TAPE 2 AS "THE TAPE."
- WHEN CHOICES OCCUR, CONSULT TAPE 3.
- TAPE 3 CONTAINS A "PATH".  
EACH NUMBER TELLS WHICH CHOICE TO MAKE.
- RUN THE SIMULATION ALL THE WAY DOWN THE BRANCH, AS FAR AS THE ADDRESS GOES.

TRY THE NEXT CHOICE.

INCREMENT TAPE 3

REPEAT

or the  
computation  
dies out.

2322111

2322112

2322113

2322114

2322121

IF ACCEPT IS EVER ENCOUNTERED,  
HALT AND ACCEPT.



A LANGUAGE IS "TURING RECOGNIZABLE"  
IFF SOME NON-DETERMINISTIC  
TURING MACHINE ~~ACCEPTS~~ RECOGNIZES  
IT.

HALTING?

IF A NON-DETERMINISTIC T.M. HALTS  
ON ALL BRANCHES WITHOUT ACCEPTING,  
THEN ~~IT~~ IT "REJECTS".

A LANGUAGE IS ~~RECOGNIZABLE~~ "DECIDABLE"  
IFF SOME NON-DETERMINISTIC  
TURING MACHINE DECIDES IT.

"DECIDES":

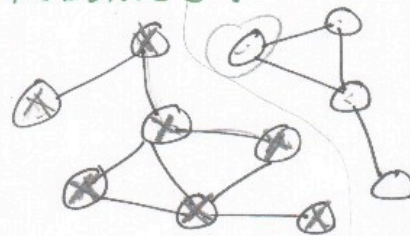
Always HALTS! Will always  
ACCEPT OR REJECT! Will  
never LOOP!

## EXAMPLE

CAN AN ARBITRARY PROBLEM BE  
EXPRESSED AS A LANGUAGE?

UNDIRECTED GRAPH

IS THE GRAPH  
"CONNECTED"?



WE MUST ENCODE THE PROBLEM.  
INTO A LANGUAGE:

$$A = \{ \langle G \rangle \mid G \text{ IS A CONNECTED GRAPH} \}$$

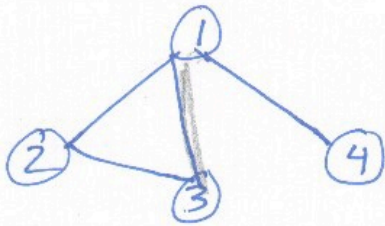
WE WANT:

A TM TO DECIDE THIS LANGUAGE.  
ACCEPT = "Yes, this is a connected graph"

REJECT = "No, Either it is not a valid  
representation of a graph OR  
the graph is UNCONNECTED."

LOOP? THIS PROBLEM IS DECIDABLE.  
THE T.M. WILL ALWAYS HALT.

ONE REASONABLE REPRESENTATION:



$\langle G \rangle =$

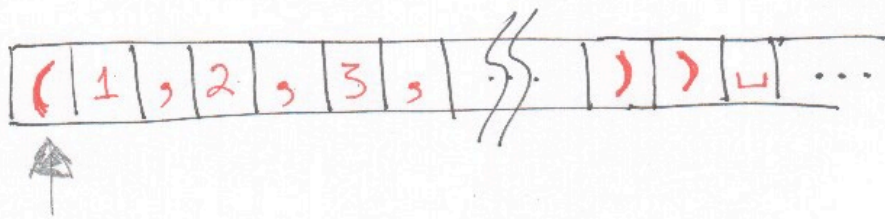
$(1, 2, 3, 4) \left( (1, 2), (2, 3), (1, 3), (1, 4) \right)$

List of node "names"

An edge from ① to ③

LIST OF EDGES

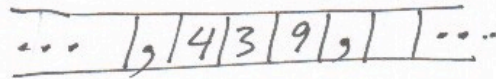
$\Sigma = \{ (, ), , 1, 2, 3, 4, \dots, 9 \}$



# REPRESENTING NUMBERS ON A TAPE

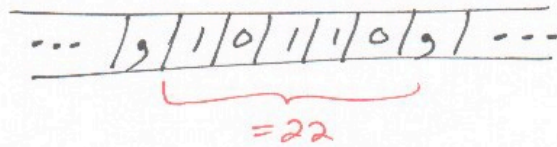
DECIMAL

$$\Sigma = \{0, 1, 2, \dots, 9, \dots, \#, \$, \times, \dots\}$$



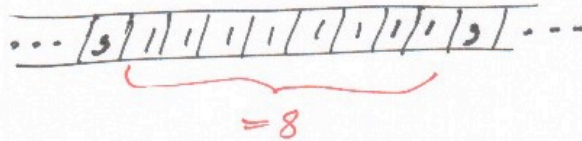
BINARY

$$\Sigma = \{0, 1, \dots\}$$



UNARY

$$\Sigma = \{1, \dots\}$$



(This is no more than a programming detail.)

# ALGORITHM = TURING MACHINE

- HIGH-LEVEL SPECIFICATION  
PSEUDO-CODE  
EXPRESSED IN PROGRAMMING LANGUAGE
- IMPLEMENTATION-LEVEL  
CONTENTS OF THE TAPE.  
DATA REPRESENTATION.  
MOTION OF THE TAPE HEAD.  
MORE DETAIL, BUT STILL ABSTRACT
- T.M. SPECIFICATION  
STATES  
ALPHABETS  
TRANSITION FUNCTION  
FULLY DETAILED (& INCOMPREHENSIBLE?)

Once we are comfortable with T.M. specification details, we'll start to give more abstract algorithms, with the understanding that we could build the exact T.M. whenever necessary.

## HIGH-LEVEL ALGORITHM

SELECT A NODE AND MARK IT.

REPEAT

FOR EACH NODE  $N \dots$

IF  $N$  IS UNMARKED AND  
THERE IS AN EDGE FROM  $N$   
TO AN ALREADY MARKED NODE  
THEN  
MARK NODE  $N$ .

END

UNTIL NO MORE NODES CAN BE MARKED

FOR EACH NODE  $N \dots$

IF  $N$  IS UNMARKED  
THEN "REJECT"

END

"ACCEPT"

## IMPLEMENTATION - LEVEL ALGORITHM

- CHECK THAT INPUT DESCRIBES A VALID GRAPH
  - CHECK NODE LIST
    - SCAN "C", FOLLOWED BY DIGIT,...
    - CHECK THAT ALL NODES ARE DIFFERENT, i.e., NO REPEATS.
  - CHECK EDGE LIST...
  - etc.
- MARK FIRST NODE.
  - PLACE A DOT UNDER THE FIRST NODE IN NODE LIST.
- SCAN THE NODE LIST TO FIND A NODE THAT IS NOT MARKED..  
etc., etc.

## ENUMERATORS

LIKE A TURING MACHINE:

INFINITE TAPE

FINITE STATE CONTROL

PLUS...

A PRINTER

### OPERATION

- THE TAPE IS INITIALLY EMPTY (i.e., NO INPUT).
- THE PRINTER PRODUCES A SERIES OF STRINGS.
- THE MACHINE ENUMERATES (i.e., IT "LISTS OUT" / "PRINTS") THE STRINGS IN A LANGUAGE.
- IT MAY HALT OR LOOP.
- THE LANGUAGE MAY BE INFINITE.
- IT MAY PRINT OUT DUPLICATES (JUST IGNORE DUPLICATE STRINGS).
- IT MAY PRINT IN ANY ORDER.



## THEOREM

A LANGUAGE IS TURING-RECOGNIZABLE  
IFF SOME ENUMERATOR  
ENUMERATES IT.

## PROOF

GIVEN "E" CONSTRUCT A TM "M".

ON INPUT  $w \dots$

RUN "E"

COMPARE EACH OUTPUT STRING TO  $w$ .

IF WE FIND A MATCH, ACCEPT.

GIVEN A TM "M", CONSTRUCT AN  
ENUMERATOR "E".

CONSTRUCT E, USING M AS A  
"SUBROUTINE".

in  $\Sigma^*$ .

RUN M ON ALL POSSIBLE STRINGS.  $\wedge$

IF M EVER ACCEPTS A STRING,  
THEN PRINT IT OUT.

PROBLEM?

M MIGHT LOOP ON SOME  
PARTICULAR STRINGS.

WE MUST RUN ALL THESE  
SIMULATIONS IN PARALLEL!

41

## GOAL

RUN A TM ON ALL STRINGS  
IN  $\Sigma^*$   
SIMULTANEOUSLY / IN PARALLEL.

## APPROACH

WE CAN LIST OUT ALL STRINGS  
IN  $\Sigma^* = \{s_1, s_2, s_3, s_4, \dots\}$

EG:  $\epsilon, 0, 1, 00, 01, 10, 11, 000, 001, \dots$

THE COMPUTATION ON ANY STRING  $s_i$   
MAY BE INFINITE.

WE MUST SOMEHOW INTERLEAVE  
THE DIFFERENT COMPUTATIONS.

WORK ON  $s_1$  A LITTLE,

THEN WORK ON  $s_2$  A LITTLE,

...

EVENTUALLY, MUST GO BACK  
AND DO MORE WORK ON  $s_1$

## ALGORITHM

FOR  $i = 1, 2, 3, \dots$  (INFINITE LOOP)  
    FOR  $j = 1$  TO  $i$   
        SIMULATE TURING MACHINE  $M$   
        USE  $S_j$  AS INPUT.  
        RUN SIMULATION FOR  $i$  STEPS  
        IF  $M$  ACCEPTS  $S_j$   
            (... WITHIN  $i$  STEPS...)  
        THEN PRINT  $S_j$   
    END  
END

EXAMPLE: ASSUME  $M$  ACCEPT  
 $S_1$  AFTER 3 STEPS  
 $S_3$  AFTER 4 STEPS

$i \rightarrow$

|       | 1 | 2 | 3 | 4 | 5 |
|-------|---|---|---|---|---|
| $S_1$ | ✓ | ✓ | ✓ | ✓ | ✓ |
| $S_2$ |   | ✓ | ✓ | ✓ | ✓ |
| $S_3$ |   |   | ✓ | ✓ | ✓ |
| $S_4$ |   |   |   | ✓ | ✓ |
| $S_5$ |   |   |   |   | ✓ |

OUTPUT:

$S_1$   
 $S_1$   
 $S_3$   
 $S_1$