

# *Student Design Work through PBL:*

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*Re-engaging Students with Creativity,  
Design & Professional Development*

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# *Background*

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## **First year Laboratory Programme**

Lab lectures ~1hr

6 hours per week

Experiment Rotation working pairs



# *Background*

**Professional Skills Programme**

**Mandatory component for IET accreditation**



Lectures

~1 hr

Workshops

~2 hrs

# *Motivation*

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**Professional Skills Programme**

**Mandatory component for IET accreditation**

Concerted effort required .... to fail the module.

# *Motivation*

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## **First year Laboratory Programme**

..... Teaching materials were very tired

..... allowed students to 'sleep walk' and still  
get good grades

..... Creativity / personal expression

# *Motivation*

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**Address as a single problem:**

**Enhance student learning with creativity and design**

**Laboratory Skills**

**Professional Skills**

# *Motivation*

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**Address as a single problem:**

**Enhance student learning with creativity and design**

**Laboratory Skills**

**Professional Skills**

# Planning

Week	Student Preparation		Professional Skills/Design session	Assessment		Lab Session	Assessment
2	E1						
3	E2		E1	Peer-based assessment			
4	Project		E2	Peer-based assessment		E1 - Transistor Amplifier	single mark
5			Project & Criteria Design	Peer-based assessment		E2 - Filter Design	single mark
6						Project	Feedback only
7				Project		Feedback only	
8					Project	Feedback only	
9						Project Showcase	Peer-based assessment



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# Implementation

Web pages replace lab lectures + lab manual

## Transistor Amplifier Tender

### Design Brief

TPC (The Transist...  
upcoming electron...  
requirements stipu...

### Base Emitter SI

A circuit to show...  
oscilloscope).

**Part A:** To de...

**Part B:** To de...

a voltage to de...

## Bipolar Junction Transistor Basics

Components like resistors  
yet do not produce any e...  
not "passive" are called "i...  
active nature, and given i...  
circuits. They are primari...  
transistors form the basic...  
with the most common to...  
come from the way they i...  
workings of a transistor. V

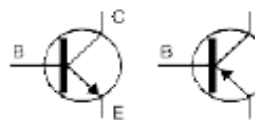


Figure 1 - Schematic sym...

The operation of a transist...  
control the current flow in...  
workings of the transistor...  
consider the analogy in fi...  
the base, the water can fl...  
analogous to the Base Cu...  
(analogous to the current

Quantity	Equation	Symbol
DC Current Gain	$I_C/I_B$	$h_{FE}$
AC Current Gain	$\Delta I_C/\Delta I_B$	$\beta$ or $h_{FE}$
AC Input Resistance	$\Delta V_{BE}/\Delta I_B$	$R_{in}$ or $h_{ie}$

Table 1 - BJT characteristics

### Packaging

When using transistors it is important to know which leg of the device is which. The easiest way to do this is to find the data sheet for the transistor you will be using. If you have difficulty finding the datasheet the labs will usually have copies of the datasheets for the transistors they have in stock or they will be able to tell you where to get them.



Part
Part

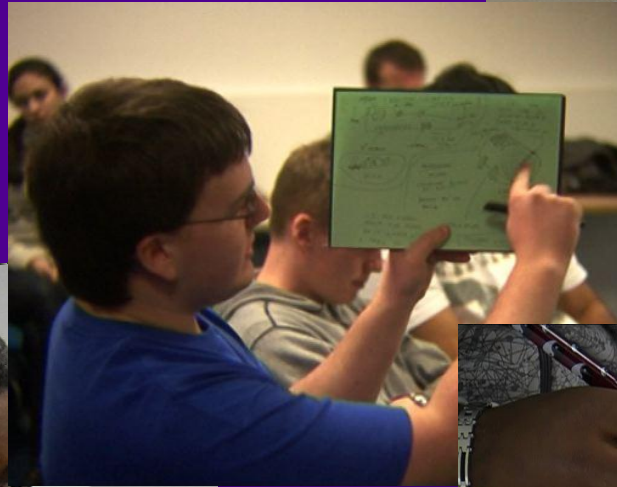
Table E1-1 - S

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# *Implementation*

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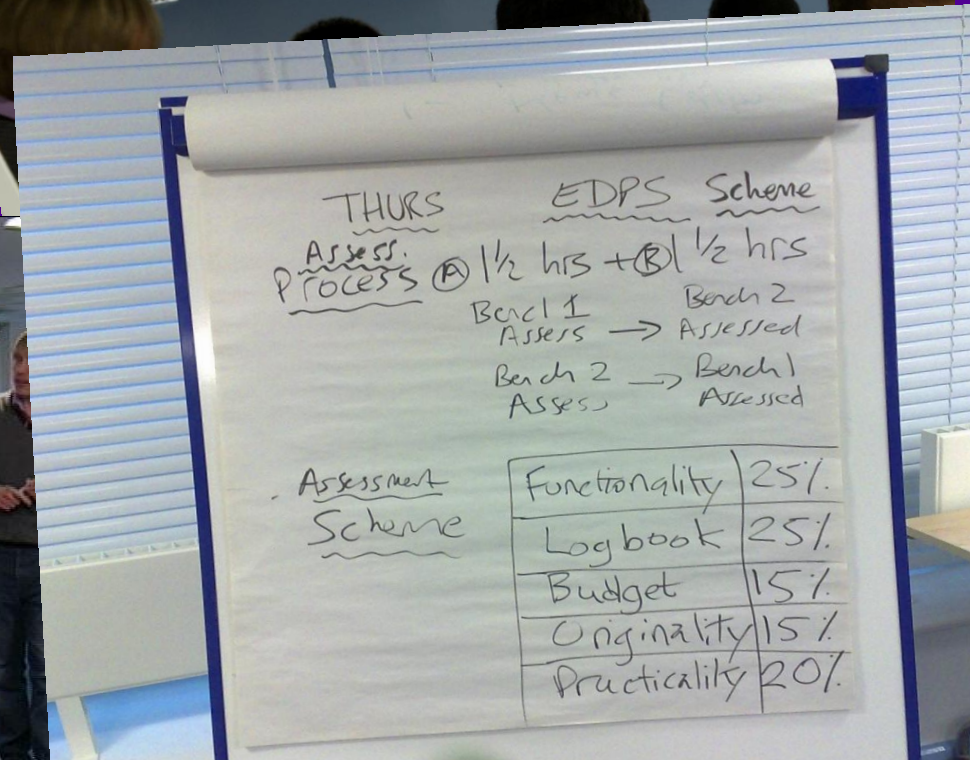
Design session & PA





# Implementation

Design session :  
Brain storming  
Mind mapping  
Peer Designed PA.



THURS EDPS Scheme  
Assess. Process (A) 1/2 hrs + (B) 1/2 hrs  
Bench 1 Asses → Bench 2 Assessed  
Bench 2 Asses → Bench 1 Assessed

Assessment Scheme

Functionality	25%
Logbook	25%
Budget	15%
Originality	15%
Practicality	20%

# Analysis

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## Student questionnaire

	pre-PBL	PBL	
Interest	<b>2.96</b>	<b>3.12</b>	
Presentation and delivery	<b>3.36</b>	<b>3.45</b>	
Effectiveness of learning	<b>3.07</b>	<b>3.42</b>	
	<b>Professional Skills</b>		

Table 1 Summary of mean responses to the three questions polled.

# Analysis

## Student questionnaire: Prof Skills

Comments from Professional Skills course Questionnaire	
Pre-PBL	PBL
examples being given now should aid learning	the new exercises were fun
emphasis on trivial points	good to mix with labs so feels like it has some use
obvious, done it before	EDPS/lab very unclear at times
silly subject, don't see the point	Boring but an effective learning experience
good very helpful to get understanding of work in the field	Doing lab prep within edps makes lab more interesting.
some aspects seem pointless	Gained a better understanding of circuits with this mode of learning
exercises are a good way to get points across	Mostly dull, but group work is interesting
insufficient feedback on practical tasks	Complicated
bit boring, some practicals are pointless	Higher level of interaction now means its more engaging and effective
some workshops seem childish	Not a lot to comment on since there wasn't much Prof Skills work happening
fairly well structured, but trivial at times	The 'experiment' was successful IMO
some of this is a waste of time	Great idea connecting Labs to EDPS
insufficient feedback on presentations work	Didnt like new methods of working
	Much more complicated than before - the prep requires much time to find out about new things we havent seen before

# Analysis

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## Student questionnaire

	pre-PBL	PBL
Interest	<b>3.93</b>	<b>3.82</b>
Presentation and delivery	<b>3.45</b>	<b>3.43</b>
Effectiveness of learning	<b>3.66</b>	<b>3.75</b>
	<b>Laboratory</b>	

Table 2 Summary of mean responses to the three questions polled.

# Analysis

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## Student questionnaire: Lab

Laboratory Course Questionnaire Comments	
Pre-PBL	PBL
more feedback on how to improve wanted	Getting more fun all the time
a lot of prep needed	Project work is difficult to understand
lengthy but most enjoyable part of the course	Interesting projects, theory difficult
need more help when I don't understand	Demonstrators less helpful, and have higher expectations of prior knowledge
good experience	Demonstrators good but sometimes too picky
more background/theory needed	I preferred the old method of working
more background on project needed	A necessary evil, but fun at times
better than last semester	Linking Labs/EDPS works well - the school should continue with this
biased demonstrator assessments	Great idea, but drawing on a whiteboard hasn't helped me with lab
don't understand error handling	Fun and interesting
	After last 2 weeks of the new system I think the assessments were much fairer
	Generally OK and very interesting, but often not sure what is expected from us
	Good module, especially combining with lab

# *Conclusions*

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Students seamlessly adopted directed web-based learning  
- rapidly became self-directed learners.

Professional skills teaching now taken seriously.

Students valued 'real world' problems  
→ creativity → engagement.

The pilot study represented first steps moving on from  
being professional students to professional engineers/learners.

# *Acknowledgements*

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*I would like to thank the following staff that helped make this SCEPTrE Fellowship project:*

SCEPTrE: Jo Tait, Norman Jackson and Fred Buining  
EE : Janco Calic  
Jeremy Allan, Phil Jackson, Graham Reed  
Richard Clarke, Bill Backhouse, Dave Fishlock

# *Follow On*

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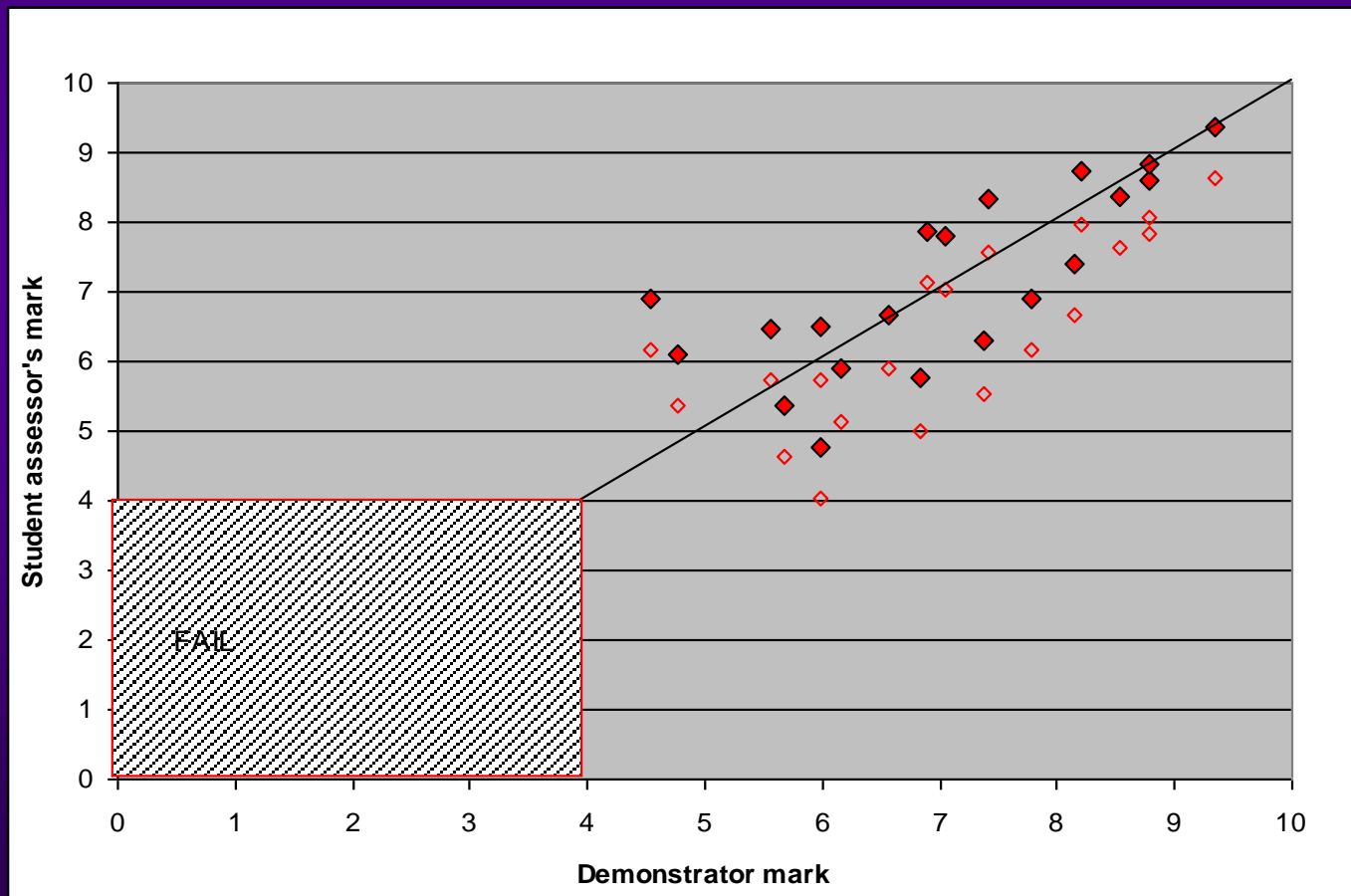
## **Spring Semester Lab/Prof Skills Programme**

- Choose 3 from 5 scripted experiments
- 4 weeks of Design Briefings
- 3 week Lab Project
- 1 week Project Showcase + PA



# Analysis

Plot PA marks vs PG Demonstrator marks



# *Follow On*

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## **Autumn Semester Lab Programme**

- new workshop training session
- new competency test



→ open-access for competent users

# Follow On

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## Autumn Semester Lab Programme

- 2 weeks assessment free
- feedback forms + single grades

Student name <i>Andrew Probst</i>		Date <i>28.10.2008</i>	Demonstrator name <i>EMRE</i>							
Laboratory Session Feedback & Assessment		A1	A2	B1	<b>B2</b>	B3	C1	C2	C3	Grade <small>[B &amp; C Experiments only]</small> <i>B+</i>
Aspects of the work executed to a good professional standard <i>- good progress</i> <i>- finished all the work extras</i>		Aspects that need to be delivered to a better professional standard <i>- labelling</i> <i>- more comments</i>				Overall summary of student performance <i>- good</i>				
PLEASE ENSURE ALL FIELDS ARE COMPLETED										