NAME

mkdep − construct Makefile dependency list

SYNOPSIS

mkdep [**−ap**] [**−f depend_file**] [flags] file ...

DESCRIPTION

Mkdep takes a set of flags for the C compiler and a list of C source files as arguments and constructs a set of include file dependencies which are written into the file *depend_file*, or ‘‘.depend’’ by default. An example of its use in a Makefile might be:

```
CFLAGS= -O -DDEBUG -I../include -I.
SRCS= file1.c file2.c

depend:
        mkdep ${CFLAGS} ${SRCS}
```

where the macro SRCS is the list of C source files and the macro CFLAGS is the list of flags for the C compiler.

If the **-p** option is provided, *mkdep* produces dependencies of the form ‘‘program: program.c’’ so that subsequent makes will produce *program* directly from its C module rather than using an intermediate *.o* module. This is useful in directories that contain many programs, each of whose source is contained in a single C module. The **-a** option causes appending to the output file, so that multiple *mkdep*’s may be run from a single Makefile.

SEE ALSO

cc(1), cpp(1), make(1)