

ADVANCED HIGH SPEED CMOS

74AC SERIES

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage AC series	2 to 6	V
V _I	ACT series	4.5 to 5.5	V
T _A	Input Voltage	0 to V _{CC}	V
	Operating Temperature (74 Series)	– 40 to 85	C

Type	Description	Package
74AC00	Quad 2-Input NAND Gate	TSSOP14
74AC02	Quad 2-Input NOR Gate	TSSOP14
74AC04	Hex Inverter	TSSOP14
74AC08	Quad 2-Input NAND Gate	TSSOP14
74AC138	3 to 8 Line Decoder (Inv.)	TSSOP16
74AC14	Hex Schmitt Inverter	TSSOP14
74AC240	Octal Bus Buffer (3-State/Inv.)	TSSOP20
74AC244	Octal Bus Buffer (3-State)	TSSOP20
74AC245	Octal Bus Transceiver (3-State)	TSSOP20
74AC257	Quad 2-Channel Multiplexer (3-State)	TSSOP16
74AC32	Quad 2-Input OR Gate	TSSOP14
74AC373	Octal D-Type Latch (3-State)	TSSOP20
74AC374	Octal D-Type Flip-Flop (3-State)	TSSOP20
74AC573	Octal D-Type Latch (3-State)	TSSOP20
74AC574	Octal D-Type Flip-Flop (3-State)	TSSOP20
74AC74	Dual D-Type Flip-Flop With Preset And Clear	TSSOP14
74AC86	Quad Exclusive OR Gate	TSSOP14
74ACT00	Quad 2-Input NAND Gate	TSSOP14
74ACT02	Quad 2-Input NOR Gate	TSSOP14
74ACT04	Hex Inverter	TSSOP14
74ACT08	Quad 2-Input AND Gate	TSSOP14
74ACT138	3 to 8 Line Decoder (Inv.)	TSSOP16
74ACT14	Hex Schmitt Inverter	TSSOP14
74ACT240	Octal Bus Buffer (3-State/Inv.)	TSSOP20
74ACT244	Octal Bus Buffer (3-State)	TSSOP20
74ACT245	Octal Bus Transceiver (3-State)	TSSOP20
74ACT257	Quad 2-Channel Multiplexer (3-State)	TSSOP16
74ACT32	Quad 2-Input OR Gate	TSSOP14
74ACT373	Octal D-Type Latch (3-State)	TSSOP20
74ACT374	Octal D-Type Flip-Flop (3-State)	TSSOP20
74ACT573	Octal D-Type Latch (3-State)	TSSOP20
74ACT574	Octal D-Type Flip-Flop (3-State)	TSSOP20
74ACT74	Dual D-Type Flip-Flop With Preset And Clear	TSSOP14
74ACT86	Quad Exclusive OR Gate	TSSOP14