










**Standard Circuits****Memories & Smartcard Products**

<a href="#"> <u>AN394</u></a>	MICROWIRE EEPROM COMMON I/O OPERATION
<a href="#"> <u>AN397</u></a>	MEMORY PRODUCT TIMING SPECIFICATIONS
<a href="#"> <u>AN404</u></a>	WRITE PROTECTION IN THE I <sup>2</sup> C AND XI <sup>2</sup> C EEPROM FAMILIES
<a href="#"> <u>AN619</u></a>	FAST BURST SRAM DESIGN CONSIDERATIONS
<a href="#"> <u>AN620</u></a>	ON-BOARD PROGRAMMING OF OTP EPROM
<a href="#"> <u>AN624</u></a>	SERIAL EEPROM MEMORY COMPATIBLE WITH THE PLUG&PLAY VESA DISPLAY DATA CHANNEL 1.0
<a href="#"> <u>AN625</u></a>	COMPATIBILITY FOR 4-MEGABIT SINGLE VOLTAGE FLASH MEMORY FROM ST AND AMD
<a href="#"> <u>AN627</u></a>	SERIAL EEPROM MEMORY COMPATIBLE WITH THE PLUG&PLAY VESA DISPLAY DATA CHANNEL 1.0 AND 2.0
<a href="#"> <u>AN907</u></a>	COMPATIBILITY BETWEEN ST BOOT BLOCK AND INTEL SMARTVOLTAGE FLASH MEMORIES