

**74LVQ SERIES**

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Supply Voltage	2 to 3.6	V
V <sub>I</sub>	Input Voltage	0 to V <sub>CC</sub>	V
T <sub>A</sub>	Operating Temperature (74 Series)	– 40 to 85	°C

Type	Description	Package
<a href="#">74LVQ00</a>	Quad 2-Input NAND Gate	TSSOP14/SO14
<a href="#">74LVQ02</a>	Quad 2-Input NOR Gate	TSSOP14/SO14
<a href="#">74LVQ04</a>	Hex Inverter	TSSOP14/SO14
<a href="#">74LVQ08</a>	Quad 2-Input NAND Gate	TSSOP14/SO14
<a href="#">74LVQ125</a>	Quad Bus Buffer (3-State)	TSSOP14/SO14
<a href="#">74LVQ138</a>	3 to 8 Line Decoder (Inv.)	TSSOP16/SO16
<a href="#">74LVQ14</a>	Hex Schmitt Inverter	TSSOP14/SO14
<a href="#">74LVQ157</a>	Quad 2-Channel Multiplexer	TSSOP14/SO16
<a href="#">74LVQ174</a>	Hex D-Type Flip-Flop With Clear	TSSOP14/SO16
<a href="#">74LVQ240</a>	Octal Bus Buffer (3-State/Inv.)	TSSOP20/SO20
<a href="#">74LVQ241</a>	Octal Bus Buffer (3-State)	TSSOP20/SO20
<a href="#">74LVQ244</a>	Octal Bus Buffer (3-State)	TSSOP20/SO20
<a href="#">74LVQ245</a>	Octal Bus Transceiver (3-State)	TSSOP20/SO20
<a href="#">74LVQ273</a>	Octal D-Type Flip-Flop With Clear	TSSOP14/SO20
<a href="#">74LVQ32</a>	Quad 2-Input OR Gate	TSSOP14/SO14
<a href="#">74LVQ373</a>	Octal D-Type Latch (3-State)	TSSOP20/SO20
<a href="#">74LVQ374</a>	Octal D-Type Flip-Flop (3-State)	TSSOP20/SO20
<a href="#">74LVQ573</a>	Octal D-Type Latch (3-State)	TSSOP20/SO20
<a href="#">74LVQ574</a>	Octal D-Type Flip-Flop (3-State)	TSSOP20/SO20
<a href="#">74LVQ74</a>	Dual D-Type Flip-Flop With Preset And Clear	TSSOP14/SO14
<a href="#">74LVQ86</a>	Quad Exclusive OR Gate	TSSOP14/SO14