

**Static Rams****ASYNCHRONOUS FAST SRAM**[!\[\]\(d66ff64371a51729ac8c1cdaa685ba6f\_img.jpg\) M624256](#)VERY FAST CMOS 1 MEGABIT (256K X 4) SRAM WITH  
OUTPUT ENABLE[!\[\]\(003082e50e3009141f59bd5df831749f\_img.jpg\) M628032](#)

VERY FAST CMOS 32K X 8 SRAM WITH OUTPUT ENABLE

[!\[\]\(faf942dc3e59ce8eb64b4ac481eca7e0\_img.jpg\) M628128](#)1 MEGABIT (128K X 8) VERY FAST SRAM WITH OUTPUT  
ENABLE[!\[\]\(d3102649f02e825ddb76dc3de0190154\_img.jpg\) M63532P](#)32KX32 PIPE-LINED BRAM LOW VOLTAGE CACHE BURST  
SRAM[!\[\]\(95b425611cbd2b8716a140cf67c81822\_img.jpg\) M638032](#)LOW VOLTAGE 256K (32K X 8) VERY FAST SRAM WITH  
OUTPUT ENABLE