# SEMICUSTOM DESIGN TOOLS

**SGS-THOMSON MICROELECTRONICS**

## SGS-THOMSON ASIC DESIGN KITS

| Type | Description |
|------|-------------|
| Cadence ASIC Design Kit (ADK) | SUN based. Includes support for Verilog, Veritime, Verifault.<br>Delay calculator with RC-backannotation.<br>Module Generators. EWS interface.<br>ATE support to testers.<br>Synopsys interface. Preview interface.<br>Bonding diagram manager.<br>VHDL simulation (LEAPFROG).<br>Clock tree synthesis.<br>Qualified as Golden System. |
| Mentor ASIC Design Kit (MDK) | SUN and HP based. Includes support for Quicksim, Quickpath, Quickgrade.<br>Delay calculator with RC - backannotation.<br>Module generators.<br>ATE support to testers.<br>Synopsys and Autologic interface.<br>VHDL simulation (QUICKVHDL).<br>Clock tree synthesis.<br>Qualified as Golden System. |
| Synopsys Synthesis Design Kit | Logic Synthesis including<br>Design Compiler, Design Analyzer and Test Compiler. Available on SUN and HP platforms.<br>Mentor, Cadence and Verilog interfaces. |
| Verilog Stand alone Simulator Design Kit | Available on SUN platform. |
| System HILO Stand alone Simulator Design Kit | Available on SUN, HP and DEC platforms. |
| Synopsys $V_{SS}$ Stand alone Simulator Design Kit | Available on SUN and HP platforms. |