BIOS: Basic **I**nput/**O**utput **S**ystem - A set of programs encoded in Read Only Memory (ROM) to facilitate the transfer of data and control instructions between the computer and peripherals.

CMOS: Complementary **M**etal-**O**xide **S**emiconductor - An energy-saving chip fabricated to duplicate the functions of other chips, such as memory chips or microprocessors. The system settings for most PCs are stored in the CMOS.

{button A,JI(`',`A')}{button B,JI(`',`B')}{button C,JI(`',`C')}{button D,JI(`',`D')}{button E,JI(`',`E')}
{button F,JI(`',`F')}{button G,JI(`',`G')}{button H,JI(`',`H')}{button I,JI(`',`I')}{button J,JI(`',`J')}
{button K,JI(`',`K')}{button L,JI(`',`L')}{button M,JI(`',`M')}
{button N,JI(`',`N')}{button O,JI(`',`O')}{button P,JI(`',`P')}{button Q,JI(`',`Q')}{button R,JI(`',`R')}{button S,JI(`',`S')}{button T,JI(`',`T')}
{button U,JI(`',`U')}{button V,JI(`',`V')}{button W,JI(`',`W')}{button X,JI(`',`X')}{button Y,JI(`',`Y')}{button Z,JI(`',`Z')}

It is recommended that the help file be read from the **Readme.hlp**.   The Vocab.hlp is designed for linking purposes only.

A

B

BIOS

C

CMOS

D

E

F

G

H

I

ISP

J

JRE

K

L

LAN

M

N

O

P

PCI

Q

QEMM

R

RAM

S

T

U

URL

V

VESA
VLB

W

X

Y

Z

ISP: Internet **S**ervice **P**rovider

JRE: Java™ **R**untime **E**nvironment

LAN: Local **A**rea **N**etwork

PCI: Peripheral **C**omponent **I**nterconnect local bus - This architecture is the successor to VLB.

QEMM: Quarterdeck **E**xpanded **M**emory **M**anager from Quarterdeck Corp.

RAM: **R**andom **A**ccess **M**emory

URL: Uniform **R**esource **L**ocator - An addressing scheme used to link resources on the World Wide Web. Using URLs, a web browser can point to most resources on the net.

VESA: **V**ideo **E**lectronics **S**tandards **A**ssociation.

VLB: VESA **L**ocal **B**us - A high-speed bus for graphics cards that provides better video performance on high-end machines.