Figures

Figure 1-1 MIPS 32-bit Virtual Address Format (MIPS II Mode) 8
Figure 1-2 MIPS 64-bit Virtual Address Format (MIPS III Mode) 9
Figure 1-3 Architectural Block Diagram of Address/Data Flow 12
Figure 1-4 MIPS II Virtual Memory Map 13
Figure 1-5 MIPS II 32-bit versus MIPS III 64-bit Kernel Mode Address Space 14
Figure 1-6 Device Driver Position in the Kernel 19
Figure 3-1 VME-bus Adapter 59
Figure 4-1 Indigo2Memory Space 109
Figure 4-2 Product ID Format 119
Figure 5-1 Minor Number Bit Definitions 135
Figure 6-1 The SysAD Bus in Context 193
Figure 9-1 IRIX 5.3 Kernel Network Architecture 237
Figure 10-1 IRIS Front Panel Dip Switches 271