PC Processors (Pentium)

	Pentium ™	*Pentium*
Vendor	Intel®	Intel
Type	CISC/RISC	CISC/RISC
Code name	P5	P54C
Avail date	Available June 93	Available March 94
IBM models	PS/2 Model 95	
	VP P60/D	
MHz	60, 66 MHz	**100/66, 90/60, 75/50 MHz**
iCOMP	567 at 66 MHz	735 at 90/60 MHz
Data bus	64 bit Data Path	*Same*
Processor	32 bit Processor	*Same*
Address bus	32 bit Address Path	*Same*
Cache	8 KB instruction cache	*Same*
	Write-thru	*Same*
	2 way set; with parity;	*Same*
	8 KB data cache	*Same*
	Write-thru or write-back	*Same*
	2 way set; with parity	*Same*
Features	Pipelined math co std	*Same*
	2 execute units (superscalar)	*Same*
	Internal and external parity	*Same*
	Dynamic branch prediction	*Same*
	Optimized instructions	*Same*
	Address pipelining	*Same*
	Burst mode bus (to 528 MB/sec)	*Same*
	Power management (SMM)	*Same*
		Dual processing ready via
		built in APIC (Advanced Programmable Interrupt Controller)
	273 pin / 0.8u	296 pin / 0.6u
Voltage	5.0 volts	**3.3 volts**
OverDrive™	P5T;	① P54CT uniprocessor replacement;
upgrade	273 pin;	320 pin;
	Available 1996	Available 1996
		② P54CM upgrades P54C to
		multiprocessing;
		100/66, 90/60 MHz;
		Available mid 1995