## **Local Bus Architectures**

A single system can have multiple buses in the same PCI and VL-Bus (in the same system) unit, including both VL-Bus and PCI. Local bus / VL-Bus 32 bit bus (up to 50 MHz) The picture to the right represents the IBM<sup>®</sup> ValuePoint™ 486 and Memory Performance Series. This system comes standard with cache VL-Bus graphics and VL-Bus IDE controllers on all systems. There is a choice of models with a riser card **PCI** IDE disk Graphics with either VL-Bus and ISA expansion slots or PCI and bridge controller controller ISA expansion slots. VL-Bus 2.0 VL-Bus 2.0 In addition, a user can swap either a VL-Bus/ISA or a Peripheral Com ponent Interconnect (PCI) 32 bit bus at 33 MHz PCI/ISA riser card at any time, so can switch local bus architectures for the expansion slots. ISA bus controller The ability to switch local bus expansion slots is called **PCI PCI PCI** SelectaBus™ and is implemented by a \$115 riser card. slot slot slot ISA 16 bit expansion bus 8 bit 8 bit 8 bit 8 bit ISA slot ISA slot ISA slot Diskette **Parallel** Serial Keyboard ISA slot controller port port /mouse PCI and VL-Bus (in the same system) Another example of how VL-Bus and PCI could be in the same system. CPU and Local bus Memory cache **PCI** bridge Peripheral Component Interconnect (PCI) 32 bit bus at 33 MHz PCI-VL LAN Motion Disk Audio Memory Bridgevideo adapter controller Graphics Memory ISA, EISA, MCA Controller bus controller Graphics Standard ISA, EISA, or MCA Memory 16 or 32 bit expansion bus I/O Modem Fax board

All trademarks are the property of their respective owners (listed on Trademark sheet)

No warranties are expressed or implied in this summary (2LOCAL) Compiled by Roger Dodson, IBM. July 1994