PC Processors (486 family)

	80486 SX	*80486 SX2*	*80486 SL*	*80486 DX*	*80486 DX2*
Vendor	Intel	Intel	Intel	Intel	Intel
Code name	P23		H4C	P4	P24
Type	CISC	CISC	CISC	CISC	CISC
Avail date	Available now	Available April 94	Available now	Available now	Available now
IBM models	ValuePoint 76, 77, 85, 90, 95		350, 350C, 750, 750Cs, 750C, 750P	ValuePoint 70, 90, 95, P75, 195, 295 Upgrades for PS/2	ValuePoint 77, 85, 90, 95 Upgrades for PS/2, VP
MHz	16, 20, 25, 33 MHz	50/25 MHz	25, 33 MHz	25, 33, 50 MHz	66/33, 50/25, 40/20
iCOMP	100 at 25 MHz	180 at 50/25 MHz	166 at 33 MHz	249 at 50 MHz	297 at 66/33 MHz
Data bus	32 bit Data Path	32 bit Data Path	32 bit Data Path	32 bit Data Path	32 bit Data Path
Processor	32 bit Processor	32 bit Processor	32 bit Processor	32 bit Processor	32 bit Processor
Address bus	32 bit Address Path	32 bit Address Path	32 bit Address Path	32 bit Address Path	32 bit Address Path
Cache	8 KB unified cache Write-thru 4 way set	8 KB unified cache Write-thru 4 way set	8 KB unified cache Write-thru 4 way set	8 KB unified cache Write-thru 4 way set	8 KB unified cache Write-thru 4 way set
Features			Math cop optional (ThinkPad 350 have none: ThinkPad 750 has one)	Math coprocessor std	Math coprocessor std
	Optimized instructions Address pipelining Burst mode bus Power mgmt (SMM) [1] 168 pin / 1.0u	Optimized instructions Address pipelining Burst mode bus Power mgmt (SMM) 168 pin / 0.8u	Optimized instructions Address pipelining Burst mode bus Power mgmt (SMM) 168 pin / 1.0u	Optimized instructions Address pipelining Burst mode bus Power mgmt (SMM) [1] 168 pin / 1.0u	Optimized instructions Address pipelining Burst mode bus Power mgmt (SMM) [1] 168 pin / 0.8u
Voltage	5.0 or 3.3 volts	5.0 volts	**3.3 volts**	5.0 volts	5.0 volts
OverDrive upgrade	① 168/169 pin 486DX2 ② 237/238 pin P24T based on Pentium™ technology; Available late 1994	① 237/238 pin P24T based on Pentium technology; Available late 1994	None	① 168/169 pin 486DX2 ② 237/238 pin P24T based on Pentium technology; Available late 1994	① 237/238 pin P24T based on Pentium technology; Available late 1994

	IntelDX4 ™
Vendor	Intel
Code name	P24C
Type	CISC
Avail date	Available April 94
IBM models	
MHz	100/50, 100/33, 83/33, 75/25
iCOMP	435 at 100 Mhz
Data bus	32 bit Data Path
Processor	32 bit Processor
Address bus	32 bit Address Path
Cache	**16 KB** unified cache Write-thru 2 way set; with parity
Features	Math coprocessor std
	Optimized instructions Address pipelining Burst mode bus (to 160 MB/sec) Power mgmt (SMM) 168 pin / 0.6u
Voltage	**3.3 volts**
OverDrive upgrade	235/237 pin P24CT based on Pentium technology; 100/50 and 100/33 MHz; Available 1996

Note 1: Effective June 1993, the **486 SX**, **486 DX**, and **486 DX2** (called 'SL Enhanced Intel486™ microprocessors') includes the **486 SL** power mgmt technology called System Management Mode (SMM).

Intel OverDrive™ Processor: with a single chip upgrade, gives up to a 70% performance boost w/o modifying external system clock or memory subsystem. The chip may be placed in an open socket on planar or in the socket after the existing processor is removed.

Burst mode bus: for reads and writes from processor to memory; 4 back-to-back data transfers (usually in 5 cycles); 486 is 128 bits (16 bytes) and Pentium CPU is 256 bits (64 bits x 4; (32 bytes).

(2INTEL) Compiled by Roger Dodson, IBM. March 1994