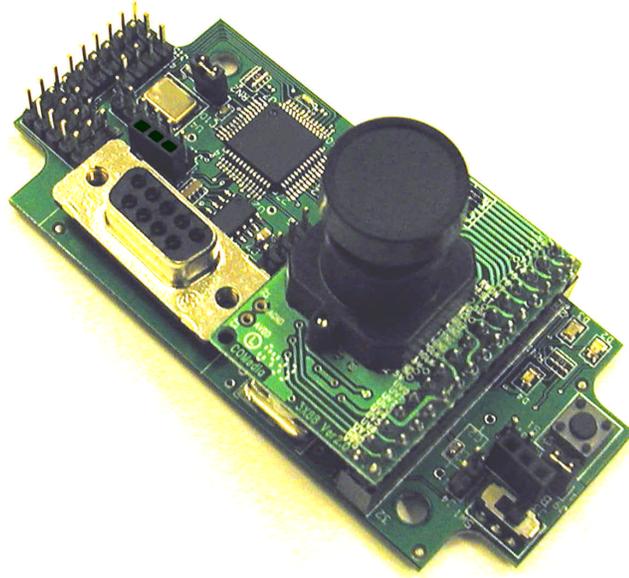


CMUcam2 Vision Sensor

(With The OV7620 Camera Module)



User's Guide

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This icon will refer to pages where more detailed information can be found.



This icon will warn you about common mistakes.



This icon will point you to pages where commands used in the text are described.



This icon will suggest a generic tip from your friend the yellow dart.

This is the CMUcam2 Manual v1.05 for the CMUcam2 v1.0 firmware.
For more information go to <http://www.cs.cmu.edu/~cmucam> or contact us at cmucam@cs.cmu.edu
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Edited by Charles Rosenberg and Illah Nourbakhsh

Introduction

The CMUcam2 consists of a SX52 microcontroller (<http://www.ubicom.com/products/sx/sx.html>) interfaced with an OV6620 or OV7620 Omnivision CMOS camera (<http://www.ovt.com>) on a chip that allows simple high level data to be extracted from the camera's streaming video. The board communicates via a RS-232 or a TTL serial port and has the following functionality:

- Track user defined color blobs at up to 50 Frames Per Second*
- Track motion using frame differencing at 26 Frames Per Second
- Find the centroid of any tracking data
- Gather mean color and variance data
- Gather a 28 bin histogram of each color channel
- Manipulate Horizontally Pixel Differenced Images
- Transfer a real-time binary bitmap of the tracked pixels in an image
- Arbitrary image windowing
- Adjust the camera's image properties
- Dump a raw image (single or multiple channels)
- Up to 160 x 255 Resolution**
- Supports Multiple Baudrates: 115,200 57,600 38,400 19,200 9,600 4,800 2,400 1,200
- Control 5 servo outputs
- Slave parallel image processing mode off of a single camera bus
- Automatically use servos to do two axis color tracking
- B/W Analog video output (PAL or NTSC)**
- Flexible output packet customization
- Multiple pass image processing on a buffered image
- Works with the OV7620 or OV6620 module

*Frame Rate Depends on Window Size

**Camera Properties Depend on Camera Module

Typical Configurations and Uses

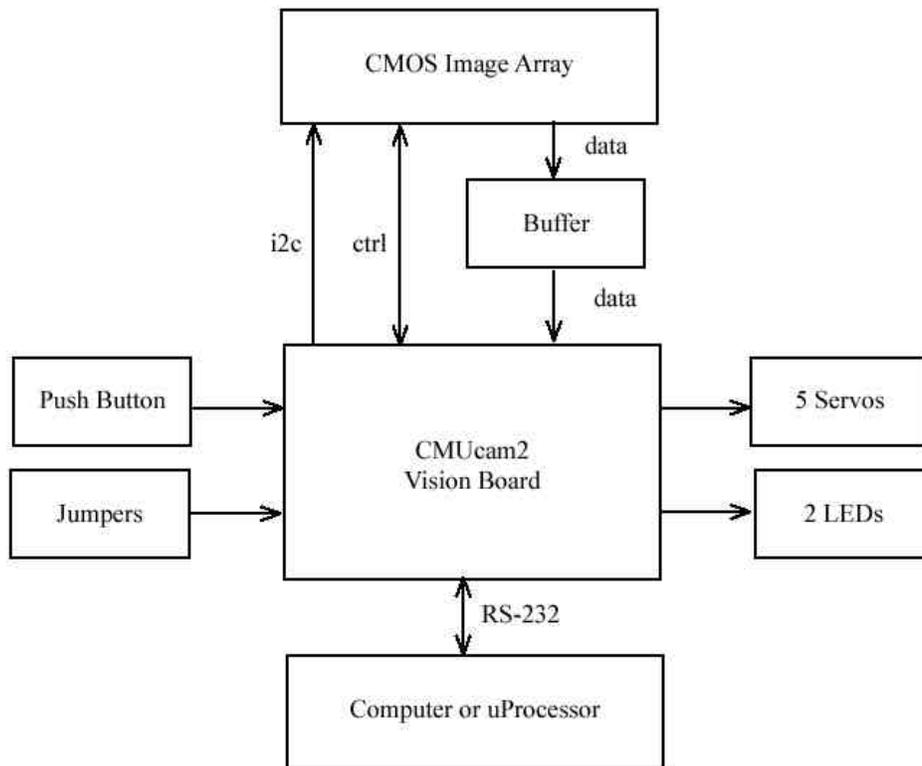
Typical Uses

One of the primary uses of the CMUcam2 is to track or monitor color. The best performance can be achieved when there are highly contrasting and intense colors. For instance, it can easily track a red ball on a white background, but it would be hard to differentiate between different shades of brown in changing light. Tracking colorful objects can be used to localize landmarks, follow lines, or chase a moving beacon.

Using color statistics, it is possible to monitor a scene, detect a specific color or do primitive motion detection. If the camera detects a drastic color change, then chances are something in the scene changed. Using "line mode," the CMUcam2 can act as an easy way to get low-resolution binary images of colorful objects. This can be used to do more sophisticated line following that includes branch detection, or even simple shape recognition. These more advanced operations would require custom algorithms that would post process the binary images sent from the CMUcam2.



See line mode on page 53.



Typical Configuration



See poll mode
on page 58.
See delay mode
on page 48.
See raw mode
on page 59.

The most common configuration for the CMUcam2 is to have it communicate to a master processor via a standard RS232 serial port. This “master processor” could be a computer, PIC, Basic Stamp, Handy Board, Brainstem or similar micro controller setup. The CMUcam2 is small enough to add simple vision to embedded systems that cannot afford the size or power of a standard computer based vision system. Its communication protocol is designed to accommodate even the slowest of processors. If your device does not have a fully level shifted serial port, you can also communicate to the CMUcam2 over the TTL serial port. This is the same as a normal serial port except that the data is transmitted using non-inverted 0 to 5 volt logic. The CMUcam2 supports various baud rates to accommodate slower processors. For even slower processors, the camera can operate in “poll mode”. In this mode, the host processor can ask the CMUcam2 for just a single packet of data. This gives slower processors the ability to more easily stay synchronized with the data. It is also possible to add a delay between individual serial data characters using the “delay mode” command. Due to the communication delays, both poll mode and delay mode will lower the total frame rate that can be processed. Frame resolutions are not affected by delay mode or baud rate as they were in the original CMUcam.

Operational Explanation

How does an image get converted into a series of pixels?

The CMOS image sensor is the heart of what actually gathers the information. It is a silicon chip that contains a grid of boxes, each of which are sensitive to different colors of light. After light passes through the lens, it stimulates these boxes, generating a different voltage proportional to the amount of light. This voltage gets converted into a single numerical value for each channel. In the case of the CMUcam2, this value is in the range of 16 to 240. There is a red channel, a blue channel and two green channels, each of which are only sensitive to that particular color of light. The extra green channel helps fill in the grid so that each pixel can be evenly spaced across the sensor. The extra green information also more closely approximates the human eye, which is more sensitive to the color green. For the purpose of simplification, the CMUcam2 ignores the second green value.

Camera Sensor Output Pixel Mapping

It is sometimes useful to understand more precisely how the data from the camera sensor is translated into pixels. Here we explain it for the OV6620 sensor, but the same basic layout applies to the OV7620 sensor.

The sensor has 356 columns and 292 rows of light sensitive cells arranged on a grid. Each location can detect a single color: red, green or blue. Here is the sensor layout of the first four rows:

Row 1: B(1,1) G(1,2) B(1,3) G(1,4) B(1,5) G(1,6) ...B(1,355) G(1,356)
Row 2: G(2,1) R(2,2) G(2,3) R(2,4) G(2,5) R(2,6) ...G(2,355) R(2,356)
Row 3: B(3,1) G(3,2) B(3,3) G(3,4) B(3,5) G(3,6) ...B(3,355) G(3,356)
Row 4: G(4,1) R(4,2) G(4,3) R(4,4) G(4,5) R(4,6) ...G(4,355) R(4,356)

The camera module takes the data from two sensor rows at a time to generate each line output from the camera module:

Row 1: B(1,1) G(2,1) R(2,2) G(1,2) B(1,3) G(2,3) R(2,4) G(1,4) ...
Row 2: B(3,1) G(2,1) R(2,2) G(3,2) B(3,3) G(2,3) R(2,4) G(3,4) ...

The CMUcam2 takes this data and outputs following pixel data:

Row 1: [R(2,2):G(1,2):B(1,1)] [R(2,4):G(1,4):B(1,3)] ...
Row 2: [R(2,2):G(3,2):B(3,1)] [R(2,4):G(3,4):B(3,3)] ...

What is tracking a color and how does the CMUcam2 do it?

Color tracking is the ability to take an image, isolate a particular color and extract information about the location of a region of that image that contains just that color. As an example, assume that you are given a photograph that contains a red ball sitting on a dirt road. If someone were to ask you to draw a box around anything that was the color red in the image, you would quite easily draw a rectangle around the ball. This is the basic idea behind color tracking. You did not need to know that the object was a ball. You only needed to have a concept of the color red in order to isolate the object in the picture. In this section we will briefly address how the CMUcam2 actually uses the information in a camera image to perform color tracking.



(Photograph Courtesy of Jim Reed)

In order to specify color, you need to define a minimum and maximum allowable value for each of the, three, color channels. Every unique color is represented by a set of red, green, and blue values that indicate how much of each channel is mixed into that final color. The tricky part about specifying a color is that you need to define a range of allowable values for each of the color channels.

Since light is not perfectly uniform and the color of an object is not perfectly uniform, you need to accommodate for these variations. However, you do not want to relax these bounds too much, or many unwanted colors will be accepted. Since, in the case of the CMUcam2, each color channel is converted into a number between 16 and 240, you can bound each channel with two numbers, an upper and lower limit. If you have two limits for each of the three channels, this means that six values can be used to constrain the entire color space that you wish to track. If you imagine the colors being represented by a cube where each side is a different color channel (red, green and blue) then the six values used to select your color would draw a three dimensional box inside that cube that defines your desired set of colors.

Once you have a bound for the color you wish to track, the CMUcam2 takes these bounds and processes the image. There are many ways to track colors in an image that can be quite complex. The CMUcam2 uses a simple one-pass algorithm that processes each new image frame from the camera independently. It starts at the top left of the image and sequentially examines every pixel row by row. If the pixel it is inspecting falls inside the range of colors that the user specified, it marks that pixel as being tracked. It also examines the position of the current tracked pixel to see if it is the top most, bottom most, left most or right most position of all of the tracked pixels found thus far in the image. If it finds that the pixel is outside of the current bounding box of the tracked region, it grows the bounding box to contain this new pixel. Because the location of even a single tracked pixel can change the bounding box, the bounding box can sometimes fluctuate quite a bit from frame to frame. Noise filtering (see next paragraph) can be used to reduce some of that fluctuation. The only other major piece of information that is stored is a sum of the horizontal and vertical coordinates of the tracked pixels. At the end of the image you can take the horizontal sum and the vertical sum of the tracked pixels and divide each by the total number of tracked pixels, you get a value that shows where the middle of the tracked object is located. Because each tracked pixel only contributes a small part to the final horizontal and vertical sums the middle (often called the centroid) of the tracked pixels is typically a much more stable measurement than the bounding box. Once all of the pixels in the image have been checked, the total number of tracked pixels can also be used in conjunction with the area of the bounding box to calculate the confidence of the tracked object.

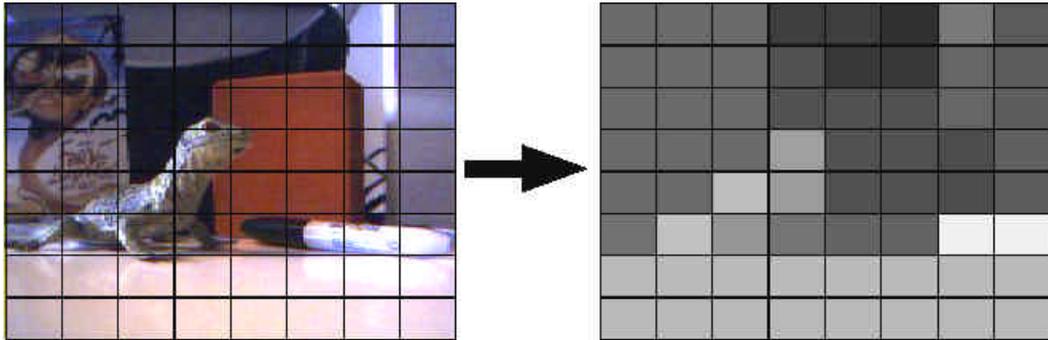
Noise filtering allows us to make the color tracking ranges larger so we can accommodate larger variations in the image pixel values without causing other random variations in the image to be tracked. The idea behind noise filtering is that we only want to consider a pixel to be of the tracked color if it is part of a group of pixels that are within the color tracking bounds. Again in the CMUcam2 we implement this in a way that only requires a single pass over the image. While processing the pixels in an image the CMUcam2 maintains a counter, which keeps track of how many sequential pixels in the current row, before the current pixel were within the tracked color bounds. If that value is above the noise filter value then the current pixel is marked as a tracked pixel.

How does the CMUcam2 do Frame Differencing?

Frame differencing is a method of identifying changes in a series of images. Given multiple images at different times from the same or similar view points, it is possible to compare them in order to isolate objects that may have moved.

Using the CMUcam2's frame differencing functionality is a good way to detect and track such motion in a scene. Instead of storing an entire image, the CMUcam2 stores an abstraction of the image. Using a similar process to color tracking, the CMUcam2 will generate or compare the image on a line-by-line basis as it receives the data.

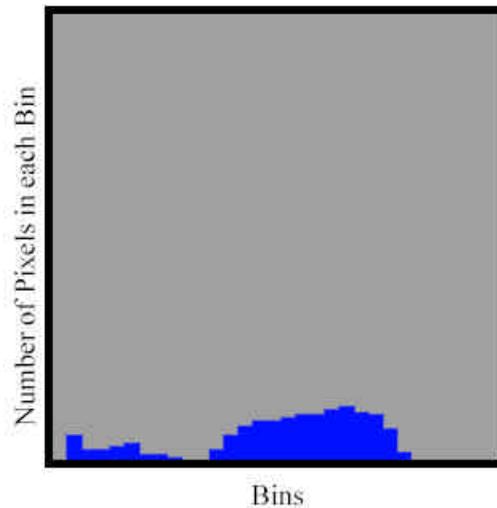
The CMUcam2 internally represents a reference image as an array of 8 by 8 bytes. Each element of this array stores the average of a corresponding region on the main camera image. The default setting uses the green or intensity channel, but this can be changed for situations where one channel clearly shows more variation than the others. When a new image is read in, it is also converted into an array of 8x8 bytes. To look for a change, each block in the 8x8 grid is subtracted from the corresponding reference image block. If there is more than a specified threshold, a change is flagged. The rest of the data, such as middle mass, is calculated in an almost identical manner to the way it is in color tracking.



What is a histogram and what is it good for?

A histogram is a type of chart that displays the frequency and distribution of data. In the case of the CMUcam2, the histograms show the frequency and distribution of color values found in an image. Each bar represents a range of color values or a specific channel. The CMUcam2 can divide the possible color values from 6 to 240 into up to 28 different bins. Each bin contains the number of pixels found in the image that fall within those color bounds. A large value in one particular bin indicates that many pixels of those colors were found in the image. Each histogram only represents one select channel of color. Using buffer mode it is possible to quickly grab three histograms, one for each channel.

Histograms are a way of abstracting the contents of an image. They have many uses such as primitive object recognition, determining thresholds, or color balancing. They are particularly useful for distinguishing between different textures. Try pointing the CMUcam2 with auto-gain turned off at two different textured surfaces and notice the difference in their color distributions. This effect could be used to distinguish floor surfaces or detect obstacles. When used in conjunction with pixel differencing a histogram can tell you about the strength of the edges visible to the camera.



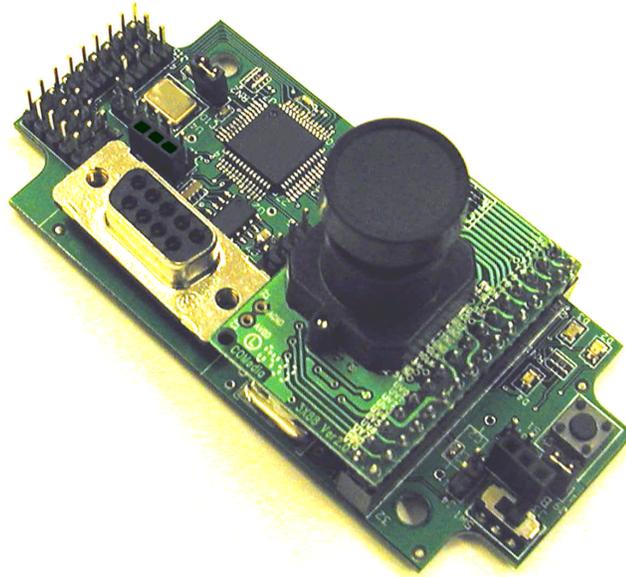
Getting Started

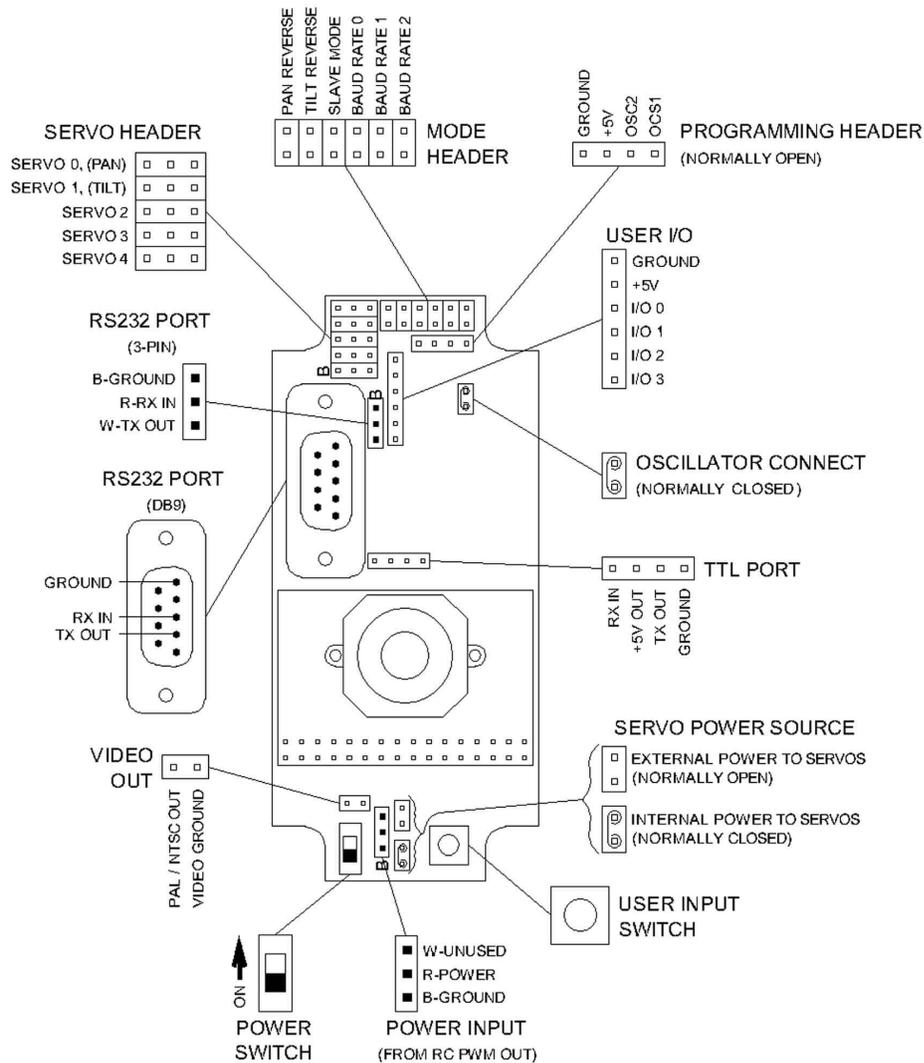
Setting Up the Hardware



See page 67, for start-up troubleshooting.

In order to initially test your CMUcam2, you will need a serial cable, a power adapter and a computer. The CMUcam2 can use a power supply that produces between 6 to 15 volts of DC power capable of supplying at least 200mA of current. Power may be provided by either an AC adapter, (possibly included), or a battery supply. These should be available at any local electronics store. The serial cable should have been provided with your CMUcam2. Make sure that you have the CMOS sensor board, (camera module), connected to the CMUcam2 board so that it is in the same orientation as the picture shows below.





Make Sure Clock and Serial Jumper are in place.

First, connect the power. Make sure that the negative side of your power plug is oriented at the end of the connector with the “B” on the board. If the camera came with an AC adapter, make sure that the seats into the socket correctly. Now that the camera has power, connect the serial link between the camera and your computer. This link is required initially so that you can test and focus your camera. Be sure to have only one RS232 port cable attached to the camera at a time. Check to make sure that the clock jumper is connected. This allows the clock to actively drive the processor. Once everything is wired up, try turning the board on. The power LED should illuminate green and only one LED should remain on. Both LEDs turn on upon startup, and one turns off after the camera has been successfully configured.

Testing the Firmware

Once you have set the board up and downloaded the firmware, a good way to test the system is to connect it to the serial port of a computer.

Step 1: If one does not already exist, build a serial and/or power cable.

Step 2: Plug both of them in.

Step 3: Open the terminal emulator of your choice.

Step 4: Inside the terminal emulator set the communication protocol to 115,200 Baud, 8 Data bits, 1 Stop bit, no parity, local echo on, no flow control and if possible turn on “add line feed” (add `\n` to a received `\r`). These settings should usually appear under “serial port” or some other similar menu option.

Step 5: Turn on the CMUcam2 board; the Power LED should light up and only one of the two status LEDs should remain on.

Step 6: You should see the following on your terminal emulator:

```
CMUcam2 v1.0 c6
:
```

If you have seen this, the board was able to successfully configure the camera and start the firmware.

Step 7: Type `gv` followed by the enter key. You should see the following:

```
:gv
ACK
CMUcam2 v1.0 c6
:
```

This shows the current version of the firmware. If this is successful, your computer’s serial port is also configured correctly and both transmit and receive are working.



See page 70, for more detailed Terminal software information.



See get version on page 51.

Focusing with the CMUcam2 Graphical User Interface (GUI)

When you first run your CMUcam2, the lens will most likely not be in focus. In order to focus the camera you need to look at some dumped images. The easiest way to do this is using a graphical user interface that can display the CMUcam2 frame dump packets. One option is to use the CMUcam2GUI, a Java program that can be found on the CMUcam2 website.

Step 1: Testing if you have java installed



The CMUcam
GUI needs java
1.4.0 or newer.

The first step is to determine if your computer already has java installed. The easiest way to do this is go to the “start menu” in windows and select “run”. Inside the run dialog, type “command” to get a dos prompt. (In Unix or later versions of the Mac OS, open up a shell.) Now try typing “Java -version” into your command line. If a message that says “Java version “1.x.xx” appears then java is installed. If instead you get “command not found” or some similar message, then you need to go to java.sun.com and download a copy of Java (J2SE, JDK, JRE are all valid things to install). Sun should have platform specific instructions on how to install java. Also be sure that your version of Java is 1.4.0 or newer. If it is not, then you will need to download a new copy of Java.

Step 2: Running the CMUcam2GUI

Once you have Java installed, download a copy of the latest CMUcam2GUI Java program. Unzip the CMUcam2GUI.zip file. Open up the stand_alone folder. In Windows double click on the CMUcam2GUI jar file. In Unix, navigate to the CMUcam2GUI directory and type “java -jar CMUcam2GUI” to execute the GUI.

NOTE: If java is functioning correctly, a small pop-up window will open labeled 'Serial Port Select.' If this window does not appear you may not have Java configured and installed properly. You can use "java -version" in an MS-DOS window to ensure that you have installed version 1.4.2 or newer. You may also want to ensure that there is no "CLASSPATH" environment variable in the system (go to Settings..Control Panel..System..Advanced..Environmental Variables and if you see a variable named 'CLASSPATH' remove it).

Step 3: Grabbing a Frame

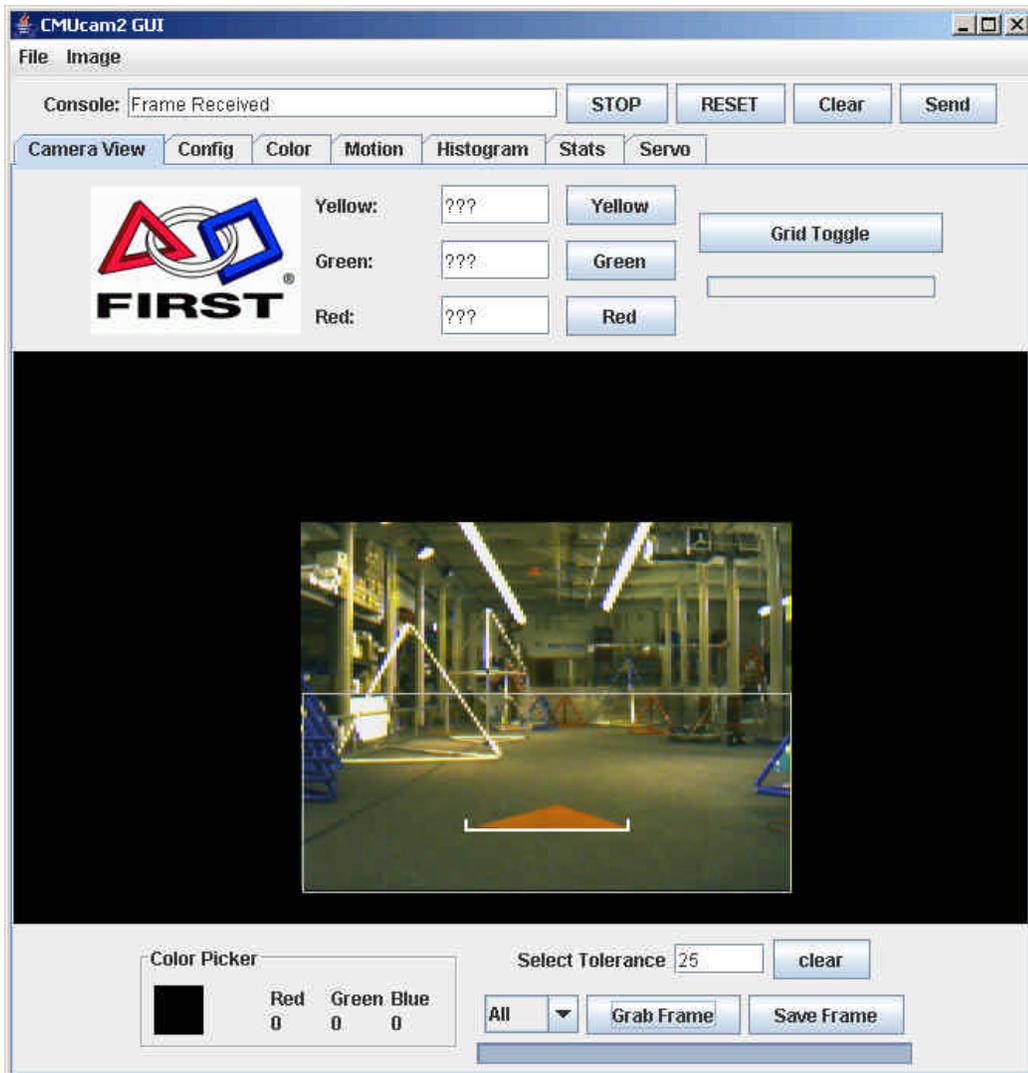
You should now see a dialog box that asks you to select the correct serial COM port.

In windows, type in the number of the COM port that the CMUcam is connected to and press the “Ok” button.

In Unix, make sure that the path to your com port is correct and then press the “Ok” button.

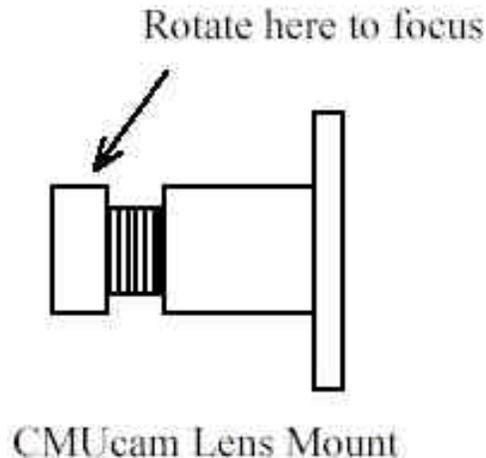


The CMUcamGUI should now open and display the message “CMUcam version 2 type X ready.” in the “Console” box. That means that the CMUcam2GUI found and was able to communicate with the camera. Once this works, select “Send Frame”. After a few seconds you should see an image appear in the window.



Step 4: Focusing

Once you have the ability to grab frames from the camera, you should be able to rotate the front part of the CMUcam lens and see the image change. Try to get the picture to be as sharp as possible by dumping frames and changing the position of the lens a small amount each time. Usually the camera is in focus when the lens is a few rotations away from the base. (Once you have focused the lens you may find it useful to use some electrical tape to keep it in place) Rotate here to focus CMUcam Lens Mount.



Step 5: Other things to try once the camera is focused

Now take a quick look at the Config tab. When you change Color Space, White Balance, etc. (except for Noise Filter), it will automatically get sent and configured to the CMUcam.

Now go to the Color tab. This has the TrackWindow button. Place a uniform, highly color-saturated object in front of the camera and click this button to track. To stop it use the "STOP" button top right. Try it with line mode by setting Config line mode on.

Go to Motion tab. Position the camera so it is looking at something static (nonmoving) and hit Load Frame. Then immediately hit Frame Diff and continuous frame differencing to the loaded frame begins. Move a small object like a pencil across the camera field of view, (FOV), to test. When done hit Stop.

Now go to Histogram tab. In here you can do 1D histograms of each color channel individually. Left to right, the histogram shows amount of '0' at the left extreme (no intensity in that color) and '255' at the right extreme (high intensity in that color). It's continuous once you hit Get Histogram. Try something black, homogenously colored, something with varied color. Again, use STOP to finish.

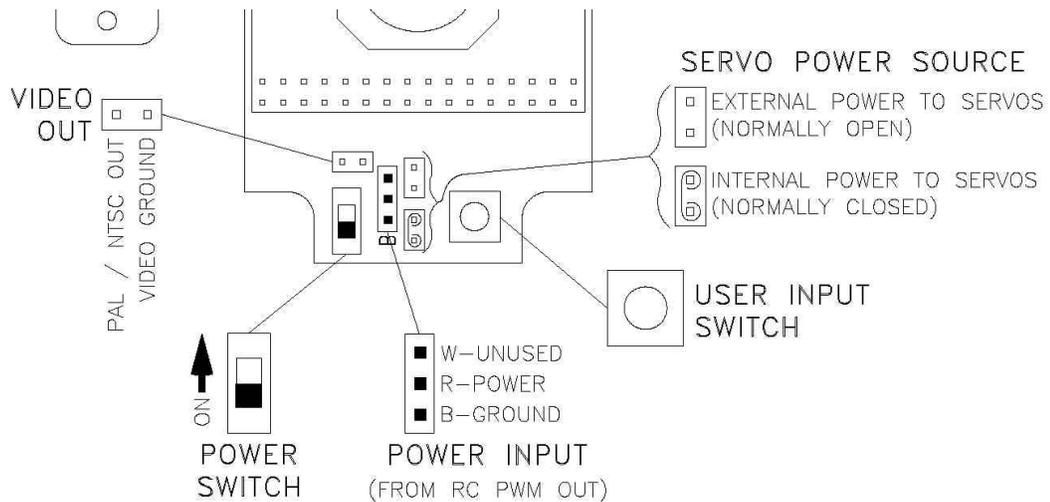
On the Stats page, once you hit GetMean, there is a very nice continuous update of the mean color seen across the camera's window.

Demo Mode

Demo mode causes the camera to call track window and then drive two standard hobby servos towards the object being tracked. This can be initiated autonomously at startup. First you need to plug a pan and/or tilt servo into servo ports 0 and 1. Servo port 0 is for the pan, while 1 is for the tilt. Next, make sure that the servos are being powered by either the internal servo power jumper or by an external power source. While holding down the User Input Switch, turn the camera on. The tracking LED should begin rapidly blinking. Immediately release the push button and wait for the LED to stop blinking. Next, point the camera at a colored object and press the User Input Switch again. This should grab the color of the object and begin automatically servo-ing towards it. If the servos appear to be driving in the reverse direction, add the appropriate servo direction jumper. During the period when the LED is blinking, the camera is adjusting to the light conditions in the room. Try not to hold the object in front of the camera while this is occurring. Experiment with different colors and lighting. You will notice that some work much better than others.



See page 24 for pan and tilt servo reverse jumpers.

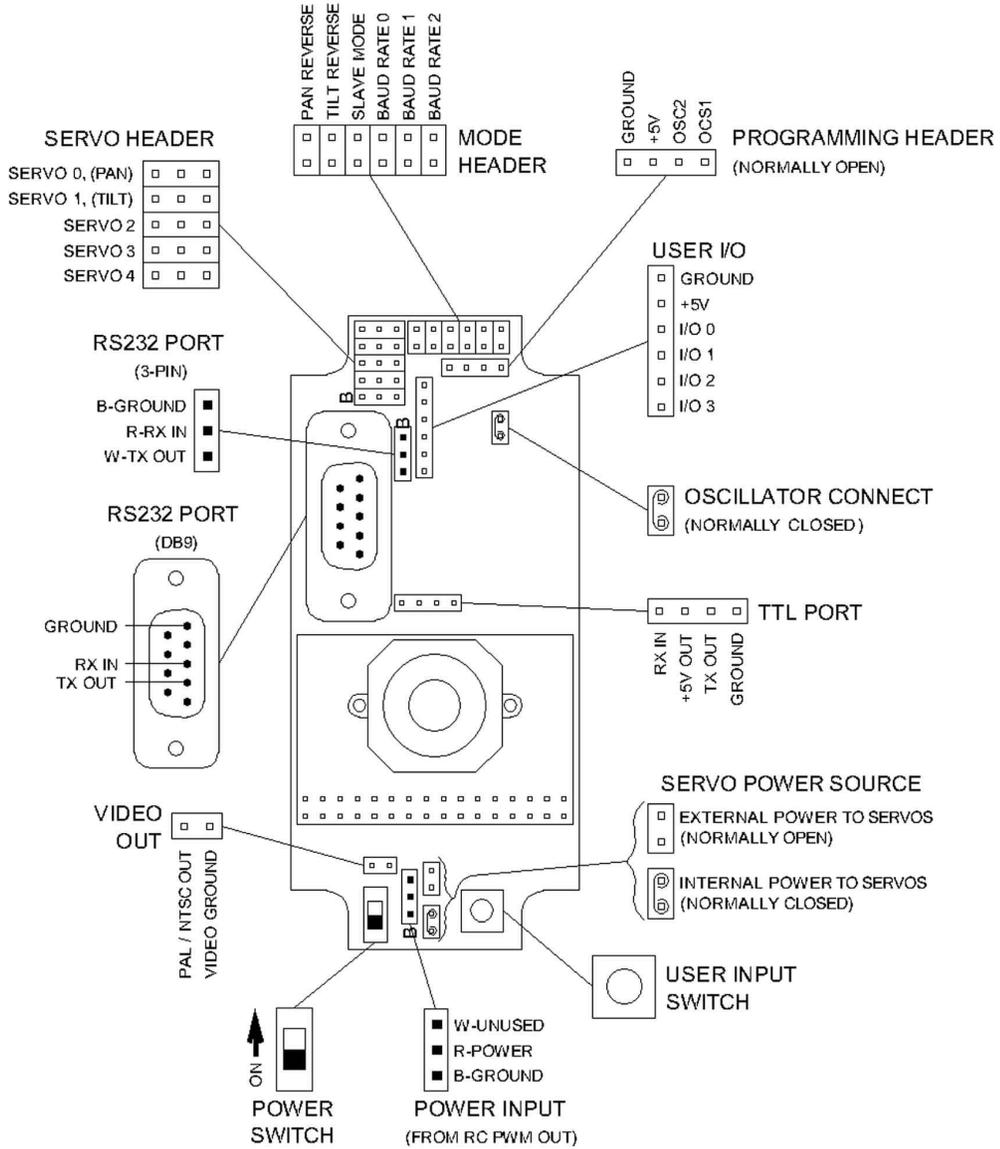


See RS on page 60.
See CR on page 32.
See SM on page 62.
See TW on page 64.

The following steps are performed during power up in demo mode:

1. RS is sent to the camera
2. Pause 5 seconds while blinking the LED to allow the camera to stabilize
3. The Camera register string "CR 18 32 19 32" is sent.
4. Auto Servo Mode is enabled.
5. TW is called.

Board Layout



Ports

Power

The input power to the board goes through a 5-volt regulator. It is ideal to supply the board with between 6 and 15 volts of DC power that is capable of supplying at least 200 milliamperes of current.

The servos can either be powered by internal power or by the external servo power connector. To run them from internal power, connect a jumper across the “internal power to servo” jumper. To run them off of external power, leave the “internal power to servo” jumper open, and connect an external 5-volt power supply to the “external power to servo” connector.



Do not connect an external servo supply while the “internal power to servo” jumper is in place. The main power switch does not switch the external servo power.

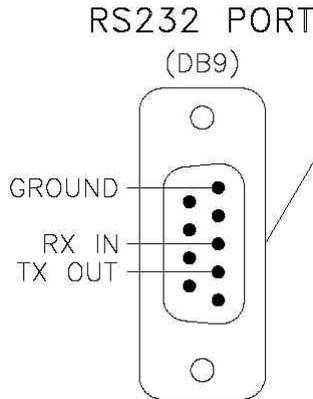
SERVO POWER SOURCE

- EXTERNAL POWER TO SERVOS
(NORMALLY OPEN)
- INTERNAL POWER TO SERVOS
(NORMALLY CLOSED)

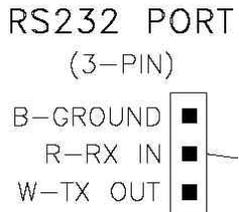
Communication Ports

The CMUcam2 has 2 standard level shifted serial ports to communicate to a computer as well as a TTL serial port for talking to a micro controller.

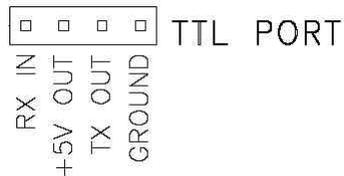
The RS232, (DB9), serial port only uses 3 of the 9 pins. This connector is provided for a standard DB9 M/F cable to connect to a COM port on a computer.



The RS232, (3-pin), serial port can be used to connect to an RS232 port on a micro controller.



The TTL connector can be used to communicate with a micro controller without the use of a level-shifting chip. It operates between 0 and 5 volts.



Only one RS232 port on the camera can be used at a time.



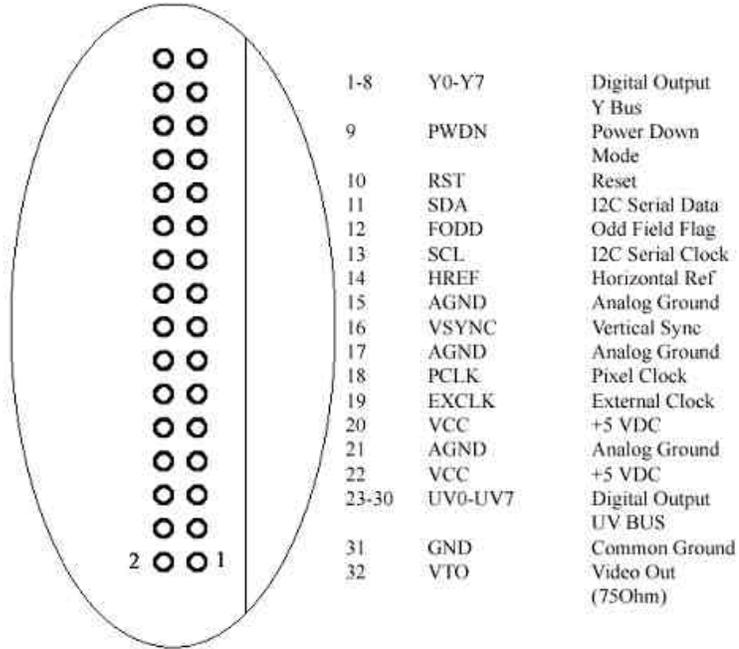
See the picture on page 12 to make sure that you have the CMOS sensor connected correctly.



See page 28 for more information on the CMOS camera chips.

Camera Bus

This bus interfaces with the CMOS camera chip. The CMOS camera board is mounted parallel to the processing part of the board and connects starting at pin





See page 20 for more information on servo power Ports.

Servo Port

The CMUcam2 has the ability to control 5 servos. This can be useful if you do not wish to use a separate servo controller. The servo port can also be used as general-purpose digital outputs.

SERVO HEADER

SERVO 0, (PAN)	□	□	□
SERVO 1, (TILT)	□	□	□
SERVO 2	□	□	□
SERVO 3	□	□	□
SERVO 4	□	□	□



See SV servo command on page 62.
See SO servo command on page 61.

User Configuration Jumpers

The jumpers can be used to set the camera's baud rates or configure various modes of operation.

PAN REVERSE	TILT REVERSE	SLAVE MODE	BAUD RATE 0	BAUD RATE 1	BAUD RATE 2	
<input type="checkbox"/>	MODE HEADER					
<input type="checkbox"/>						

Pin	0	1	2	Baud Rate
--	--	--	--	115,200 Baud
--	--	X	--	57,600 Baud
--	X	--	--	38,400 Baud
--	X	X	--	19,200 Baud
X	--	--	--	9,600 Baud
X	--	X	--	4,800 Baud
X	X	--	--	2,400 Baud
X	X	X	--	1,200 Baud

X - jumper closed
_ - jumper open

Pan and Tilt Reverse Jumpers

During Auto Servo Mode, or demo mode it may be necessary to reverse the direction of the pan or tilt servo. Connecting the pan and/or tilt jumper will cause auto servo mode to send the opposite commands to each servo. Note, this only works for auto servo mode, and not for normal servo operations.

Slave Mode Jumper

The CMUcam2 supports a mode of operation that allows multiple boards to process data from the same camera. If a PC104 style pass-through header is used instead of the standard double row female header, it is possible to rack multiple boards along the same camera bus. Upon startup, if the "SM" jumper is set, the camera becomes a slave. Slave mode stops the camera board from being able to configure or interfere with the CMOS camera's settings. Instead it processes the format setup by the master vision board. When linking the buses together you must only have one master; all other boards should be setup to be in slave mode. In this current version of the system there is no message passing between boards other than the image data from the camera bus. This means you have to communicate to each slave board via a separate serial link. This communication to the board should be identical to using a single CMUcam2. For example, you could have the master board tracking some color while the slave board could be told to get mean color data. Each board runs independently of one another and only the master can control camera registers.



See CT command on page 48.

User I/O Connector



See GI command on page 49.
See SD command on page 60.

The CMUcam has 4 auxiliary Input Output ports that can be used for reading data from external devices. Note, that pin 3 is used for the sleep deeply command.

USER I/O

- GROUND
- +5V
- I/O 0
- I/O 1
- I/O 2
- I/O 3



Make Sure the Camera is operating at full frame rate and in YCrCb mode.

Video Out Port

Using the OV6620 camera module, you will be able to get a PAL video signal from the analog port of the CMUcam2. This would sync up with any PAL monitor, but will not work with a standard NTSC monitor.

The OV7620 camera module will output a standard black and white NTSC video signal.



See CR command on page 30 for info on how to switch to YCrCb mode.

To use this output, it is necessary to keep the camera at its maximum frame rate, (the default), and switch it into YCrCb mode in order to see the image on a monitor.



Notes on Better Tracking

Auto-gain and White Balance

Auto-gain is an internal control that adjusts the brightness level of the image to best suit the environment. It attempts to normalize the lights and darks in the image so that they approximate the overall brightness of a hand adjusted image. This process iterates over many frames as the camera automatically adjusts its brightness levels. If for example a light is turned on and the environment gets brighter, the camera will try and adjust the brightness to dim the overall image.

White balance on the other hand attempts to correct the camera's color gains. The ambient light in your image may not be pure white. In this case, the camera will see colors differently. The camera begins with an initial guess of how much gain to give each color channel. If active, white balance will adjust these gains on a frame-by-frame basis so that the average color in the image approaches a gray color. Empirically, this "gray world" method has been found to work relatively well. The problem with gray world white balance is that if a solid color fills the camera's view, the white balance will slowly set the gains so that the color appears to be gray and not its true color. Then when the solid color is removed, the image will have undesirable color gains until it re-establishes its gray average.

When tracking colors, like in demo mode, you may wish to allow auto-gain and white balance to run for a short period and then shut them off. While on for a period of about 5 seconds, the camera can set its brightness gain and color gains to what it sees as fit. Then turning them off will stop the camera from unnecessarily changing its settings due to an object being held close to the lens or shadows etc. If auto-gain and white balance were not disabled and the camera changed its settings for the RGB values, then the new measured values may fall outside the originally selected color tracking thresholds.



The camera module requires Auto-gain to be enabled to utilize white balance.

YCrCb Color Space

YCrCb is a different color space definition from the more commonly known RGB space. In YCrCb the pixel illumination data is stored in the Y channel. Because of this property, in YCrCb mode the camera may be more resistant to changes in illumination. Because it is a different color space, images in YcrCb do not look like standard RGB images when directly mapped by a frame dump program. The RGB channels map to CrYCb. So in YCrCb mode, the value returned as the red parameter is actually Cr, the green parameter is Y and the blue parameter is Cb. So if you wish to track a red object, you need to look at a dumped frame to see what that object's colors map to in YCrCb. It should then be possible to find the Cr and Cb bounds while giving a very relaxed Y bound showing that illumination is not very important. Below are the transformations used by the camera to convert RGB into YCrCb:



Notice that the RGB channels map to give you CrYCb, not YCrCb.

RGB -> CrYCb
 $Y = 0.59G + 0.31R + 0.11B$
 $Cr = 0.713x (R - Y)$
 $Cb = 0.564x (B - Y)$

When using YCrCb, make sure you take into account that in terms of all CMUcam I/O, Red maps to Cr, Green to Y and Blue to Cb.

About the CMOS Camera Modules

From power up, the camera can take up to 5 seconds to automatically adjust to the lighting conditions. Drastic changes in the environment, such as lights being turned on and off, can induce a similar readjustment time. When using the camera outside, due to the sun's powerful IR emissions, even on relatively cloudy days, it will probably be necessary to use either an IR filter or a neutral density camera filter to decrease the ambient light level. The field of view depends on the lens attached to the camera. It is possible to special order the camera with wider or narrower lenses. Individual lenses can be purchased separately.

The functions provided by the camera board are meant to give the user a toolbox of color vision functions. Actual applications may greatly vary and are left up to the imagination of the user. The ability to change the viewable window, grab color / light statistics and track colors can be interwoven by the host processor to create higher-level functionality.

One notable property of the CMOS sensor array is that it returns values between 16 and 240 for each pixel. This effect is noticeable when the camera is tracking colors, getting mean color data or dumping a frame. This limited range on the data does not depend on the mode of the camera and still applies in YCrCb mode.

Serial Commands

The serial communication parameters are as follows:

- 1,200 to 115,200 Baud
- 8 Data bits
- 1 Stop bit
- No Parity
- No Flow Control (Not Xon/Xoff or Hardware)

All commands are sent using visible ASCII characters (123 is 3 bytes "123").

Upon a successful transmission of a command, the ACK string should be returned by the system. If there was a problem in the syntax of the transmission, or if a detectable transfer error occurred, a NCK string is returned. After either an ACK or a NCK, a `\r` is returned. When a prompt (`\r` followed by a `:`) is returned, it means that the camera is waiting for another command in the idle state. White spaces do matter and are used to separate argument parameters.

The `\r` (ASCII 13 carriage return) is used to end each line and activate each command.

If visible character transmission causes too much overhead, it is possible to use varying degrees of raw data transfer.



See Raw Mode on page 59 for information on configuring ascii vs raw text packets.

Functionally Grouped Command Listing

Buffer Commands	
BM	Buffer Mode
RF	Read Frame

Camera Module Commands	
CR	Camera Register
CP	Camera Power
CT	Camera Type

Data Rate Commands	
DM	Delay Mode
PM	Poll Mode
PS	Packet Skip
RM	Raw Mode
PF	Packet Filter
OM	Output Packet Mask

Servo Commands	
SV	Servo Position
SP	Servo Parameters
GS	Get Servo Position
SM	Servo Mask
SO	Servo Output

Image Windowing Commands	
SF	Send Frame
DS	Down Sample
VW	Virtual Window
FS	Frame Stream
HR	HiRes Mode
GW	Get Window
PD	Pixel Difference

Auxiliary I/O Commands	
GB	Get Button
GI	Get Auxiliary I/O
L0(1)	LED control

Color Tracking Commands	
TC	Track Color
TI	Track Inverted
TW	Track Window
NF	Noise Filter
LM	Line Mode
GT	Get Tracking Parameters
ST	Set Tracking Parameters

Histogram Commands	
GH	Get Histogram
HC	Histogram Config
HT	Histogram Track

Frame Differencing Commands	
FD	Frame Difference
DC	Difference Channel
LF	Load Frame
MD	Mask Difference
UD	Upload Difference
HD	HiRes Difference
LM	Line Mode

Color Statistics Commands	
GM	Get Mean
LM	Line Mode

System Level Commands	
SD	Sleep Deeply
SL	Sleep
RS	Reset
GV	Get Version

Alphabetical Command Listing

Alphabetical Listing		
BM	Buffer Mode	30
CP	Camera Power	30
CR	Camera Register	30
CT	Set Camera Type	48
DC	Difference Channel	48
DM	Delay Mode	48
DS	Down Sample	48
FD	Frame Difference	49
FS	Frame Stream	49
GB	Get Button	49
GH	Get Histogram	49
GI	Get Aux IO inputs	49
GM	Get Mean	50
GS	Get Servo Positions	50
GT	Get Tracking Parameters	50
GV	Get Version	51
GW	Get Window	51
HC	Histogram Configure	51
HD	High Resolution Difference	51
HR	HiRes Mode	51
HT	Set Histogram Track	52
L0 (1)	Led Control	52
LF	Load Frame to Difference	52

LM	Line Mode	53
MD	Mask Difference	56
NF	Noise Filter	56
OM	Output Packet Mask	57
PD	Pixel Difference	57
PF	Packet Filter	58
PM	Poll Mode	58
PS	Packet Skip	58
RF	Read Frame into Buffer	58
RM	Raw Mode	59
RS	Reset	59
SD	Sleep Deeply	60
SF	Send Frame	60
SL	Sleep Command	60
SM	Servo Mask	61
SO	Servo Output	61
SP	Servo Parameters	62
ST	Set Track Command	62
SV	Servo Position	62
TC	Track Color	63
TI	Track Inverted	63
TW	Track Window	63
UD	Upload Difference buffer	64
VW	Virtual Window	64

\r

This command is used to set the camera board into an idle state. Like all other commands, you should receive the acknowledgment string “ACK” or the not acknowledge string “NCK” on failure. After acknowledging the idle command the camera board waits for further commands, which is shown by the ‘:’ prompt. While in this idle state a \r by itself will return an “ACK” followed by \r and : character prompt. This is how you stop the camera while in streaming mode.

Example of how to check if the camera is alive while in the idle state:

```

:
ACK
:

```



See RF on page 58 to read a new frame when buffer mode is enabled.

BM active \r

This command sets the mode of the CMUcam's frame buffer. A value of 0 (default) means that new frames are constantly being pushed into the frame buffer. A value of 1, means that only a single frame remains in the frame buffer. This allows multiple processing calls to be applied to the same frame. Instead of grabbing a new frame, all commands are applied to the current frame in memory. So you could get a histogram on all three channels of the same image and then track a color or call get mean and have these process a single buffered frame. Calling **RF** will then read a new frame into the buffer from the camera. When **BM** is off, **RF** is not required to get new frames.

Example of how to track multiple colors using buffer mode:

```
:BM 1
ACK
:PM 1
ACK
:TC 200 240 0 30 0 30
ACK
T 20 40 10 30 30 50 20 30
:RF
ACK
:TC 0 30 200 240 0 30
ACK
```



Processing on an already buffered image is much faster than processing a new image.



See SL and SD on page 60 to decrease the camera's power consumption even more.

CP boolean \r

This command toggles the Camera module's Power. A value of 0, puts the camera module into a power down mode. A value of 1 turns the camera back on while maintaining the current camera register values. This should be used in situations where battery life needs to be extended, while the camera is not actively processing image data. Images in the frame buffer may become corrupt when the camera is powered down.

CR [reg1 value1 [reg2 value2 ... reg16 value16]]\r

This command sets the Camera's internal Register values directly. The register locations and possible settings can be found in the Omnivision CMOS camera documentation. All the data sent to this command should be in decimal visible character form unless the camera has previously been set into raw mode. It is possible to send up to 16 register-value combinations. Previous register settings are not reset between CR calls; however, you may overwrite previous settings. Calling this command with no arguments resets the camera and restores the camera registers to their default state. This command can be used to hard code gain values or manipulate other low level image properties.

Register		DESCRIPTION	PAGE
DEC	HEX		
00	[00]	AGC Gain Control	22
01	[01]	Blue Gain Control	22
02	[02]	Red Gain Control	22
03	[03]	Saturation Control	22
	[04], [05]	Reserved	4
06	[06]	Brightness Control	4
07	[07]	Analog Sharpness Control	4
	[08], [09], [0A], [0B]	Reserved	4
12	[0C]	Wht Bal Background Control – Blue Channel	4
13	[0D]	Wht Bal Background Control - Red Channel	4
	[0E], [0F]	Reserved	4
16	[10]	Auto-Exposure-Control	4
17	[11]	Clock Rate Control	5
18	[12]	Common Control A	5
19	[13]	Common Control B	5
20	[14]	Common Control C	6
21	[15]	Common Control D	6
22	[16]	Frame Drop	6
23	[17]	Horizontal Window Start	7
24	[18]	Horizontal Window End	7
25	[19]	Vertical Window Start	7
26	[1A]	Vertical Window End	7
27	[1B]	Pixel shift	7
28	[1C]	Manufacture ID High Byte	7
29	[1D]	Manufacture ID Low Byte	8
	[1E], [1F]	Reserved	8
32	[20]	Common Control E	8
33	[21]	Y Channel Offset Adjustment	8
34	[22]	U Channel Offset Adjustment	8
35	[23]	Crystal Current Control	8
36	[24]	AEW Auto Exposure White Pixel Ratio	9
37	[25]	AEC Auto Exposure Black Pixel Ratio	9
38	[26]	Common Control F	9
39	[27]	Common Control G	10
40	[28]	Common Control H	10
41	[29]	Common Control I	10
42	[2A]	Frame Rate Adjust 1	10
43	[2B]	Frame Rate Adjust 2	10
44	[2C]	Black Expanding	11
45	[2D]	Common Control J	11
46	[2E]	V Channel Offset Adjustment	11
	[2F] THRU [5F]	Reserved	11
96	[60]	Signal Process Control A	11
97	[61]	Signal Process Control B	11
98	[62]	RGB Gamma Control	12
99	[64]	Y Gamma Control	12
100	[65]	Signal Process Control C	12
101	[66]	AWB Process Control	12

Register DEC	HEX	DESCRIPTION	PAGE
102	[67]	Color Space Selection	12
103	[68]	Signal Process Control D	12
104	[69]	Analog Sharpness	13
105	[6A]	Vertical Edge Enhancement Control	13
	[6B], [6C], [6D], [6E]	Reserved	13
111	[6F]	Even/Odd Noise Compensation Control	13
112	[70]	Common Control K	13
113	[71]	Common Control J	13
114	[72]	Horizontal Sync 1st Edge Shifting	13
115	[73]	Horizontal Sync 2nd Edge Shifting	14
116	[74]	Common Control M	14
117	[75]	Common Control N	14
118	[76]	Common Control O	14
	[77], [78], [79], [7A], [7B]	Reserved	14
124	[7C]	Field Average Level Storage	14

1.13 SCCB REGISTER SETS

OV7620 can be configured, by setting pin CS<2:0> high or low at reset/power up, to one of eight slave IDs as listed below, the ID can not be altered once the chip is out of reset or power up state.

OV7620 support two option: **single** chip and **multiple** chip decided by PIN **MID**. If **MID** set to **LOW** (Default value), chip slave ID is **42**(for write) and **43**(for read). If **MID** set to **HIGH**, OV7620 can support 8 slave ID selection. Default **MID** is **LOW** by internal setting. In write cycle, the second byte in SCCB bus is the subaddress for selecting the individual on chip registers, the third byte is the data associated with this register. Writing to unimplemented subaddress and reserved subaddress is ignored. In read cycle, the second byte is the data associated with the previous stored subaddress. Reading of unimplemented subaddress returns unknown.

Registers [00] ~ [02] contains image effect parameters that also can be modified by internal controls in auto adjust mode. This provides a simple way to read out those parameters computed by chip internal controls. To do this, first set the chip in auto adjust mode (Register 13 bit 0=1, register 12 bit 2 = 1, register 12 bit 5=1), wait for the image is stable, the register [00],[01] and [02] will be updated by internal control circuit. Then returns it to manual adjust mode(register 13 bit 0=0), all the registers retain the last adjusted values and can be read or overwritten by external host. When the chip is operated in auto adjust mode(register 13 bit 0=1), register [00] ~ [02] will be update by internal algorithm and if write data to them, there will be no effect on chip parameters. The register data can be read out.

The detailed definitions of each register are described below.

Register 00 - rw: AGC gain control

Bits	Null	AGC6	AGC5	AGC4	AGC3	AGC2	AGC1	AGC0
Default	-	-	0	0	0	0	0	0

AGC<5:0> - gain setting for the entire image channel.

The formula is:

Gain = (AGC<3:0>/16+1)*(AGC<4>+1)*(AGC<5>+1); range (1x ~ 7.75x), AGC<5> and AGC<4> control SA2.

Register 01 - rw: Blue gain control

Bits	BLU7	BLU6	BLU5	BLU4	BLU3	BLU2	BLU1	BLU0
Default	1	0	0	0	0	0	0	0

The formula is: Blue gain=1+(BLU<7:0> - [80])/[100]; range (0.5x ~ 1.5x).

BLU<7> - Sign bit. If "1", Blue gain increase; "0" gain decrease.

Register 02 - rw: Red gain control

Bits	RED7	RED6	RED5	RED4	RED3	RED2	RED1	RED0
Default	1	0	0	0	0	0	0	0

RED<6:0> - white balance value for the red channel.

The formula is: Red_gain=1+(RED<7:0> - [80])/[100]; range (0.5x ~ 1.5x).

RED<7> - Sign bit. If "1", Red channel gain increase; "0" gain decrease.

Register 03 - rw: Saturation control

Bits	SAT7	SAT6	SAT5	SAT4	SAT3	SAT2	SAT1	SAT0
Default	1	0	0	0	0	0	0	0

SAT<7:0> - saturation adjustment for the UV channel based on the default setting; range (-4dB ~ +6dB). If SAT<7:0> > [80], increase; if SAT<7:0> < [80], decrease.

Register 04 & 05 - w: Reserved Register

This register is reserved for internal test use. Write data to this register will be no function.

Register 06 - rw: Brightness control

Bits	BRT7	BRT6	BRT5	BRT4	BRT3	BRT2	BRT1	BRT0
Default	1	0	0	0	0	0	0	0

BRT<7:0> - brightness adjustment for the Y/RGB channel based on the default setting; range (-200mv ~ +200mv). If BRT<7:0> > [80], brightness increase; If BRT<7:0> < [80], brightness decrease. This register is auto/manual controllable. If register 2D bit 4=1, this register is controlled by chip automatically, if write value to this register, this value will be updated by internal circuit. Only when register 2D bit 4=0, this register can be set to any value.

Register 07 - rw: Analog Sharpness control

Bits	SHP7	SHP6	SHP5	SHP4	SHP3	SHP2	SHP1	SHP0
Default	1	1	0	0	0	0	1	1

SHP<7:4> - Sharpness Threshold.

SHP<3:0> - Sharpness Magnitude.

Register [08], [09], [0A], [0B] - w: Reserved.

These four registers are reserved for internal use. Write data to these registers will not function.

Register 0C - rw: White Balance background control -- Blue channel

Bits	NUL	NUL	ABLU5	ABLU4	ABLU3	ABLU2	ABLU1	ABLU0
Default	-	-	1	0	0	0	0	0

Changes AWB Hue Control

ABLU<4:0> - White Balance background blue color component ratio adjustment. Adjust resolution is 0.625% and total range is (+20% - -20%) This register is used to offset image background blue component ratio.

ABLU<5> - Sign bit. If "1", decrease background blue component ratio; "0" increase blue component ratio.

Register 0D - rw: White Balance background control -- Red channel

Bits	NUL	NUL	ARED5	ARED4	ARED3	ARED2	ARED1	ARED0
Default	-	-	1	0	0	0	0	0

Changes AWB Hue Control

ARED<4:0> - White Balance background red color component ratio adjustment. Adjust resolution is 1.5% and total range is (+20% - -20%) This register is used to offset image background red component ratio.

ARED<5> - Sign bit. If "1", decrease background red component ratio; "0" increase red component ratio.

Register [0E], [0F]- rw: Reserved

These two registers are reserved for internal use. Write data to these registers will not function.

Register 10 - rw: Auto-Exposure-Control Register

Bits	AEC7	AEC6	AEC5	AEC4	AEC3	AEC2	AEC1	AEC0
Interlace	0	1	1	1	1	1	1	1
Progressive	1	1	1	1	1	1	1	1

AEC<7:0> - exposure time setting; the formula is Interlaced: $TEXPOSURE = TLINE \times AEC(7:0)$; Progressive: $TEXPOSURE = TLINE \times AEC(7:0) \times 2$; where $TLINE = \text{Frame Time} / 525$ if you use 27MHz, $TLINE = 63.5 \mu S$ Range is: [00] - [7F] for Interlaced; [00] - [FF] for Progressive Scan.

* This register setting is only effective when operated in manual adjust mode (register 13 bit 0=0). Nevertheless, this register is always accessible through the SCCB bus. If register 13 bit 0=1, this register will be updated by internal circuit according AEC algorithm, and if write special value to this register will be useless. The register value can be read out at any time and latest AEC value will be return. If register 13 bit 0=0, or register 29 bit 7=1, the register will hold last value unchanged (either input from SCCB or AEC algorithm result).

It generally takes no more than two fields for the image to reach the intended exposure after changing the setting.

Register 11 - rw: Clock rate control

Bits	SYN7	SYN6	CLK5	CLK4	CLK3	CLK2	CLK1	CLK0
Default	0	0	0	0	0	0	0	0

CLK<5:0> - system clock prescaler; this register defines the chip pixel clock rate, clock rate is defined by following formula:

(16 Bit mode) $PCLK = (CLK_input / ((CLK<5:0> + 1) * 2))$

(8 Bit mode) $PCLK = (CLK_input / (CLK<5:0> + 1))$

SYN<7:6> - Three sync output polarity selection:

SYN7 = 0, SYN6 = 0: HSYNC negative, CHSYNC negative, VSYNC positive edge;

SYN7 = 0, SYN6 = 1: HSYNC negative, CHSYNC negative, VSYNC negative;

SYN7 = 1, SYN6 = 0: HSYNC positive, CHSYNC negative, VSYNC positive.

SYN7 = 1, SYN6 = 1: HSYNC negative, CHSYNC positive, VSYNC positive.

* The effect of the change is immediate, however, it generally takes about two fields for the image to reach the stable state.

Register 12 - rw: Common control A

Bits	COMA7	COMA6	COMA5	COMA4	COMA3	COMA2	COMA1	COMA0
Default	0	0	1	0	0	1	0	0

COMA7 - "1" initiates the chip soft reset, the reset takes place after the acknowledge bit is issued, the effect is the same as power up the chip, the chip is initialized to a default state, all registers including SCCB's contents are set to default, this bit is self cleared after the reset.

COMA6 - "1" selects mirror image

COMA5 - "1" enables AGC. "0" - stop AGC and set register [00] to default value. Only effective in auto adjust mode.

COMA4 - "1" select 8 Bit Digital output format is Y U Y V Y U Y V ...

COMA3 - "1" selects raw data signal as video data output, "0" selects YCrCb as video data output. The selection applies to both analog video and digital video.

COMA2 - "1" enable auto white balance, "0" AWB stop and AWB register [01] and [02] value is held at last updated value. Can used as one-shot AWB mode. Valid only in auto mode.

COMA1 - "1" selects Color Bar Test pattern output.

COMA0 - "1" select precise A/D Black Level Compensation (BLC) line method. "0" use standard black level compensation to do A/D BLC field method which is more stable but less precise.

Register 13 - rw: Common control B

Bits	COMB7	COMB6	COMB5	COMB4	COMB3	COMB2	COMB1	COMB0
Default	-	-	0	0	0	0	0	1

COMB7 - Reserved.

COMB6 - Reserved.

COMB5 - "1" selects 8 bit data format, Y/CrCb and RGB video data is multiplexed to the eight bit **Y** bus, tristate **UV** bus; "0" selects 16 bit format, data go to both **Y<7:0>** bus and **UV<7:0>** bus.

COMB4 - "0" enables digital output in CCIR601 format. "1" enables CCIR656 format.

COMB3 - "0" selects horizontal sync for output to pin **CHSYNC**, "1" selects composite sync for output.

COMB2 - "1" tri-states bus **Y<7:0>** and **UV<7:0>**, "0" enables both buses.

COMB1 - "1" initiates the single frame transfer, for this function to work, field drop mode (FD<1:0> in register [16]) must set to "OFF". See figure below. After this bit is set, for Interlaced mode, **HREF** is only asserted for consecutive two fields beginning at Odd field. This bit is cleared automatically at the end of this frame. For Progressive Scan mode, **HREF** is only asserted for one frame. Clearing this bit in the middle of active frame has no effect to the assertion of current **HREF**.

COMB0 - "1" enables auto adjust mode, in this mode, internal exposure circuitry overwrites those parameters in registers [00]~[02], the chip adjusts the image based on a preset algorithm. "0" manual adjust mode.

Register 14- rw: Common control C

Bits	COMC7	COMC6	COMC5	COMC4	COMC3	COMC2	COMC1	COMC0
Default	0	-	0	0	0	1	-	-

COMC7 - AWB activation threshold selection: 1- high; 0-low.

COMC6 - Reserved.

COMC5 - QVGA digital output format selection. 1 - 320x240; 0 - 640x480.

COMC4 - Field/Frame vertical sync output in VSYNC port selection: 1 - frame sync, only inserted in ODD field vertical sync; 0 - field vertical sync, effect in Interlaced mode

COMC3 - HREF polarity selection: 0 - HREF positive effective, 1 - HREF negative.

COMC2 - RGB gamma selection: 1 - Gamma on, value defined by register [62] value; 0 - gamma is 1 (linear).

COMC1 - Reserved.

COMC0 - Reserved.

Register 15- rw: Common control D

Bits	COMD7	COMD6	COMD5	COMD4	COMD3	COMD2	COMD1	COMD0
Default	-	0	-	-	-	-	-	1

COMD7 - Reserved.

COMD6 - PCLK polarity selection. "0" OV7620 output data at PCLK falling edge and data bus will be stable at PCLK rising edge; "1" rising edge output data and stable at PCLK falling edge. When OV7620 work as CCIR656 format, COMB4=1, this bit is disable and should use PCLK rising edge latch data bus.

COMD5 - Reserved.

COMD4 - Reserved.

COMD3 - Reserved.

COMD2 - Reserved.

COMD1 - Reserved.

COMD0 - U V digital output sequence exchange control. 0 - V U V U ... for 16Bit, V Y U Y ... for 8 Bit; 1- U V U V ... for 16Bit and U Y V Y ... for 8 Bit.

Register 16 - rw: Frame Drop

Bits	FD7	FD6	FD5	FD4	FD3	FD2	FD1	FD0
Default	0	0	0	0	0	0	1	1

FD<7:2>- Frame drop selection, it operates in ODD and EVEN mode as defined by FD<1:0>, it is ignored in OFF & FRAME mode. Its purpose is to divide the video signal into programmed number of time slots in unit of field/frame, and to allow HREF to be active only one field/frame during the period. This function does not affect the video data or pixel rate.

000000 - 000001: disable digital data output, only output black reference level.

000010 - 111111: Output 1 of (2 ~ 63) frame. If set register 33 bit 1= 1, that means only drop 1 frame from (2 ~ 63) frame.

Interlaced:

FD<1:0>- field mode selection. Each frame consists of two fields: Odd & Even, these bits defines the assertion of HREF in relation to the two fields.

00 - OFF mode; HREF is not asserted in both fields, one exception is the single frame transfer operation (see the description for the register [13])

01 - ODD mode; HREF is asserted in odd field only.

10 - EVEN mode; HREF is asserted in even field only.

11 - FRAME mode; HREF is asserted in both odd field and even field. FD<7:2> useless (default).

Progressive Scan:

FD<1:0> - frame mode selection.

00 - OFF mode; HREF is not asserted in both fields, one exception is the single frame transfer operation (see the description for the register [13])

01,10 - SLOT mode; HREF is asserted in frame according FD<7:2>.

11 - FRAME mode; HREF is asserted in every frame. FD<7:2> useless

Register 17 - rw: Horizontal Window start

Bits	HS7	HS6	HS5	HS4	HS3	HS2	HS1	HS0
Default	0	0	1	0	1	1	1	1

HS<7:0> - selects the starting point of HREF window, each LSB represents four pixels for Interlaced/ Progressive full resolution mode, two pixels for QVGA resolution mode, this value is set based on an internal column counter, the default value corresponds to 640 horizontal window. Maximum window size is 664. see window description below. HS<7:0> programmable range is [2C]- [D2], and should less than HE<7:0>. HS<7:0> should be programmable to value larger than or equal to [2C]. Value larger than [D2] is invalid. See Figure 1.14.

Register 18 - rw: Horizontal Window end

Bits	HE7	HE6	HE5	HE4	HE3	HE2	HE1	HE0
Default	1	1	0	0	1	1	1	1

HE<7:0> - selects the ending point of HREF window, each LSB represents four pixels for full resolution and two pixels for QVGA resolution, this value is set based on an internal column counter, the default value corresponds to the last available pixel. The HE<7:0> programmable range is [2D] - [D2]. HE<7:0> should be larger than HS<7:0> and less than or equal to [D2]. Value larger than [D2] is invalid. See Figure 1.14.

Register 19- rw: Vertical Window start

Bits	VS7	VS6	VS5	VS4	VS3	VS2	VS1	VS0
Default	0	0	0	0	0	1	1	0

VS<7:0> - selects the starting row of vertical window, in full resolution mode, each LSB represents 1scan line in one field for Interlaced Mode, 2 scan line in one frame for Progressive Scan Mode. In QVGA resolution (set by register 14 bit 5), each LSB represents 1 scan line in one field for Interlaced Mode, 1scan line in one frame for Progressive Scan Mode. See Figure 1.14. Min. is [05], max. is [F6] and should less than VE<7:0>.

Register 1A- rw: Vertical Window end

Bits	VE7	VE6	VE5	VE4	VE3	VE2	VE1	VE0
Default	1	1	1	1	0	1	0	1

VE<7:0>- selects the ending row of vertical window, in full resolution mode, each LSB represents 1scan line in one field for Interlaced Mode, 2 scan line in one frame for Progressive Scan Mode. In QVGA resolution, each LSB represents 1 scan line in one field for Interlaced Mode, 1scan line in one frame for Progressive Scan Mode. See Figure 1.14. Min. is [05], max. is [F6] and should larger than VS<7:0>.

The window size calculate formula is as below:

1. Horizontal size:
VGA mode: Horizontal window size = (Register [18] - Register [17])*4.
QVGA mode: Horizontal window size = (Register [18] - Register [17])*2.
2. Vertical size: VGA mode:
Vertical window size = (Register [1A]- Register [19]+1);
QVGA mode: Horizontal window size = (Register [1A] - Register [19]+ 1).

Register 1B- rw: Pixel shift

Bits	PS7	PS6	PS5	PS4	PS3	PS2	PS1	PS0
Default	0	0	0	0	0	0	0	0

PS<7:0> - to provide a way to fine tune the output timing of the pixel data relative to that of HREF, it physically shifts the video data output time early or late in unit of pixel clock as shown in the figure below. This function is different from changing the size of the window as is defined by HS<7:0> & HE<7:0> in register [17] and [18].

Register 1C- r: Manufacture ID high byte

Bits	MIDH7	MIDH6	MIDH5	MIDH4	MIDH3	MIDH2	MIDH1	MIDH0
Default	0	1	1	1	1	1	1	1

MIDH<7:0> - read only, always returns "7F".

Register 1D- r: Manufacture ID low byte

Bits	MIDL7	MIDL6	MIDL5	MIDL4	MIDL3	MIDL2	MIDL1	MIDL0
Default	1	0	1	0	0	0	1	0

MIDL<7:0>- read only, always returns "A2"

Register 1E ~ 1F- rw: Reserved

These two registers are reserved for internal use. Write data to these registers will not function.

Register 20- rw: Common control E

Bits	COME7	COME6	COME5	COME4	COME3	COME2	COME1	COME0
Default	0	0	0	0	0	-	0	0

COME7 - Modified CCIR656 format vertical sizing enabled. "1" will enable vertical windowing function. "0" will limit vertical size to 480 lines - unchanged by [19] and [1B].

COME6 - Field luminance average signal generation enable. Value is stored in register [7C]

COME5 - "1" First stage aperture correction enable. Correction strength will be decided by register [07]. "0" disable first stage aperture correction.

COME4 - "1" Second stage aperture correction enable. Correction strength and threshold value will be decided by register 26 bit 7 ~ register 26 bit 4.

COME3 - AWB smart mode enable. 1 - Drop out pixel when compare pixel red, blue and green component level to change register [01] and [02], which luminance level is higher than presetting level and lower than presetting level, this two level is set by register [0F]. 0 - calculate all pixels to get AWB result. Valid only when register 13 bit 0=1 and register 12 bit 2=1

COME2 - Reserved.

COME1 - AWB fast/slow mode selection. "1" - AWB is always fast mode, that is register [01] and [02] is changed every field/frame. "0" AWB is slow mode, [01] and [02] change every 16/64 field/ frame decided by register 70 bit 1. When AWB enable, register 12 bit 2=1, AWB is working as fast mode at first 1024 field/frame, than as slow mode later.

COME0 - Digital output driver capability increase selection: "1" Double digital output driver current; "0" low output driver current status.

Register 21- rw: Y Channel Offset Adjustment

Bits	Y7	Y6	Y5	Y4	Y3	Y2	Y1	Y0
Default	1	0	0	0	0	0	0	0

Y<6:0>- Y channel digital output offset adjustment. Range: +127mV ~ -127mV. If COMG2=0, this register will be updated by internal auto A/D BLC circuit, and write a value to this register with SCCB has no effect. If COMG2=1, Y channel offset adjustment will use the register stored value which can be changed by SCCB. If COMF1=0, this register has no adjustment effect to A/D output data. If output RGB raw data, this register will adjust R/G/B data.

Y7: Offset adjustment direction 0 - Add Y[6:0]; 1 - Subtract Y[6:0].

Register 22- rw: U Channel Offset Adjustment

Bits	U7	U6	U5	U4	U3	U2	U1	U0
Default	1	0	0	0	0	0	0	0

U<6:0>- U channel digital output offset adjustment. Range: +128mV ~ -128mV. If register 27 bit 2=0, this register will be updated by internal auto A/D BLC circuit, and write a value to this register with SCCB has no effect. If register 27 bit 2=1, U channel offset adjustment will use the register stored value which can be changed by SCCB. If register 26 bit 1=1, this register has no effect to A/D output data. If output RGB raw data, this register will adjust R/G/B data.

U7: Offset adjustment direction: 0 - Add U[6:0]; 1 -Subtract U[6:0].

If register 2D bit 0 = 0, this register has no function.

Register 23- rw: Crystal Current control.

Bits	CC7	CC6	CC5	CC4	CC3	CC2	CC1	CC0
Default	0	0	0	-	-	-	-	-

CC<7:6>- Crystal amplifier current gain. (00) maximum current; (11) minimum current
 CC<5:0>- Reserved

Register 24- rw: AEC Auto Exposure White Pixel Ratio

Bits	AEW7	AEW6	AEW5	AEW4	AEW3	AEW2	AEW1	AEW0
Interlace	0	0	0	0	1	0	0	0
Progressive	0	0	0	1	0	0	0	0

Registers 24 and 25 together control the AEC target values for image brightness.

For a brighter image, increase register 24 and decrease register 25.

For a darker image, decrease register 24 and decrease register 25.

AEW<7:0>- used to calculate the white pixel ratio. OV7620 AEC algorithm counts the whole field/frame white pixel (its luminance level is higher than a fixed level) and black pixel (its luminance level is lower than a fixed level) number. When white/black pixel ratio is same as the ratio defined by registers [25] and [26], image stable. This register is used to define the white pixel ratio, default is 25%, each LSB represent step: Interlaced: 1.3%; Progressive Scan: 0.7%. Change range is: Interlaced: [01] ~ [4A]; Progressive Scan: [01] ~ [96]. Increase AEW<7:0> will increase the white pixel ratio. For same light condition, the image brightness will increase if AEW<7:0> increase.

Note: AEW<7:0> must combined with register [26] AEB<7:0>. Keep the relation always true: AEW<7:0> + AEB<7:0> > [4A] for Interlaced; AEW<7:0> + AEB<7:0> > [90].

Register 25- rw: AEC Auto Exposure Black Pixel Ratio

Bits	AEB7	AEB6	AEB5	AEB4	AEB3	AEB2	AEB1	AEB0
Interlace	0	1	0	0	1	0	1	0
Progressive	1	0	0	0	1	0	1	0

AEB<7:0>- used to calculate the black pixel ratio. OV7620 AEC algorithm is count whole field/frame white pixel (its luminance level is higher than a fixed level) and black pixel (its luminance level is lower than a fixed level) number. When white/black pixel ratio is same as the ratio defined by registers [25] and [26], image stable. This register is used to define black pixel ratio, default is 75%, each LSB represent step: Interlaced: 1.3%; Progressive Scan: 0.7%. Change range is: Interlaced: [01] ~ [4A]; Progressive Scan: [01] ~ [96]. Increase AEB<7:0> will increase black pixel ratio. For same light condition, the image brightness will decrease if AEB<7:0> increase.

Note: AEB<7:0> must combined with register [25] AEW<7:0>. Keep the relation always true: AEW<7:0> + AEB<7:0> > [4D] for Interlaced; AEW<7:0> + AEB<7:0> > [90].

Register 26 - rw: Common control F

Bits	COMF7	COMF6	COMF5	COMF4	COMF3	COMF2	COMF1	COMF0
Default	1	0	1	0	0	0	1	0

COMF<7:6>- Digital Sharpness threshold selection.

[00] - Difference of neighbor pixel luminance is larger than 8 mV, correction on.

[01] - 16 mV.

[10] - 32 mV.

[11] - 64 mV.

COMF<5:4>- Digital Sharpness Magnitude selection.

[01] - Strength is 50% of difference of neighbor pixel luminance.

[10] - 100%.

[11] - 200%.

COMF3 - Reserved

COMF2 - Swap bus MSB/LSB. "1" LSB->Bit7, MSB->Bit0; "0" normal.

COMF1 - "1" A/D Black level calibration enable. Do not use "0".

COMF0 - "1" Output first 4 line black level for Interlaced Mode and 8 line black level for Progressive Scan Mode before valid data output. HREF number will increase 4/8 relatively. "0" no black level output.

Register 27 - rw: Common control G

Bits	COMG7	COMG6	COMG5	COMG4	COMG3	COMG2	COMG1	COMG0
Default	1	1	1	0	0	0	1	0

COMG7: Reserved.

COMG6: Reserved.

COMG5: Reserved.

COMG4: RGB matrix disable. "1" - Bypass RGB matrix. "0" - Enable RGB matrix.

COMG3: Reserved.

COMG2: "1" Enables manual adjustment of A/D offset: 1 - A/D data will add or subtract a value defined by registers [21] and [22]. 0 - A/D data will be shifted by a value defined by registers [21], [22] and [2E], which is updated by internal circuit.

COMG1: - Disables CCIR range clip.

COMG0: - Special interface for external micro-controller and RAM timing control. See timing chart.

Register 28 - rw: Common control H

Bits	COMH7	COMH6	COMH5	COMH4	COMH3	COMH2	COMH1	COMH0
Default	1	1	1	0	0	0	1	0

COMH7: - "1" selects One-Line RGB raw data output format, "0" selects normal dual-line (repetitive) raw data output, effective only in Progressive Scan mode.

COMH6: - "1" enable Black/White mode.

COMH5: - "1" select Progressive Scan mode; "0" select Interlaced mode.

COMH4: - Freeze AEC/AGC value - current values retained. This is effective only when register 13 bit 0=1.

COMH3: - AGC disable.

COMH2: - Raw data output format: "1" - Green on Y channel, B R B R....on UV channel (GRB422), "0" - G R G R.... on Y channel, B G B G..... on UV channel.

COMH1: - 2x Gain boost. "1" Double PreAmp gain to 6dB. "0" PreAmp gain is 0dB.

COMH0: - Reserved.

Register 29 - rw: Common control I

Bits	COMI7	COMI6	COMI5	COMI4	COMI3	COMI2	COMI1	COMI0
Default	0	0	0	0	0	0	0	0

COMI7: - AEC disable. "1" If register 13 bit 0=1, AEC stop and register [10] value will be held at last AEC value and not be updated by internal circuit. "0" - if register 13 bit 0=1, register [10] value will be updated by internal circuit

COMI6: - Enable slave sync mode selection. "1" slave mode, use external CHSYNC and VSYNC. "0" master mode

COMI<5:4> - Reserved.

COMI3: - Central weighted exposure control.

COMI2: - Reserved.

COMI1 - COMI0: Version flag.

Register [2A] - rw: Frame Rate Adjust Register 1

Bits	EHS7	EHS6	EHS5	EHS4	EHS3	EHS2	EHS1	EHS0
Default	0	0	0	0	0	0	0	0

EHS7 - Frame Rate adjustment enable bit. "1" Enable.

EHS<6:5> - Highest 2 bit of frame rate adjust control byte. See explanation in register [2B].

EHS4 - "1" - UV component delay 2 pixel. "0" no 2*Tp delay.

EHS3 - Y channel brightness adjustment enable. When COMF2=1 active.

EHS2 - For QVGA raw data format. "1" will force Y to output B G B G and UV to output G R G R

EHS<1:0> - Reserved.

Register [2B] - rw: Frame Rate Adjust Register 2

Bits	EHSL7	EHSL6	EHSL5	EHSL4	EHSL3	EHSL2	EHSL1	EHSL0
Default	0	0	0	0	0	0	0	0

EHSL<7:0> - Lowest 8 bit of frame rate adjust control byte. Frame rate adjustment resolution is 0.12%. Control word is 10 bit. Every count decreases frame rate by 0.12%. Range is 0.12% - 112%. If frame rate adjustment is enabled, COME7 must be set to "0".

Register [2C] - rw: Black Expanding Register

Bits	EXBK7	EXBK6	EXBK5	EXBK4	EXBK3	EXBK2	EXBK1	EXBK0
Default	1	0	0	0	1	0	0	0

EXBK<7:4> - Coarse Auto Black Level adjustment. Range is 0.08% - 1.3%

EXBK<3:0> - Fine Auto Black Level adjustment. Range is 0.08% - 1.3%.

Register [2D] - rw: Common Control J

Bits	COMJ7	COMJ6	COMJ5	COMJ4	COMJ3	COMJ2	COMJ1	COMJ0
Default	1	0	0	0	0	0	-	1

COMJ7 - Reserved. Always set to "1".

COMJ6 - QVGA 60 frame/s selection. "1" Only Odd field in Interlace Mode data output, "0" Odd/Even field data output frame rate is 30 frames/s. VGA is output at 60 frames/s in dual line mode raw data.

COMJ5 - Reserved. Always set to "0".

COMJ4 - Auto brightness enabled.

COMJ3 - Reserved. Always set to "0".

COMJ2 - Banding filter enable. After adjust frame rate to match indoor light frequency, this bit enable a different exposure algorithm to cut light band induced by fluorescent light.

COMJ1 - Reserved. Always set to "0".

COMJ0 - Reserved. Always set to "1".

Register [2E]- rw: V Channel Offset Adjustment

Bits	V7	V6	V5	V4	V3	V2	V1	V0
Default	1	0	0	0	0	0	0	0

V7-V0: V channel digital output offset adjustment. Range: +128mV ~ -128mV. If COMG2=0, this register will be updated by internal auto A/D BLC circuit, and write a value to this register with SCCB has no effect. If COMG2=1, V channel offset adjustment will use the register stored value which can be changed by SCCB. If COMF1=1, this register has no effect to A/D output data. If output raw data, this register will adjust R/G/B data.

V7: Offset adjustment direction: 0 - Add V[6:0]; 1-Substrate V[6:0].

If COMJ0 = 0, this register value is common to U and V channel.

Register [2F] THRU [5F] - w: Reserved

Address [2F] - [5F] are reserved for internal use.

Register 60- rw: Signal Process Control A

Bits	SPCA7	SPCA6	SPCA5	SPCA4	SPCA3	SPCA2	SPCA1	SPCA0
Default	0	0	1	0	0	1	1	1

SPCA7: 1.5x gain boost.

SPCA6: Reserved.

SPCA5: "1" disables green averaging for UV channel.

SPCA4: "1" disables green averaging for luminance channel.

SPCA<3:2> Reserved.

SPCA<1:0>: Reserved. Color set to "0111"; B&W set to "0000".

Register 61- rw: Signal Process Control B

Bits	SPCB7	SPCB6	SPCB5	SPCB4	SPCB3	SPCB2	SPCB1	SPCB0
Default	1	0	0	0	0	0	1	0

SPCB7: "1" YUV mode; "0" raw data mode.

SPCB6: Reserved. Always set to "0".

SPCB5: Reserved. Always set to "0".

SPCB4: Reserved. Always set to "0".

SPCB3: Reserved. Always set to "0".

SPCB2: Limits range of register [6] to half value.

SPCB<1:0>: Auto Brightness target reference level: (00) -- 0 IRE; (01) -- 6 IRE; (10) -- 10 IRE; (11) -- 20 IRE.

Register 62- rw: RGB Gamma Control

Bits	RGM7	RGM6	RGM5	RGM4	RGM3	RGM2	RGM1	RGM0
Default	0	0	0	1	0	0	1	1

RGM<7:1> raw data or UV gamma curve selection.

RGM0: Reserved. Always set to "0".

Register 64- rw: Y Gamma Control

Bits	YGM7	YGM6	YGM5	YGM4	YGM3	YGM2	YGM1	YGM0
Default	0	1	0	1	1	0	0	1

YGM<7:1>: Y gamma curve selection.

YGM<0>: "1" enable; "0" disable (linear).

Register 65- rw: Signal Process Control C

Bits	SPCC7	SPCC6	SPCC5	SPCC4	SPCC3	SPCC2	SPCC1	SPCC0
Default	0	1	0	0	0	0	1	0

SPCC<7:3> Reserved.

SPCC2: A/D mode selection. Increase A/D range by 1.5X

SPCC<1:0>: A/D reference selection. <00>: input signal range 0.9V; <01>: 1.0V peak <10>: 1.15V peak; <11>: 1.26V peak. Do not use <00> selection.

Register 66- rw: AWB Process Control

Bits	AWBC7	AWBC6	AWBC5	AWBC4	AWBC3	AWBC2	AWBC1	AWBC0
Default	0	1	0	1	0	1	0	1

White balance limiting function - YUV matrix control. Register 74:7 must be enabled for AWB process control.

AWBC<7:6>: Smart AWB ignores RGB raw data pixel values above (00):70%, (01): 80%, (10): 90%, (11):100%.

AWBC<5:4>: Smart AWB ignores RGB raw data pixel values below (00):10%, (01) 20%, (10) 30%, (11) 40%.

AWBC<3:2>: U threshold level selection if use U/V as white balance feedback 00: (-10% ~ 10%); 01: (-20% ~ 20%); 10: (-30% ~ 30%); 11: (-40% ~ 40%)

AWBC<1:0>: V threshold level selection if use U/V as white balance feedback 00: (-10% ~ 10%); 01: (-20% ~ 20%); 10: (-30% ~ 30%); 11: (-40% ~ 40%)

Register 67- rw: Color Space Selection

Bits	YUV7	YUV6	YUV5	YUV4	YUV3	YUV2	YUV1	YUV0
Default	0	0	0	1	1	0	1	0

YUV<7:6>: UV coefficient selection (U/V is output and u/v is input)

- [00]: YUV
- [01]: Analog YUV
- [10]: CCIR 601 YCrCb
- [11]: PAL YUV

YUV5: U/V signal delay 2 pixel selection

YUV4: U/V signal with 3 point chroma average(2 pixel delay accordingly)

YUV<3:2>: Y signal delay selection: (00) - 0; (01) - 1; (10) - 2; (11) - 3 pixels

YUV1: Auto saturation control (decreases color noise) enable.

YUV0: Auto saturation control range selection: 0 - 1.5x; 1 - 1x.

Register 68- rw: Signal Process Control D

Bits	SPCD7	SPCD6	SPCD5	SPCD4	SPCD3	SPCD2	SPCD1	SPCD0
Default	1	1	0	0	1	1	0	0

SPCD<7:5>: AEC/AGC Brightness Target level selection. 000 - 10%; 001 - 30%; 010 - 50%; 011 - 70%; 100 - 80%; 101 - 90%; 110 - 100%; 111 - 110%.

SPCD4: Reserved. Always set to "0".

SPCD<3:2>: Anti-alias threshold: 11 lowest threshold; 01, 10 midrange threshold; 00 highest threshold.

SPCD<1:0>: Anti-alias magnitude: 00 - low strength; 01, 10 mid strength; 11:high strength.

Register 69- rw: Analog Sharpness

Bits	EDGE7	EDGE6	EDGE5	EDGE4	EDGE3	EDGE2	EDGE1	EDGE0
Default	0	1	1	1	0	0	1	0

EDGE<7:3> Reserved.

EDGE2: Vertical Edge Enhancement enable. Register 20:5 must be set to "1".

EDGE<1:0>: Reserved.

Register 6A- rw: Vertical Edge Enhancement Control

Bits	VEG7	VEG6	VEG5	VEG4	VEG3	VEG2	VEG1	VEG0
Default	-	1	0	0	0	0	1	0

VEG<6:4>: Vertical Edge Enhancement threshold range

VEG<3:0>: Vertical Edge Enhancement magnitude value. 0000: weakest; 1111: strongest.

Register [6B], [6C], [6D], [6E] rw: Reserved

Address [6B] - [6E] are reserved for internal use.

Register 6F - rw: Even/Odd Noise Compensation Control

Bits	EOC7	EOC6	EOC5	EOC4	EOC3	EOC2	EOC1	EOC0
Default	-	-	1	1	1	0	1	0

EOC7: Reserved.

EOC6: Reserved.

EOC<5:4>: Color Kill luminance threshold selection: 00 - none; 01 - 2.6v; 10 - 2.4v; 11 - 2.3v. Lower luminance selection will activate color kill.

EOC<3:0>: Set to factory recommended values.

Register 70 - rw: Common Control K

Bits	COMK7	COMK6	COMK5	COMK4	COMK3	COMK2	COMK1	COMK0
Default	1	0	-	0	0	-	0	1

COMK7 - "1" HREF edges coincident (no delay) with PCLK negative/falling edges (COMD6 must be set to "0"). "0" HREF edge occurs 10ns after PCLK positive/rising edge.

COMK6 - Output port drive current additional 2x control bit.

COMK5 - Reserved.

COMK4 - Selects ZV port timing. "1" VSYNC output ZV port vertical sync signal. "0" normal TV vertical sync signal.

COMK3 - Accelerated saturation mode for camera mode change. (QVGA, 8 Bit output, CCIR 656 mode and Progressive Scan Mode). After relative control bit set, the first VS will be the stable image with suitable AEC/AWB setting. "0" - slow mode, after mode change need more field/frame to get stable AEC/AWB setting image.

COMK2 - Reserved.

COMK1 - AWB update rate selection. "1" fast mode; "0" slow mode.

COMK0 - Set to "1" in single line mode, otherwise set to "0" and set COMG4 to disable.

Register 71 - rw: Common Control J

Bits	COML7	COML6	COML5	COML4	COML3	COML2	COML1	COML0
Default	0	0	0	0	0	0	0	0

COML7 - Auto Brightness update rate: "1" - Slow mode; "0" - fast mode.

COML6 - Gated PCLK selection. "1" - Enables PCLK gated by HREF; "0" - PCLK is free running clock

COML5 - Swap HREF output pin with CHSYNC. "1" - HREF pin output CHSYNC signal; "0" - No swap.

COML4 - Swap CHSYNC output pin with HREF. "1" - CHSYNC pin output HREF signal; "0" - normal output.

COML<3:2>- Highest 2 bit for HSYNC rising edge shift control, combined with register [72]

COML<1:0>- Highest 2 bit for HSYNC falling edge shift control, combined with register [73]

Register 72- rw: Horizontal Sync 1st Edge shifting

Bits	HSDY7	HSDY6	HSDY5	HSDY4	HSDY3	HSDY2	HSDY1	HSDY0
Default	0	0	0	1	0	1	0	0

HSDY<7:0> - Lower 8 bit control for shifting horizontal sync CHSYNC first edge. Range is [000] - [3FF].

Every count equals 1 PCLK.

Register 73 - rw: Horizontal Sync 2nd Edge shifting

Bits	HEDY7	HEDY6	HEDY5	HEDY4	HEDY3	HEDY2	HEDY1	HEDY0
Default	0	1	0	1	0	1	0	0

HEDY<7:0> - Lower 8 bit control for shifting horizontal sync CHSYNC second edge. Range is [000] - [3FF].

Every count equals 1 PCLK.

Register 74 - rw: Common Control M

Bits	COMM7	COMM6	COMM5	COMM4	COMM3	COMM2	COMM1	COMM0
Default	0	0	1	0	0	0	0	0

COMM7 - Enable UV Smart AWB threshold control.

COMM<6:5> - AGC maximum gain selection: 00 - 2x; 01 - 4x; 10 - 2x; 11 - 8x

COMM<4:0> - Reserved.

Register 75 - rw: Common Control N

Bits	COMN7	COMN6	COMN5	COMN4	COMN3	COMN2	COMN1	COMN0
Default	1	0	0	0	0	0	1	0

COMN7 - "1" enables Auto brightness range limit. Minimum will be [40]. Otherwise will be [00] ~ [FF].

COMN<6:3> - Reserved.

COMN2 - This bit further reduces the exposure time to 1/120 second or 1/100 second when the banding filter is enabled and the light is too strong.

COMN1- If enabled, manual write white balance value, then change to auto, the stable time will be less.

Speeds white balance stable time when switching from manual to AWB.

COMN0 - Enables addition of 2 pixel averaging.

Register 76 - rw: Common Control O

Bits	COMO7	COMO6	COMO5	COMO4	COMO3	COMO2	COMO1	COMO0
Default	0	0	0	0	0	0	0	0

COMO7 - Output XCLK from **FODD** pin.

COMO6 - Reserved

COMO5 - Software power down enable: 1 - enable; 0 - wake up

COMO4 - Reserved

COMO3 - Limits the Minimum Exposure time to 4 lines rather 1 line with AEC enable

COMO2 - Tri-state sync and CLK output, except data line

COMO<1:0> - Reserved

Register [77], [78], [79], [7A], [7B] - rw: Reserved

Address [2F] - [5F] are reserved for internal use.

Register 7C - rw: Field Average Level Storage

Bits	AVG7	AVG6	AVG5	AVG4	AVG3	AVG2	AVG1	AVG0
Default	0	0	0	0	0	0	0	0

AVG<7:0> -- Storage field luminance average value if register 20 bit 6=1.

Notice: for QVGA and Progressive Scan mode, the real luminance average value is double of this register value, other mode is same. If set to RGB raw data mode, the value is Green component average value.

Example of switching into YCrCb mode with White Balance off:

```
:CR 18 32
ACK
:
```

* indicates the default state



See page 24 for information on slave mode.

CT boolean \r

This command toggles the **C**amera **T**ype while the camera is in slave mode. Since the CMUcam2 can not determine the type of the camera without communicating with the module, it is not possible for it to auto-detect the camera type in slave mode. A value of 0, sets the CMUcam2 into ov6620 mode. A value of 1 sets it into ov7620 mode. The default slave mode startup value assumes the ov6620.



See LF and FD on page 52 and page 49.

DC value \r

This command sets the **C**hannel that is used for frame **D**ifferencing commands. A value of 0, sets the frame differencing commands LF and FD to use the red (Cr) channel. A value of 1 (default) sets them to use the green (Y) channel, and 2 sets them to use the blue (Cb) channel.

DM value \r

This command sets the **D**elay **M**ode which controls the delay between characters that are transmitted over the serial port. This can give slower processors the time they need to handle serial data. The value should be set between 0 and 255. A value of 0 (default) has no delay and 255 sets the maximum delay. Each delay unit is equal to the transfer time of one bit at the current baud rate.

DS x_factor y_factor \r

This command allows **D**own **S**ampling of the image being processed. An x_factor of 1, (default), means that there is no change in horizontal resolution. An x_factor of 2, means that the horizontal resolution is effectively halved. So all commands, like send frame and track color, will operate at this lower down sampled resolution. This gives you some speed increase and reduces the amount of data sent in the send frame and bitmap linemodes without clipping the image like virtual windowing would. Similarly, the y_factor independently controls the vertical resolution. (Increasing the y_factor down sampling gives more of a speed increase than changing the x_factor.) The virtual window is reset to the full size whenever this command is called.

Example of down sampling the resolution by a factor of 2 on both the horizontal and vertical dimension:

```
:DS 2 2
ACK
:GM
ACK
S 89 90 67 5 6 3
S 89 91 67 5 6 2
```



See LF on page 52 to load a new baseline frame to difference from.

FD threshold \r

This command calls **Frame Differencing** against the last loaded frame using the **LF** command. It returns a type T packet containing the middle mass, bounding box, pixel count and confidence of any change since the previously loaded frame. It does this by calculating the average color intensity of an 8x8 grid of 64 regions on the image and comparing those plus or minus the user assigned *threshold*. So the larger the threshold, the less sensitive the camera will be towards differences in the image. Usually values between 5 and 20 yield good results. (In high resolution mode a 16x16 grid is used with 256 regions.).



See SF on page 60.

FS boolean \r

This command sets the **Frame Streaming** mode of the camera. A value of 1, enables frame streaming, while a 0 (default) disables it. When frame streaming is active, a send frame command will continuously send frames back to back out the serial connection. See SF on page 60.



See Demo Mode on page 18.

GB \r

This command **Gets a Button** press if one has been detected. This command returns either a 1 or a 0. If a 1 is returned, this means that the button was pressed sometime since the last call to Get Button. If a 0 is returned, then no button press was detected.



See HC and HT Commands on pages 51 and 52.

GH <channel> \r

This command **Gets a Histogram** of the *channel* specified by the user. The histogram contains 28 bins each holding the number of pixels that occurred within that bin's range of color values. So bin 0 on channel 0 would contain the number of red pixels that were between 16 and 23 in value. If no arguments are given, get histogram uses the last channel passed to get histogram. If get histogram is first called with no arguments, the green channel is used. The value returned in each bin is the number of pixels in that bin divided by the total number of pixels times 256 and capped at 255.

GI \r

This command **Gets the auxiliary I/O Input** values. When get inputs is called, a byte is returned containing the values of the auxiliary IO pins. This can be used to read digital inputs connected to the auxiliary I/O port. The aux I/O pins are internally lightly pulled high. See page 24 for pin numbering. Note that the pins are pulled up internally by the processor.

Example of how to read the auxiliary I/O pins, (in this case, pins 0 and 1 are high, while pins 2 and 3 are low).

```
: GI
3
ACK
:
```

GM \r

This command will **G**et the **M**ean color value in the current image. If, optionally, a sub region of the image is selected via virtual windowing, this function will only operate on the selected region. The mean values will be between 16 and 240 due to the limits of each color channel on the CMOS camera. It will also return a measure of the average absolute deviation of color found in that region. The mean together with the deviation can be a useful tool for automated tracking or detecting change in a scene. In YCrCb mode RGB maps to CrYCb. This command returns a Type S data packet that by default has the following parameters:

```
S Rmean Gmean Bmean Rdeviation Gdeviation Bdeviation\r
```

Example of how to grab the mean color of the entire window:

```
:SW 1 1 40 143
ACK
:GM
ACK
S 89 90 67 5 6 3
S 89 91 67 5 6 2
```

GS servo \r

This command will **G**et the last position that was sent to the **S**ervos.

Example of how to use get servo:

```
:GS 1
ACK
128
:
```

GT \r

This command **G**ets the current **T**rack color values. This is a useful way to see what color values track window is using.

This example shows how to get the current tracking values:

```
T 12 34 ....
:GT
ACK
200 16 16 240 20 20
:
```



See page 57 to see how the OM command can create a custom S Packet.



See SV command on page 62.

GV \r

This command **G**ets the current **V**ersion of the firmware and camera module version from the camera. It returns an ACK followed by the firmware version string. c6 means that it detects an OV6620, while c7 means that it detected an OV7620.

Example of how to ask for the firmware version and camera type:

```
:GV
ACK
CMUcam2 v1.00 c6
:TW
ACK
```

GW \r

This command **G**ets the current virtual **W**indowing values. This command allows you to confirm your current window configuration. It returns the x1, y1, x2 and y2 values that bound the current window.

HC #_of_bins scale \r

This command lets you **C**onfigure the **H**istogram settings. The first parameter takes one of three possible values. A value of 0 (default) will cause GH to output 28 bins. A value of 1 will generate 14 bins and a value of 2 will generate 7 bins. The scale parameter (default 0) allows you to better examine bins with smaller counts. Bin values are scaled by 2^{scale} where scale is the second parameter of the command.

#_of_bins

Input	Bins
0	28
1	14
2	7

HD boolean \r

This command enables or disables **H**iRes frame **D**ifferencing. A value of 0 (default) disables the high resolution frame differencing mode, while a value of 1 enables it. When enabled, frame differencing will operate at 16x16 instead of 8x8. The captured image is still stored internally at 8x8. The extra resolution is achieved by doing 4 smaller comparisons against each internally stored pixel. This will only yield good results when the background image is relatively smooth, or has a uniform color.

HR state \r

This sets the camera into **H**iRes mode. This is only available using the OV6620 camera module. A *state* value of 0 (default) gives you the standard 88x143, while 1 gives you 176x287. HiRes mode truncates the image to 176x255 for tracking so that the value does not overflow 8 bits.



See GH on page 49 to see how to get histograms.



See LF and FD on pages 52 and 49 to see how to use the more basic frame Differencing commands.



See GH on page 49 to see how to get a histogram.

HT boolean \r

This command enables or disables **Histogram Tracking**. When histogram tracking is enabled, only values that are within the color tracking bounds will be displayed in the histograms. This allows you to select exact color ranges giving you more detail, and ignoring any other background influences. A value of 0 (default) will disable histogram tracking, while a value of 1 will enable it. Note that the tracking noise filter applies just like it does with the TC and TW commands.

L1 boolean \r

L0 boolean \r

These commands enable and disable the two tracking **LEDs**. A value of 0 will turn the LED off, while a value of 1 will turn it on. A value of 2 (default) will leave the LED in automatic mode. In this mode, LED 1 turns on when the camera confidently detects an object while tracking and provides feedback during a send frame. In automatic mode, LED 0 does nothing, so it can be manually set.



See FD on page 49.

LF \r

This command **L**oads a new **F**rame into the processor's memory to be differenced from. This does not have anything to do with the camera's frame buffer. It simply loads a baseline image for motion differencing and motion tracking.

LM type mode \r

This command enables **Line Mode**, which transmits more detailed data about the image. It adds prefix data onto either **T** or **S** packets. This mode is intended for users who wish to do more complex image processing on less reduced data. Due to the higher rate of data, this may not be suitable for many slower micro controllers. These are the different types and modes that line mode applies to different processing functions:

Type	Mode	Effectuated Command	Description
0	0	TC TW	Default where line mode is disabled
0	1	TC TW	Sends a binary image of the pixels being tracked
0	2	TC TW	Sends the Mean, Min, Max, confidence and count for every horizontal line of the tracked image.
1	0	GM	Default where line mode is disabled
1	1	GM	Sends the mean values for every line in the image
1	2	GM	Sends the mean values and the deviations for every line being tracked in the image
2	0	FD	Default where line mode is disabled
2	1	FD	Returns a bitmap of tracked pixels much like type 0 mode 0 of track color
2	2	FD	Sends the difference between the current image pixel value and the stored image. This gives you delta frame differenced images.
2	3	LF FD	This gives you the actual averaged value for each element in a differenced frame. It also returns these values when you load in a new frame. This can be used to give a very high speed gray scale low resolution stream of images.

Note, that the "mode" of each "type" of linemode can be controlled independently.

Line Mode Type 0: Track Color

Mode 1: Bitmap of tracked region.



See TC on page 63.

When the linemode type is 0 and the mode is set to 1, TC or TW will send a binary bitmap of the image as it is being processed. It will start this bitmap with an 0xAA flag value (hex value AA not in human readable form) followed by the Xsize and Ysize of the binary image. The value 0xAA will not occur in the data stream. This is followed by bytes each of which contains the binary value of 8 pixels being streamed from the top-left to the bottom-right of the image. The bits for each row are padded with zeros to fill an integral number of bytes. The binary bitmap is terminated by two 0xAA's. This is then followed by the normally expected standard T data packet.

Example of TC with line mode on:

```
:LM 0 1
ACK
:TC
ACK
(raw data: AA Xsize Ysize XX XX .... XX XX XX AAAA) T 55 90 45 72 65 106 18 51
(raw data: AA Xsize Ysize XX XX .... XX XX XX AAAA) T 55 90 46 72 65 106 18 52
```

Mode 2: Per row statistics in the tracked region.



See OM on page 57 to see how to mask these line mode data packets.

When the linemode type is 0 and the mode is set to 2, TC or TW will send various statistics about each row that is being tracked. It sends the minimum x value, the maximum x value, the average x value, the count of tracked pixels on that line and the confidence. This can be especially useful for line following applications since you can essentially get a trace of the middle of the line. Like other linemode options, this new data is sent as a prefixed packet. The packet starts with an 0xFE, followed by the number of rows (the y-size) that it will send. The packet will then contain, the xLineMean, xLine-Min, xLineMax, line pixel count, and line confidence for each row. These will all be sent as raw values. The packet terminates with a 0xFD followed by a normal T packet.

0xFE y-size xLineMean xLineMin xLineMax LineCount Conf ... 0xFD Tpacket

Line Mode Type 1: Get Mean

Mode 1: Per line statistics.



See GM on page 50.

When the linemode type is 1 and the mode is set to 1, GM will send a raw (not human readable) mean value of every line being processed. These packets start with an 0xFE. The data is sent in the following raw format rLineMean, gLineMean, bLineMean, and terminate with an 0xFD.

0xFE Rmean Gmean Bmean ... 0xFD Mpacket

Example of GM with line mode on

```
:LM 1 1
ACK
:GM
ACK
(raw data: FE XX XX XX ... XX XX XX FD) M 45 56 34 10 15 8
(raw data: FE XX XX XX ... XX XX XX FD) M 45 56 34 10 15 8
```



See OM on page 57 to see how to mask these line mode data packets.

Mode 2: More per line statistics.

When the linemode type is 1 and the mode is set to 2, GM will send a raw (not human readable) mean value and deviation for every line being processed. These packets are started with an 0xFE. The data is sent in the following raw format rLineMean, gLineMean, bLineMean, rDeviation, gDeviation, bDeviation and terminate with an 0xFD.

0xFE Rmean Gmean Bmean Rdev Gdev Bdev ... 0xFD Mpacket

Line Mode Type 2: Frame Differencing

Mode 1: Bitmap for tracked pixels.

When the linemode type is 2 and the mode is set to 1, FD will send a binary bitmap of the image as it is being processed. It will start this bitmap with an 0xAA flag value (hex value AA not in human readable form) followed by the Xsize and Ysize of the binary image. The value 0xAA will not occur in the data stream. This is followed by bytes each of which contains the binary value of 8 (or 16) pixels being streamed from the top-left to the bottom-right of the image. The binary bitmap is terminated by two 0xAA's. This is then followed by the normally expected standard **T** data packet.

Example of TC with line mode on:

```
:LM 2 1
ACK
:FD 10
ACK
(raw data: AA XX XX XX ... XX XX XX AA AA) T 5 10 4 9 8 100
(raw data: AA XX XX XX ... XX XX XX AA AA) T 5 10 4 9 8 100
```



See FD on page 49.

Mode 2: Deltas between reference frame.

When the linemode type is 2 and the mode is set to 2, FD will send the values of the differences between the current image and the original saved frame. The packet starts with 0xFC followed by the xSize and ySize of the image buffer that is to be sent. A single value for each pixel is transmitted and the packet ends with an 0xFD. The delta values are capped at +/- 112 with 128 added to the delta, so 128 means zero difference. This forces the values to remain in the 16-240 range.

Mode 3: Deltas between reference frame When the linemode type is 2 and the mode is set to 3, LF and FD will send a binary bitmap of the internally stored image that they are operating on. This image is stored in the same format as mode 2 of frame differencing.



See FD on page 49.

```
:LM 2 2
ACK
:FD 10
ACK
(raw data: FC xSize ySize XX XX XX ... XX XX XX FD)
(raw data: FC xSize ySize XX XX XX ... XX XX XX FD)
```

MD threshold \r

This command is almost identical to FD except that it **M**asks the first frame it **D**ifferences on. Any motion detected on the first frame is masked out, so that areas with high amounts of noise are ignored. Basically, if you call frame differencing and there is always an area of the frame that is moving, then MD will mask out that portion of the image so subsequent calls to FD will ignore that portion of the image. Calling the LF command will clear any masked pixels.

NF threshold \r

This command controls the **N**oise **F**ilter setting. It accepts a value that determines how many consecutive active pixels before the current pixel are required before the pixel should be detected (default 2). The range is between 0 and 255.

Example of how to turn off noise filtering:

```
:NF 0
ACK
:
```

OM packet mask \r

This command sets the **Output Mask** for various packets. The first argument sets the type of packet:

#	Tracking Type	Packet
0	Track Color	T
1	Get Mean	S
2	Frame Difference	T
3	Non-tracked packets*	T
4	Additional Count Information**	T, H
5	Track Color Line Mode 2	T
6	Get Mean Line Modes 1 and 2	S

The *mask* should be a single byte that represents the bitwise mask of the tracking packet. So a value 255 would allow all the parameters to be printed, while a value of 3 would only allow the first two parameters to be printed. Each mask for each packet type is stored separately and remains set until the camera is reset.

*Non-Tracked packets are packets that are printed when the object being tracked is not detected. If this is set to 0, then no packet is printed when the object is not found. If this is set to 1, then just a "T 0" is sent when no object is found. If this is set to 2 (default) then the packet is identical to a tracked packet of that type.

**The additional count information flag lets you get access to the full 16 bit count values for color tracking or histogramming. A value of 0 (default) disables the 16 bit values. A value of 1, adds the 16 bit count of tracked pixels in 2 separate bytes, the first for the LSB and the second for the MSB. A value of 2 will add a 16 bit count of all pixels used to generate a histogram as the first two bytes following the H in the histogram packet. A value of 3, enables both modes simultaneously.

Example of how to only show Mx and My in a T packet:

```
:OM 0 3
ACK
:TC 200 230 0 30 0 30
T 23 45
```

PD boolean \r

This command enables the **Pixel Difference** mode. By default, the mode is off. A value of 1 causes the difference between the current pixel and the previous pixel to be used by all processing commands instead of the original pixel value. This essentially does a horizontal edge-detecting convolution on the image. So the intensity of the remaining lines in each channel is proportional to the sharpness of an edge found in that channel. The best way to understand this command is to try enabling pixel differencing and try sending a frame. Notice what types of lines appear stronger than others. You can then track these edges based on their intensity using track color etc. The difference values are capped at +/- 112 with 128 added to each delta so a value of 128 indicates a 0 difference. This forces the values to remain between 16 and 240. This command applies to all commands.



See TI on page 63 to find out how to use inverse tracking for better edge following.

PF boolean \r

This command enables the **P**acket **F**iltering mode. By default, the mode is off. A value of 1 makes it so that only the first empty packet when a tracked object disappears from the screen is displayed. No packets will be transmitted until the object returns into view. This command can help in situations where empty packets may unnecessarily tax the host processor.

PM mode \r

This command puts the board into **P**oll **M**ode. Setting the mode parameter to 1 engages poll mode while 0 (default) turns it off. When poll mode is set to 0, a continuous stream of packets is returned from a processing function. When poll mode is set to a value of 1, only one packet is returned when an image processing function is called. If mode is set to a value of 2, then poll mode will wait until an object is tracked and then return. This could be useful if you would like to rapidly change parameters or if you have a slow processor that can't keep up with a given frame rate.

Example of how to get one packet at a time:

```
:PM 1
ACK
:TC 50 20 90 130 70 255
ACK
C 38 82 53 128 35 98
:
```

PS number \r

This command controls if **P**ackets should be **S**kipped or not. The default value is 0, which means that all packets will be transmitted. A value of 1 means that every other packet will be skipped. A value of 2 means that only every second packet will be displayed etc. This is useful if you need to slow down the data rate so that your processor can keep up with the data stream when poll mode is enabled.

RF \r

This command **R**eads a new **F**rame into the buffer. This should only be used to get new data when using buffer mode (**BM**). The frame buffer is what allows multiple pass image processing on a single frame. While in buffer mode, you are constantly reprocessing the same frame until read frame is called. Under normal non-buffer mode operation, a new frame is loaded right before a processing function is called.



See BM on
page 30.

RM bit_flags \r

This command is used to engage the **R**aw serial transfer **M**ode. It reads the bit values of the first 3 (lsb) bits to configure settings. All bits cleared sets the default visible ASCII mode. If bit 0 is set, then all output from the camera is in raw byte packets. The format of the data packets will be changed so as not to include spaces or be formatted as readable ASCII text. Instead you will receive a 255 valued byte at the beginning of each packet, the packet identifying character (i.e. C for a color packet) and finally the packet data. There is no \r sent after each packet, so you must use the 255 to synchronize the incoming data. Any 255 valued bytes that may be sent as part of the packet are set to 254 to avoid confusion. If bit 1 is set, the "ACK\r" and "NCK\r" confirmations are not sent. If bit 2 is set, input will be read as raw byte values, too. In this mode, after the two command byte values are sent, send 1 byte telling how many arguments are to follow. (i.e. DF followed by the raw byte value 0 for no arguments) No \r character is required.

bit_flags = B2 B1 B0

B0	Output from the camera is in raw bytes
B1	"ACK\r" and "NCK\r" confirmations are suppressed
B2	Input to the camera is in raw bytes

B0 Output from the camera is in raw bytes

B1 "ACK\r" and "NCK\r" confirmations are suppressed

B2 Input to the camera is in raw bytes

Example of the new packet for Track Color with Raw Mode output only:

```
:RM 1
ACK
:TC 50 100 30 90 0 30
ACK
T>#$$%KFDSAG@#$$
```

RS \r

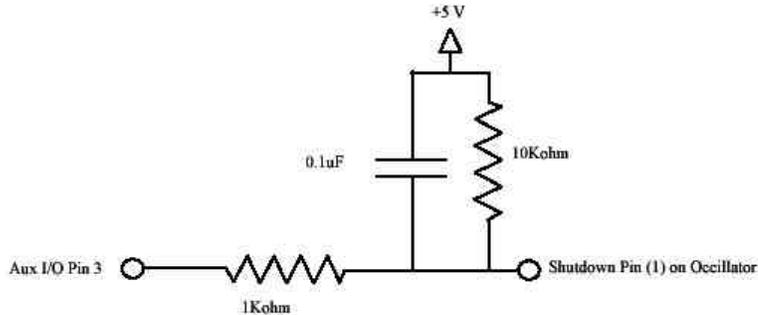
This command **Re**Sets the vision board. Note, on reset the first character is a \r. Also keep in mind that all register values are reset to their default state.

Example of how to reset the camera:

```
:RS
ACK
CMUcam2 v1.0 c6
:
```

SD \r

This command **S**leeps the camera **D**eeply to save power. This command puts the processor to sleep just as the SL command does and additionally uses one of the auxiliary I/O pins to sleep the oscillator. Wakeup from this mode is achieved by sending any character to the module, typically '\r'. The oscillator needs to shut off slightly later than the processor to ensure that that processor powers down correctly. To achieve this delay, it is necessary to add a pullup resistor on the enable line of the oscillator and then have a resistor and capacitor in series with each other before being connected to auxiliary I/O pin 3. You will need to connect 1K series resistor between the oscillator's enable pin and the aux IO pin 3. You then need to connect a 10K resistor in parallel with a 0.1uF capacitor between the enable pin on the oscillator and +5 volts. See diagram below.



See SL on page 60, for a faster, more basic sleep command.

SF [channel] \r

This command will **S**end a **F**rame out the serial port to a computer. This is the only command that will by default only return a non-visible ASCII character packet. It dumps a type F packet that consists of the raw video data row by row with a frame synchronize byte and a column synchronize byte. (This data can be read and displayed by the CMUcam2GUI java application.) To get the correct aspect ratio, double each column of pixels. Since the image is being read from a buffer, the image resolution is not dependent on baud rate. The baud rate just controls how fast the image will be transmitted. Optionally, a channel (0-2) can be added to the command which causes send frame to only send that channel. This will effectively transmit one third of the data.

Type F data packet format

1 - new frame followed by X size and Y size

2- new col

3 - end of frame

RGB (CrYCb) ranges from 16-240

1 xSize ySize 2 r g b r g b ... r g b r g b 2 r g b r g b ... r g b r g b 3



See DS on page 48, to find out how to reduce data sent by send frame. See FS on page 49. to find out how to stream frames.

SL active \r

This command enables **S**leep mode by putting the processor to sleep. Sleep mode can be used when the camera is not needed in order to save power. Sending any character wakes the camera back up after a delay of up to 10ms. It is best to use '\r' to wake the camera up since this will ensure that no unforeseen command gets executed. Sleeping will disable the servo outputs.



For greater power saving see the SD and CP commands on pages 60 and 30.

SM bit_flags \r

This command sets the **Servo Mask** on the CMUcam. The servo mask controls which automatic servo axes are active and which ones should report their values at the end of tracking packets. Pan and Tilt enable / disable turn off the respective automatic servo function while tracking. The servo reporting is added after all of the normal outputs in the Tracking packet, but before the final "\r". Note that automatic control only operates with 'T' packets returned by TC and TW commands.

bit_flags = B3 B2 B1 B0

B0	Pan Control Enable
B1	Tilt Control Enable
B2	Pan Report Enable
B3	Tilt Report Enable

Example of how to enable both pan and tilt automatic servoing and both pan and tilt reporting. In this case, since it doesn't see the object, the servos stay at position 128:

```
:SM 15  
ACK  
:TC  
ACK  
T 0 0 0 0 0 0 0 0 128 128
```

SO servo_number level \r

This command sets a **Servo Output** on the CMUcam to be either a constant high or low value. This essentially converts the servo outputs to be standard TTL digital outputs. The servo number (0-4) selects which servo you want to control, and a level value of either 1 or 0 switches between 5 and 0 volts. If a servo is connected and the output is set to 0, the servo is effectively turned off.



See page 21 for pan tilt direction jumpers. See the SM command on page 61.

SP [pan_range_far pan_range_near pan_step tilt_range_far tilt_range_near tilt_step] \r

This command sets the **Servo Parameters** that are used by the automatic tracking control law. Changing these values can help you tune your tracking for a particular servo setup. The automatic servo control uses a two stage “bang-bang” control law. When the pixel value is greater than the “far” range, the related servo will move by the step amount. When the pixel value is between the near and far range, the servo will move by half of the step amount. Any value smaller than the near value is part of the dead zone and will not trigger any servo motion.

Variable	Description	Default
pan_range_far	Pixel distance needed to do a large pan step	16
pan_range_near	Pixel distance needed to do a small pan step	8
pan_step	Servo position change of a long pan step	5
tilt_range_far	Pixel distance needed to do a large tilt step	30
tilt_range_near	Pixel distance needed to do a small tilt step	15
tilt_step	Servo position change of a long tilt step	5

ST Rmin Rmax Gmin Gmax Bmin Bmax \r

This command allows you to **Set Tracking** parameters without actually calling track color. These values can then be stored until you might call TC with no arguments later.

Example of how to use ST:

```
:ST 200 0 0 250 20 20
ACK
:TC
ACK
T 6 55 2 40 12 60 10 70
T 6 55 2 41 12 61 11 70
```



See SO on page 61 to learn how to disable the servos.

SV servo position \r

This command lets you set the position of one of five **SerVos**. The servos have an active region of between 46 and 210. A value of 128 is the center and generates a 1500 us pulse. The pulse increments by 8.68us and covers a range from 400 us to 1820 us.

Example to set servo 1 to position 200:

```
:SV 1 200
ACK
:
```



See page 57 to see how the OM command can create a custom S Packet.



Using VW on page 64 to decrease the vertical resolution will allow 50 fps tracking.

TC [Rmin Rmax Gmin Gmax Bmin Bmax] \r

This command begins to **Track a Color** . It takes in the minimum and maximum RGB (CrYCb) values and outputs a type T packet. This packet by default returns the middle mass x and y coordinates, the bounding box, the number of pixels tracked, and a confidence value. The packet can be masked using the **OM** output mask function. Remember that the color values from the CMOS camera will range from between 16 and 240. If TC is called with no arguments it will track with the precious set of tracking parameters.

Default Type T packet

T mx my x1 y1 x2 y2 pixels confidence\r

Example of how to Track a Color with the default mode parameters:

```
:TC 130 255 0 0 30 30
ACK
T 50 80 38 82 53 128 35 98
T 52 81 38 82 53 128 35 98
```

TI boolean \r

This command activates **Track Inverted** mode. When track inverted mode is enabled, the camera will track colors that are outside of the user defined color range instead of inside. This is good for either tracking edges, or tracking any object that shows up against a homogenous background.

TW \r

This command will **Track** the color found in the central region of the current **Window**. After the color in the current window is grabbed, the track color function is called with those parameters and on the full image window. This can be useful for locking onto and tracking an object held in front of the camera. Since it actually calls track color, it returns the same type T track packet. Note, the current virtual window setting will only be used for grabbing the color to track and then the window will return to its maximum size.

The following internal steps are performed when “TW” is called:

1. Shrink the window to 1/2 the size (in each dimension) of the current window centered on the current window. (sw 30 54 50 90).
2. Call the get mean command but do not display the output. (gm).
3. Restore the window to the full image size. (sw 1 1 88 143).
4. Set the min and max value for each color channel to be the mean for that channel +/- 30.

Example of how to use Track Window:

```
:TW
ACK
T 6 55 2 40 12 60 10 70
T 6 55 2 41 12 61 11 70
```



See LM on page 53 for instructions on downloading a difference buffer.

UD <64 raw bytes> \r

This command allows you to **U**pload a **D**ifference frame buffer. The command waits for 64 raw byte values that fill up the 8 by 8 internal frame difference buffer. A '\r' cancels the transfer. A value of 0 indicates that the region should be masked and not detect motion. With this command in combination with line mode type 2, it is possible to download and upload different reference frames for frame differencing.



Do not try VW 0 0 88 144, this is outside of the 1 1 88 143 bounds.

VW [x y x2 y2] \r

This command sets the **V**irtual **W**indow size of the camera. It accepts the x and y Cartesian coordinates of the upper left corner (1,1) followed by the lower right of the window you wish to set. The origin is located at the upper left of the field of view. **VW** can be called before an image processing command to constrain the field of view. Without arguments it returns to the default full window size of for the current combination of camera type, down-sampling and resolution mode. Note that reducing the vertical window size can be used to speed up processing time to achieve higher frame rates with the track color command. 50 fps can be achieved with a vertical dimension of 65 or less.

Example of setting the camera to select the mid portion of the view:

```
:VW 35 65 45 75
ACK
```



See GW on page 51 to find out how to check your window configuration.

Data Packet Description

When raw mode is disabled all output data packets are in ASCII viewable format except for the F frame and prefix packets.

ACK

This is the standard acknowledge string that indicates that the command was received and fits a known format.

NCK

This is the failure string that is sent when an error occurred. The only time this should be sent when an error has not occurred is during binary data packets.

Type F data packet format:

1 - new frame 2 - new row 3 - end of frame

RGB (CrYCb) ranges from 16 - 240

RGB (CrYCb) represents two pixels color values. Each pixel shares the red and blue.

176 cols of R G B (Cr Y Cb) packets (forms 352 pixels) 144 rows

To display the correct aspect ratio, double each column so that your final image is 352x144.

1 2 r g b r g b ... r g b r g b 2 r g b r g b ... r g b r g b 3

Type F data packet format:

1 - new frame 2 - new row 3 - end of frame

RGB (CrYCb) ranges from 16 - 240

RGB (CrYCb) represents two pixels color values. Each pixel shares the red and blue.

176 cols of R G B (Cr Y Cb) packets (forms 352 pixels) 144 rows

To display the correct aspect ratio, double each column so that your final image is 352x144

Type H packet:

H bin0 bin1 bin2 bin3 ... bin26 bin27 \r

This is the return packet from calling get histogram (**GH**). Each bin is an 8 bit value that represents the number of pixels that fell within a set range of values on a user selected channel of the image.

Bin0 – number of pixels between 16 and 23

Bin1 – number of pixels between 24 and 31

.

.

.

Bin27 – number of pixels between 232 and 240

1 2 r g b r g b ... r g b r g b 2 r g b r g b ... r g b r g b 3

1 2 r g b r g b ... r g b r g b 2 r g b r g b ... r g b r g b 3



This packet does NOT begin with an "F" and it only contains raw data.

Type T packet:

```
T mx my x1 y1 x2 y2 pixels confidence\r
```

This is the return packet from a color tracking or frame differencing command.

mx - The middle of mass x value

my - The middle of mass y value

x1 - The left most corner's x value

y1 - The left most corner's y value

x2 - The right most corner's x value

y2 - The right most corner's y value

pixels - Number of Pixels in the tracked region, scaled and capped at 255:
(pixels+4)/8

confidence - The (# of pixels / area)*256 of the bounded rectangle and capped at 255.

Type S data packet format:

```
S Rmean Gmean Bmean Rdeviation Gdeviation Bdeviation \r
```

This is a statistic packet that gives information about the camera's view.

Rmean - the mean Red or Cr (approximates r-g) value in the current window.

Gmean - the mean Green or Y (approximates intensity) value found in the current window.

Bmean - the mean Blue or Cb (approximates b-g) found in the current window.

Rdeviation - the *deviation of red or Cr found in the current window.

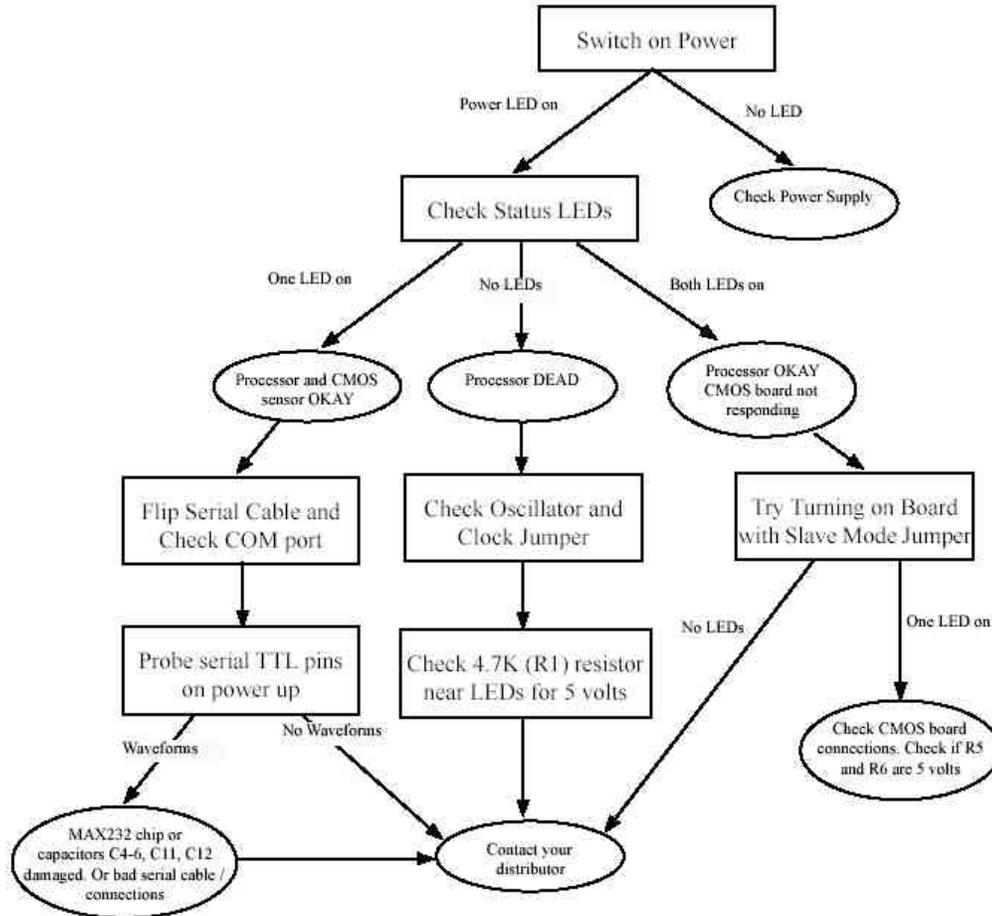
Gdeviation - the *deviation of green or Y found in the current window.

Bdeviation - the *deviation of blue or Cb found in the current window.

*deviation: The mean of the absolute difference between the pixels and the region mean.

Troubleshooting

Diagnostic Fault Tree



General

In Demo Mode, the light turns on for a second and then everything stops:

When both the camera and servo are active, the power required is greater. Try using a battery or voltage source rated at a higher current.

The power LED does not glow:

The board either has a fault, or your power supply is not generating enough power. Check the power supply and look over all of the solder connections. Try unplugging all of the cables except power and turn it on again.

I get garbage output from the camera:

Try turning the camera off and unplugging it for 10 seconds. Then plug it back in and try again. Also, make sure that the baud rate is set correctly.

I get wavy lines or a distorted black and white image when I call dumpframe:

This is most likely due to power. Make sure that you have a high enough voltage and that you are getting a clean signal. Running the camera off of fresh batteries (not an AC adaptor) is a good way to test if this is the problem.



See page 58 for poll mode and page 48 for delay mode.

My processor can not keep up with the serial data stream:

Try running the camera in poll mode and setting a delay mode value.

I don't seem to get any serial data:

Make sure that the serial cable is connected on the CMUcam side correctly. If in doubt, try reversing it.

Why does VW keep giving me a NCK?

Make sure you are within the VW 1 1 88 143 bounds.

I see the CMUcam startup message, but then nothing happens:

Check to make sure the transmit line on your serial cable is connected correctly.

CMUcam2 GUI

When I run java I get: Exception in thread "main"

java.lang.NoClassDefFoundError

Chances are you are not in the CMUcam2GUI directory. Type "dir" at the command line prompt and make sure that you see the CMUcam2GUI.class file. Also check to make sure an old version of Quicktime did not set your CLASSPATH variable (there should be no CLASSPATH variable in new versions of java).

I see CMUcam2GUI.java but I don't see the CMUcam2GUI.class file:

You should download a new copy of the GUI, because the .class files should be included. If you really need to recompile them, type "javac *.java" .

I get: 'java' is not recognized as an internal or external command, operable program or batch file:

This means that java is not correctly installed in your path. Try re-installing java and reading Sun's installation documentation.



3rd Party Software Information

Java

The CMUcam2 GUI requires java's JRE version 1.4.0 or newer. The latest version of java can be downloaded for free at <http://java.sun.com>. JRE stands for java runtime environment and contains all that is needed to run a java program. JDK stands for java development kit and does everything that the JRE does, plus it allows you to compile java programs into byte code. Since the CMUcam2 GUI is given to you already compiled, you should be able to run it on any type of computer without having to recompile.



The CMUcam2 GUI should not need to be recompiled!

Terminal Emulation Programs

The following are free terminal emulation programs that can be found as shareware on the Internet:

Windows:

HyperTerm - built into windows, but tends to be confusing

TeraTerm - Fast and easy to use

Macintosh OS 7,8,9:

Zterm - fast, free, easy to use

Mac OS X

Port Term

Unix / Linux:

Minicom - standard easy to use com program

- alt-a adds line feeds to \r

- alt-e turns on local echo

- alt-s lets you configure the serial port

Disclaimer

No warranties, either expressed or implied, are made regarding the operation, use or results of this hardware. This product is meant for educational purposes only. Any resemblance to real persons, living or dead is purely coincidental. CMUcam2 void where prohibited with some assembly required. Batteries and servos not included. Contents may settle during shipment so only use as directed. No other warranty expressed or implied. Do not use while operating a motor vehicle or heavy equipment. Apply CMUcam2 only to affected area. If condition persists, consult your physician. May be too intense for some viewers and for recreational use only. Do not disturb CMUcam2 during boot process. All models over eighteen years of age. No user-serviceable parts inside. Freshest if used before date on carton. Subject to change without notice. Many CMUcam2 times approximate and many pictures simulated. Breaking seal constitutes acceptance of agreement. This product is known to the state of California to cause birth defects. As seen on TV one size fits all. My man the yellow darts comments not actually generated by the yellow dart. Contains a substantial amount of non-tobacco ingredients. Colors may, in time, fade. Slippery when wet. If CMUcam2 acts up; keep cool; process promptly. Not responsible for direct, indirect, incidental or consequential damages resulting from any defect, error or failure to perform. Substantial penalty for early withdrawal. Keep away from fire or flame. Replace with same type. Some of the trademarks mentioned in this product appear for identification purposes only. No animals were hurt in the production of this device. This supersedes all previous notices.