

H8/300 Series Application Engineering Documents

Number	Date	Title
AE-0020	3/1/91	Parallel-to-Parallel Print Buffer Controller
AE-0021	3/1/91	H8/330 Power-Down Operation
AE-0023	5/1/91	H8/300 16x16 Multiply
AE-0033	10/15/93	Clock Synchronized Serial Communication with the H8/300 SCI
AE-0034	12/15/93	Asynchronous Serial Communication with the H8/300 SCI
AE-0037	1/6/93	Using the H8/300 to Generate a Time-of-Day Clock
AE-0039	1/25/93	A/D Conversion using the H8/330
AE-0040	1/28/93	D/A Conversion using the H8/338
AE-0043	4/14/93	External Memory Interface to the H8/3003
AE-0046	5/17/93	Interfacing Serial EEPROM to the H8/300
AE-0047	7/8/93	GNU Command Line Switches
AE-0048	9/7/93	Software MPB Serial Format
AE-0049	8/23/93	GNU Program Optimizations
AE-0051	12/8/93	E3000 Features Tutorial
AE-0052	12/23/93	H8/510 PWM Waveform Generation
AE-0053	2/10/94	H8/300 Software UART Implementation
AE-0054	2/10/94	H8/3003 Stepper Motor Control
AE-0057	4/8/94	Using the H8/300 to control a stepper motor
AE-0059	8/15/94	H8/300 Hitachi Switches
AE-0170	1/3/94	Microcontroller Crystal Oscillator Tutorial
AE-0508	8/11/94	Embedded Software Development
TN-0010	4/1/91	H8/300 CPU Divide Instruction
TN-0011	4/1/91	H8/300 CPU SUBX Instruction
TN-0020	10/1/91	Direct Memory Addressing Using C Pointers
TN-0055	8/19/92	Using the H8/3xx Series to generate various frequency

Number	Date	Title
TN-0057	8/21/92	Using the H8/3xx Series to generate uneven duty cycle
TN-0061	10/15/92	H8/300 memory map assignment using the E3000
TN-0062	10/15/92	A/D Converter Specifications
TN-0065	10/30/92	Power Consumption vs. CPU Clock for the H8/300 Family
TN-0072	11/17/92	Mode Pins During Standby Operation
TN-0090	3/8/93	HAL/GNU On-Line Manual
TN-0091	3/9/93	Running the Demo Program Using HAL/GNU Debugger
TN-0092	3/10/93	Compiling the HAL/GNU Demo Program
TN-0093	3/11/93	HAL/GNU Linker Command File
TN-0094	3/12/93	HAL/GNU: Generating Linker Map File
TN-0095	3/15/93	Generate Intermixed Assembly Listing with Symbol Table
TN-0096	3/16/93	HAL/GNU Code Optimization
TN-0097	3/17/93	HAL/GNU Output Format
TN-0098	3/18/93	HAL/GNU: Using Register in Inline Assembly
TN-0099	3/19/93	HAL/GNU: Storing Variable Value into Register
TN-0100	3/22/93	HAL/GNU: Storing Variable Address into Register
TN-0101	3/23/93	HAL/GNU: Placing Section in ROM/RAM
TN-0107	4/10/93	Servicing Interrupts in the H8/300 Family
TN-0117	3/31/93	Watchdog Timer Initialization
TN-0118	4/2/93	Using the H8/325 Demo Board to Evaluate the H8/329
TN-0119	5/20/93	Interfacing PSRAM Devices to the H8/3003
TN-0125	5/12/93	HAL/GNU: Program Entry Point
TN-0126	5/14/93	Using Two 8-bit Timers to Create a 16-bit Timer
TN-0131	7/1/93	DRAM Types and timing considerations for the H8/3003

Number	Date	Title
TN-0134	6/8/93	H8/338 EVB vs. H8/330 HVB
TN-0135	5/28/93	H8/338 EVB vs. H8/330 EVB
TN-0138	7/15/93	DMA Request and Transfer Time
TN-0146	6/30/93	“Hidden” RAM-Part 2
TN-0150	7/14/93	Running S-Records Using GDB
TN-0154	7/30/93	Interfacing various bit-size DRAMs to the H8/300H
TN-0155	9/7/93	Working with Multi-processor Serial Control
TN-0156	7/29/93	Bit Clear Using C and Inline Assembly
TN-0159	8/17/93	H8/325 Floating Pinout
TN-0161	8/16/93	A/D conversion accuracy in low voltage
TN-0163	8/16/93	Run GDB Commands From File
TN-0167	8/31/93	Compiling Functions for Interrupt Calls
TN-0168	9/7/93	Handling Multiple Interrupts from a Single Source
TN-0169	9/14/93	Interfacing the H8/325 Series to External Memory
TN-0170	9/15/93	Implementing a Software Interrupt on the H8/320 Series
TN-0171	9/24/93	Multiplexed I/O Functions on the H8/300H
TN-0182	11/15/93	Free-running timer output on match condition
TN-0185	11/15/93	Cast to a Union Type
TN-0190	12/1/93	GNU Compiler Option File
TN-0191	12/3/93	Design Tips for I/O Port Pins
TN-0193	12/15/93	CMOS Design Tips for the H8/53x Devices
TN-0196	11/10/93	Enabling DMA Interrupts in the H8/300H
TN-0199	12/15/93	GNU: Declaring Section Names Explicitly
TN-0202	1/4/94	Implementing software delay loops on the H8/300
TN-0203	1/6/94	32/16 Divide for the H8/300 Family

Number	Date	Title
TN-0204	1/12/94	A guide to sockets for the H8/300 family package types
TN-0206	1/14/94	D/A Output Impedence
TN-0207	1/14/94	Absolute Accuracy in H8/510
TN-0209	1/21/94	Memory Bank Switching Considerations for the H8/3XX
TN-0216	2/1/94	HAL/GNU:Reserving Memory Using the GNU Linker
TN-0223	2/17/94	Relationship between Icc and Temperature
TN-0224	2/23/94	H8/300H Instruction Timing
TN-0227	3/11/94	GNU Source Level Debugging
TN-0230	3/25/94	Serial Port Expansion for the H8/300 Family
TN-0231	3/25/94	2,3, or 4 Wire SCI. Which one to use?
TN-0243	9/8/94	Rise/Fall Time Requirements for Interrupt Signals IRQn
TN-0247	11/22/94	Software standby mode recovery time for the H8/300H