

**CYPRESS SUPPORTS SYNOPSYS LOGIC MODELING® SmartModels®
FOR ALL SPECIALTY MEMORIES**

Models Speed Design Time; Supported on Popular Simulation Platforms

SAN JOSE, Calif., October 21, 1999—Cypress Semiconductor Corporation (NYSE:CY) today announced that it has engaged in a partnership with Synopsys to support Logic Modeling® SmartModels® for all of its specialty memory products. SmartModels provide accurate timing and functionality models that allow designers to simulate a device's performance in a system, including its interaction with other system components, such as processors and ASICs, before placing a device on the circuit board. This saves design time and allows early error checking to avoid costly schedule delays.

All of Cypress's first-in, first-out (FIFO) and dual-port memories that are currently in production are now supported by SmartModels, including Deep Sync™ and x36 Sync FIFOs, and 1-Meg and FLEEx36™ dual-ports. Since these devices are often used in high-performance networking, mass storage, and telecommunications systems, SmartModel support aids designers in achieving maximum performance in their systems.

Cypress has recently announced its strategy to optimize its new specialty memories for bandwidth, rather than density. As demands on memory increase with the continued explosion of the Internet, Cypress will address the demands for higher bandwidth devices through a combination of more ports, higher clock speeds, and wider word widths.

"Offering SmartModel support is another positive signal of our commitment to grow our specialty memory business," said Geoff Charubin, director of marketing for Cypress's DataCom product line. "These models will help our customers to more efficiently design with specialty memories, decreasing their time-to-market by allowing them to simulate designs prior to layout."

SmartModels are the industry standard behavioral simulation models. In addition to representing device behavior, they provide automatic error checks, advanced ease-of-use features, and easy integration with other design tools. Most major simulators and workstations support SmartModels.

Cypress Specialty Memory Focus

Cypress has moved aggressively to capture a leadership position in the specialty memory market, introducing over 130 new FIFO and dual-port memories since the third quarter of 1996. Cypress first debuted the Deep Sync FIFO family, the only high-density synchronous FIFOs with industry-standard pinouts and architectures. Cypress introduced the first 1-Mbit FIFO in 1997, and the first complete family of 3.3-V synchronous FIFOs early in 1998. In August 1998, Cypress also assumed leadership in the dual-port market with the world's largest (1 Mbit) and fastest (100 MHz) devices. Cypress debuted the 7.2 Gbps bandwidth FLEx36 dual-ports in March 1999, and 9.6 Gbps FIFOs earlier this month.

Price and Availability

SmartModel licenses are available through Synopsys. The SmartModels for Cypress's specialty memories are at <http://www.synopsys.com/products/lm/modelDir.html>. In addition to SmartModels, Cypress provides IBIS (Input Output Buffer Specification) models to support signal integrity simulation.

About Cypress

Cypress Semiconductor provides high-performance integrated circuit solutions "By Engineers. For Engineers™." for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial-control. With a focus on emerging communications applications, Cypress's product lines include networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets.

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More than two-thirds of Cypress's sales come from fast-growing datacom/telecom markets and dynamic companies such as Lucent, Cisco, 3Com, Alcatel, Motorola, Ericsson, and Northern Telecom. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 3,500 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810). An electronic investor forum, and other investor information, is located at <http://www.cypress.com/investor/index.html>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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