

## **CYPRESS ADDS PROTOTYPING BOARD TO \$175 CPLD DEVELOPMENT KIT**

### **Provides Complete Design Solution for Ultra37000™ CPLD Family**

SAN JOSE, Calif., February 22, 1999 -- Cypress Semiconductor Corp. [NYSE:CY] today announced that it has added a prototyping circuit board to its popular *Warp2ISR™* In-System Reprogramming™ (ISR™) kit for the Ultra37000™ Complex Programmable Logic Device (CPLD) family. In addition to the board, the kit includes the popular *Warp2®* design tools, the latest release of ISR software, a cable to easily program devices from a personal computer, complete Jam programming and testing support, and the popular *VHDL for Programmable Logic* textbook. Cypress continues to offer the kit for only \$175.

The *Warp2ISR* kit gives designers the ability to verify their designs directly on the prototype board, offering increased flexibility and faster time to market. The prototype board comes with a 160-pin, 256-macrocell Ultra37000 device already pre-mounted and provides a second 208-pin socket that offers users the flexibility to try out their designs on other Ultra37000 devices.

“The *Warp2ISR* Kit provides a complete design solution for CPLDs, from design entry to simulation and prototyping,” said Norman Taffe, director of marketing for Cypress’s Programmable Logic Division. “At \$175, it continues Cypress’s tradition of offering low-cost, high-quality CPLD solutions.”

### **Ultra37000 CPLDs**

While other CPLDs offer in-system programmability, customers often find that changes in logic cannot fit into the original pinout -- rendering design changes useless without a new board layout. Cypress offers true In-System Reprogrammability. The unique Cypress architecture provides unmatched routability, allowing customers to implement changes with confidence that existing pinouts will be maintained. Cypress also offers a fixed, simple timing model that allows designers to make logic changes without affecting critical system timing parameters.

“The Ultra37000 family is the premier solution for In-System reprogramming,” continued Taffe. “The addition of the prototyping board allows our customers to make better use of this capability.”

**Price and Availability**

Verilog and VHDL versions of the *Warp2ISR* kit are available immediately priced at \$175. Japanese versions of the *Warp2ISR* kit are also available. Customers can buy the kits using Visa or MasterCard by calling Cypress at 1-800-858-1810 (U.S. only), or via Cypress’s web site at [www.cypress.com](http://www.cypress.com). For users who already own *Warp* software, the Ultra37000 ISR kit is also sold separately for only \$99.

Cypress Semiconductor Corporation, headquartered in San Jose, California, provides a broad range of integrated circuits for leading computer, networking, and telecommunications companies worldwide. Cypress's products include static RAM and specialty memories, programmable logic devices (PLDs), data communications products, timing devices, and USB microcontrollers. Its shares are listed on the New York Stock Exchange under the symbol CY, and its web site is <http://www.cypress.com>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress’s Securities and Exchange Commission filings for a discussion of such risks.

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Ultra37000, *Warp2ISR*, *Warp* and ISR are trademarks and *Warp2* is a registered trademark of Cypress Semiconductor Corp.