Press Release

CYPRESS DEBUTS 100 MHz 3.3-V FIFOs

Cypress Now Offers its Deep Sync™ FIFOs from 128 Kbit to 1 Mbit at 100 MHz

SAN JOSE, Calif., August 12, 1999—Cypress Semiconductor Corporation (NYSE:CY) today introduced ten 3.3-V Deep Sync™ FIFO (First-In/First-Out) memories at 100 MHz. Cypress now offers both 3.3-V and 5-V Deep Sync™ FIFOs at densities from 128 Kbit to 1 Mbit in 100 MHz speeds. Cypress is also the only supplier to provide common pinouts for both high and low density devices so that customers can easily upgrade densities to 1Mbit without having to redesign their circuit boards.

Over 130 New Specialty Memories Since Q396

Cypress has moved aggressively to capture a leadership position in the specialty memory market, introducing over 130 new FIFO and dual-port memories since the third quarter of 1996. Cypress first debuted the Deep Sync FIFO family, the only high-density synchronous FIFOs with industry-standard pinouts and architectures, allowing designers to upgrade existing synchronous FIFO to densities over 256 Kbits without requiring board redesign. Cypress introduced the first 1 Mbit FIFO in October of 1997, and became the first supplier of a complete family of 3.3V synchronous FIFOs early in 1998. In August 1998, Cypress also assumed leadership in the dual-port market with the world's largest (1 Mbit) and fastest (100 MHz) devices.

"When we launched the first industry-standard, high-density FIFOs and the first 1 Mbit FIFO, we made a commitment to aggressively grow our specialty memory position," said Geoff Charubin, director of marketing for Cypress's DataCom product line. "We are following through on this commitment, and will soon announce more groundbreaking products in the specialty memory area."

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FIFOs provide convenient, high-speed data buffering solutions to accommodate the transfer of information between system components such as ASICs, CPLDs, FPGAs, processors, physical layer devices, and busses that operate at different frequencies. They are commonly used in networking equipment, high-end video and graphics systems, mass storage, and telecommunication systems.

The need for 3.3V FIFOs arises as processors and memories have migrated to lower voltages. Designers are increasingly eager to replace 5V power supplies in their systems with lower voltage supplies in order to reduce power consumption, remove extra 3.3V/5V translational logic, and consolidate power planes. FIFOs, commonly used as data buffering and bus matching devices in LAN, WAN, and video systems, have been some of the last devices available in lower voltages. Cypress's new offerings directly address this need.

Price and Availability

The ten new 100 MHz, 3.3-V Deep Sync™ FIFOs are available today with pricing starting at $27.50 in 10,000-unit quantities.

Cypress Semiconductor Corporation, headquartered in San Jose, California, provides a broad range of integrated circuits for leading computer, networking, and telecommunications companies worldwide. Cypress's products include static RAM and specialty memories, programmable logic devices (PLDs), data communications products, timing devices, and USB microcontrollers. Its shares are listed on the New York Stock Exchange under the symbol CY, and its web site is http://www.cypress.com.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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