

CYPRESS FIRST TO RECEIVE VALIDATION FOR DIRECT RAMBUS™ CLOCK GENERATOR

San Jose, Calif., July 28, 1999 – Cypress Semiconductor (NYSE:CY) today announced that it is the first company in the timing technology industry to receive validation for its Direct Rambus™ Clock Generator (DRCG). The W134 is a high performance Frequency Timing Generator (FTG) that provides the differential clock signals for Direct Rambus memory subsystems.

The W134 generates clock source in the range of 267 to 400 MHz and supports Direct Rambus memory system with up to 1.6 GB per second data transfer rate. The W134 is offered in two versions– the W134M and W134S. The only difference between the two is their frequency multiplier tables. With two versions, design engineers can reach different system price/performance targets by varying memory speeds.

“Cypress has worked early on and closely with Rambus and Intel, and we are gratified to be the first company to receive validation for our DRCG from Rambus,” said Ian Chen, marketing director for Cypress's Timing Technology Division. “When data rates are at 800 MHz with 1.25 ns cycle times, it is very critical for DRCG suppliers to comprehend the architectural specification, the interactions with accompanying components such as the power supply, and the limitations of common system implementations. The W134 is the distillation of our experiences in working with every early adopter of Direct Rambus over the last 12 months.”

Press Release

Cypress First to Receive Validation for Direct Rambus Clock – Page 2

“The DRCG is a critical component necessary to realize the implementation of Direct Rambus memory systems,” said Laura Fleming, Director of Alliances and Infrastructure, Computer and Memory Group, Rambus Inc. “Cypress was the first company to deliver working DRCG samples, and is now the first to pass all the validation tests. Rambus is pleased with the support and commitment of Cypress in the development of DRCG products.”

Price and Availability

The W134M and W134S samples are available now in 24-pin 150-mil Shrink Small Outline Packages (SSOP) and are pin-to-pin compatible with each other. Production quantities are expected to be available in September 1999. Pricing starts at \$2.75 in 10,000-unit quantities.

Cypress Semiconductor Corporation, headquartered in San Jose, California, provides a broad range of integrated circuits for leading computer, networking, and telecommunications companies worldwide. Cypress's products include static RAM and specialty memories, programmable logic devices (PLDs), data communications products, timing devices, and USB microcontrollers. Its shares are listed on the New York Stock Exchange under the symbol CY, and its website is <http://www.cypress.com>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

###

Note to editors: Rambus is a registered trademarks of Rambus Inc. Direct Rambus is a trademark of Rambus Inc.