CYPRESS INTRODUCES INDUSTRIAL TEMPERATURE GRADE ZERO DELAY BUFFERS

Aimed at Applications such as Cable Modems and Base Stations

SAN JOSE, Calif., August 27, 1999 – Cypress Semiconductor Corp. (NYSE:CY) today introduced a family of industrial temperature grade zero delay buffers. Zero delay buffers replicate one timing signal into multiple copies with almost no delay and with low skew between the outputs. Consequently, they are an important ingredient for large, synchronous systems that are used to enable Internet access. Base stations and cable modems are examples of systems that require the reliable distribution of high-speed timing signals and are subject to extreme temperatures. The new devices operate at temperatures from -40 to +80 degrees Centigrade.

"These new products complement many of Cypress's offerings for the data communication and telecommunication markets," said Ian Chen, marketing director of Cypress's Timing Technology Division. "Cypress is responding to strong demand for industrial temperature grade timing products from customers designing communications equipment. In only one week of taking orders, we have already booked over 30 thousand units."

By adding industrial temperature devices to its zero delay buffer family, Cypress expands what was already the industry's widest selection. Cypress's original zero delay buffers set the industry standard, offering support for complex systems, such as switching equipment and servers, that require a great deal of synchronous activity. The recently introduced Spread Aware™ product line enables designers of peripherals and consumer products to easily adopt spread spectrum timing technology (as found in Cypress's PREMIS™ product line) for reducing EMI emissions.

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Cypress's industrial temperature zero delay buffers are listed below:

Part Number	Feedback	Bank A	Bank B	Other Features	Package
CY2308I-1	External	x1	x1	133 MHz	16-pin SOIC
CY2308I-1H	External	x1	x1	133 MHz, High-drive	16-pin SOIC, TSSOP
CY2308I-2	External	x1or x2	x1/2 or x1	133 MHz	16-pin SOIC
CY2308I-3	External	x2 or x4	x1 or x2	133 MHz	16-pin SOIC
CY2308I-4	External	x2	x2	133 MHz	16-pin SOIC
CY2308I-5H	External	x2	x2	133 MHz	16-pin SOIC
CY2305I-1	Internal	x1		Adjustable I/O delay	8-pin SOIC
CY2309I-1	Internal	x1	x1	133 MHz, High-drive	16-pin SOIC
CY2305I-1H, 5H	Internal	x1		133 MHz, High-drive	16-pin SOIC
CY2309I-1H	Internal	x1	x1	133 MHz, High-drive	16-pin SOIC

Price and Availability

The zero delay buffers are available now, offered in 8- and 16-pin SOIC and 16-pin TSSOP packages, the industry's smallest packages for zero delay buffers. The CY2308I (six options) is priced at $8.25 in 1000-unit quantities.

Cypress Semiconductor Corporation, headquartered in San Jose, California, provides a broad range of integrated circuits for leading computer, networking, and telecommunications companies worldwide. Cypress's products include static RAM and specialty memories, programmable logic devices (PLDs), data communications products, timing devices, and USB microcontrollers. Its shares are listed on the New York Stock Exchange under the symbol CY, and its website is http://www.cypress.com.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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