

## **CYPRESS REVS UP DUAL-PORT RAM OFFERING**

### **Fills Out Industry's First Complete Line of 3.3-V, 100-MHz Devices**

SAN JOSE, Calif., September 27, 1999 -- Cypress Semiconductor Corp. (NYSE:CY) today introduced 100-MHz versions of its 8-, 9-, 16-, and 18-bit wide dual-port memories. The product introduction gives Cypress the industry's first complete line of low-voltage 100-MHz devices from 8-bit to 36-bit word widths and up to 1-Mbit in density.

Cypress's 3.3-V dual-ports are offered in identical packages, pinout, and performance as corresponding 5-V devices, easing the transition to 3.3-V. The new products come in a variety of configurations, including both pipelined and burst modes.

"We continue to extend our technology leadership in the dual-port arena," said Geoff Charubin, director of marketing for Cypress's DataCom product line. "By addressing the growing needs of communications customers for higher bandwidth solutions, we aim to continue our market share growth as well."

Dual-port RAMs allow the same piece of data to be shared by multiple processors and/or busses in separate, asynchronous clock domains. Two ports provide independent access for reads and writes to any location in memory. They are used in performance-driven equipment such as RAID switches, base stations, telecom switches, and routers.

The new dual-ports are part of an aggressive specialty memory push by Cypress. In 1996, Cypress introduced the Deep Sync™ FIFO family, the first high density FIFOs with industry-standard pinouts. In 1997, Cypress debuted the first 1-Mbit FIFO, and followed that with a family of synchronous 3.3-V FIFOs in 1998 and a line of x36 FIFOs last month. In August 1998, Cypress rolled out over 60 new dual-ports, including the first at 1 Mbit. More recently, Cypress introduced the FLEx36™ family of x36 dual-port SRAMs, offering a bandwidth up to 7.2 Gbps, giving it the industry's broadest line of dual-port SRAMs.

**Price and Availability**

The new 3.3-V, 100 MHz 1-Mbit dual-ports are sampling now, with production quantities available in October. In 10,000-unit quantities, the 1-Mbit and 512-Kbit devices are priced starting at \$83 and \$58 respectively.

Cypress Semiconductor Corporation, with international headquarters in San Jose, California, provides a broad range of products for leading data communications, telecommunications, computation, consumer, and industrial-control companies worldwide. Cypress's product line includes data communications products; static RAM and specialty memories; programmable logic devices (PLDs); timing devices (clock chips), and microcontrollers for Universal Serial Bus (USB), the new, plug-and-play interface standard connecting PCs and peripherals.

The company is No. 1 in the USB and clock chip markets. More than two-thirds of Cypress's sales are into fast-growing datacom/telecom markets and dynamic companies such as *Lucent, Cisco, 3Com, Alcatel, Motorola, Ericsson, and Northern Telecom.*

Cypress's shares are listed on the New York Stock Exchange under the symbol CY. The company's worldwide web site is <http://www.cypress.com>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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