

CYPRESS ANNOUNCES STAPL SUPPORT FOR ITS CPLDs

Programming and Test Language Addresses Need for Multivendor Solution

SAN JOSE, Calif., June 2, 1999 -- Cypress Semiconductor Corp. (NYSE:CY) today announced that it will provide programming support via the new JEDEC Standard Test and Programming Language (STAPL) for programmable logic. STAPL is based on the Jam programming language. Cypress, an original supporter of Jam, will provide STAPL programming for both the Ultra37000™ and Delta39K™ CPLD families.

“STAPL provides a single, flexible platform for programming designs into silicon from multiple vendors,” said Christopher Norris, vice president of Cypress’s Programmable Logic Division. “As an original supporter of Jam, we are pleased that STAPL has achieved industry-standard status, and we will have a relatively easy task in providing STAPL support.”

STAPL is a vendor- and platform-independent language for programming devices via the IEEE standard 1149.1 TAP controller, commonly known as JTAG. It allows the specification of both the programming data and programming algorithm in a single file. Once created, the file contains all the information required to program a specific design into a targeted device.

There are two software components in the STAPL programming solution: the “composer” and the “player.” The composer writes the file required to program a specific design into a specific device. The player interprets the file and programs the target device. These elements create a universal language and toolset that addresses all PLDs and all programming methodologies. A STAPL composer and player for Cypress’s Ultra37000 CPLD family will be available on the Cypress web site (www.cypress.com) in the third quarter. The Delta39K support will be available by year-end.

Cypress Semiconductor Corporation, headquartered in San Jose, California, provides a broad range of integrated circuits for leading computer, networking, and telecommunications companies worldwide. Cypress's products include static RAM and specialty memories, programmable logic devices (PLDs), data communications products, timing devices, and USB microcontrollers. Its shares are listed on the New York Stock Exchange under the symbol CY, and its web site is <http://www.cypress.com>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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