**CYPRESS ANNOUNCES TAXIchip™-COMPATIBLE HOTLink™ TRANSCEIVER**

**Integrates Transmitter, Receiver, and FIFO to Save Board Space and Power**

SAN JOSE, Calif., June 7, 1999 -- Cypress Semiconductor Corporation (NYSE:CY) today announced a highly integrated point-to-point serial transceiver that is compatible with Advanced Micro Devices' (AMD) TAXIchip™ chipset. AMD recently announced that it will discontinue the TAXIchip line, but Cypress's new CY7C9689 HOTLink™ transceiver will allow current TAXIchip customers to enjoy a continuous, reliable source of supply, and to maintain compatibility with prior-generation products.

**TAXIchip-Compatible HOTLink Features**

Cypress has integrated the transmitter and receiver into a single-chip solution as opposed to AMD's two-chip solution. In addition, the TAXIchip-compatible HOTLink includes 256 bytes of FIFO memory on both the transmit and receive channels. Because of this high level of integration, a single CY7C9689 HOTLink can replace up to four components in a design based on the TAXIchip, saving considerable board space and approximately one third the power consumption of a TAXIchip solution.

The new CY7C9689 HOTLink supports data rates between 50-200 Mbps (5-20 Mbytes/sec.) and transmits over standard media including fiber optics, twister pair wire, and coaxial cable. It supports 4B/5B and 5B/6B, TAXIchip, and Fiber Distributed Data Interface (FDDI) encoding standards. It incorporates a completely integrated phase-locked loop (PLL) clock synchronizer, which eliminates the need for external capacitors and resistors. It also offers a built-in self-test to allow loop back and link integrity diagnostics.

"Users of the TAXIchip will be pleased to note that we have entered this segment of the market to guarantee continued supply by a top-line vendor," said Michael Bollesen, strategic marketing manager for Cypress's Datacom Division. "And they will receive the bonus of the CY7C9689 HOTLink's superior integration, which will provide significant board space, power, and cost savings."

**HOTLink Technology**

Cypress has been successful in the point-to-point market since the introduction of its HOTLink (**H**igh-speed **O**ptical **T**ransceiver **Link**) chipset in 1993. HOTLink technology is recognized as a leading solution for high-speed serial applications such as disk-to-peripheral, server-to-server, generic serial bus/backplane, and video-imaging communications. HOTLink uses an 8B/10B encoding scheme and runs up to 400 Mbps over fiber, coax, and twisted pair media.

**Price and Availability**

The CY7C9689 HOTLink is offered in 100-pin TQFP packages. It is expected to sample in September, with full production following shortly thereafter. In 10,000-unit quantities, it is priced at $27. Preliminary data sheets are available on Cypress's worldwide web site at www.cypress.com.

Cypress Semiconductor Corporation, headquartered in San Jose, California, provides a broad range of integrated circuits for leading computer, networking, and telecommunications companies worldwide. Cypress's products include static RAM and specialty memories, programmable logic devices (PLDs), data communications products, timing devices, and USB microcontrollers. Its shares are listed on the New York Stock Exchange under the symbol CY, and its web site is http://www.cypress.com.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

# # #

HOTLink is a trademark of Cypress Semiconductor Corp. TAXIchip is a trademark of Advanced Micro Devices.