

**NEW CYPRESS 1 MBIT BI-DIRECTIONAL AND TRI-BUS FIFOS ARE WORLD'S LARGEST**

**Complete 3.3-V and 5-V Line of x36 Devices Rounds Out Offering**

SAN JOSE, Calif., February 22, 1999—Cypress Semiconductor Corporation (NYSE:CY) today continued its aggressive specialty memory push with the introduction of a new family of 36-bit wide, synchronous, First-In/First-Out (FIFO) memories in both 3.3 V and 5 V. The new, 32-member family features four device configurations in unidirectional, bidirectional, and “tri-bus” operating modes. The 1 Mbit devices represent the world’s largest bi-directional and tri-bus FIFOs -- 16 times larger than competing offerings. The new FIFOs also deliver performance up to 100 MHz.

Cypress’s new x36 Deep Sync™ FIFOs can interface directly with 32- or 36-bit-wide processors and buses, eliminating the need for multiple x9 or x18 devices. They also offer the density, speed, and low power consumption needed to keep up with new performance targets in today’s networking, telecommunications, mass storage, and video systems.

“This introduction demonstrates Cypress’s continuing commitment to aggressively grow our specialty memory position,” said Geoff Charubin, director of marketing for Cypress’s DataCom product line. “Our x36 FIFOs address the need for high density and low voltage while maintaining industry-standard packages and pinouts.”

Cypress’s unidirectional and bi-directional versions of the new x36 FIFOs offer a “bus matching” feature, which allows configuration of one x36 width port to either a x9 or x18 width, providing designers with greater flexibility in interfacing with different processors. In the “tri-bus” configuration, the FIFOs contain three ports: a x36 bidirectional port, a unidirectional x18/x9 input, and a unidirectional x18/x9 output. The tri-bus devices can seamlessly interface to a x36 processor while simultaneously reading and writing data from the unidirectional ports.

**Over 130 New Specialty Memories Since Q396**

Cypress has moved aggressively to capture a leadership position in the specialty memory market, introducing over 130 new FIFO and dual-port memories since the third quarter of 1996. Cypress first debuted the Deep Sync FIFO family, the only high-density synchronous FIFOs with industry-standard pinouts and architectures. Deep Sync FIFOs are the only devices that allow designers to upgrade existing synchronous FIFO to densities over 256 Kbits without requiring board redesign. Cypress introduced the first 1 Mbit FIFO in October of 1997, and became the first supplier of a complete family of 3.3V synchronous FIFOs early in 1998. In August 1998, Cypress assumed leadership in the dual-port market with the world's largest (1 Mbit) and fastest (100 MHz) devices.

**Price and Availability**

The new x36 Deep Sync FIFOs range from 8 Kbit up to 1 Mbit densities. The 3.3-V devices are available today and the 5-V parts are expected to be in production in April. Pricing in 10,000-piece lots starts as low as \$20.00. Customers can call Cypress at (800) 858-1810 in the U.S., or (408) 943-2600, or visit [www.cypress.com](http://www.cypress.com).

Cypress Semiconductor Corporation, headquartered in San Jose, California, provides a broad range of integrated circuits for leading computer, networking, and telecommunications companies worldwide. Cypress's products include static RAM and specialty memories, programmable logic devices (PLDs), data communications products, timing devices, and USB microcontrollers. Its shares are listed on the New York Stock Exchange under the symbol CY, and its web site is <http://www.cypress.com>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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